



INPUT CURRENT LIMIT SETTINGS

D0	D1	D2	CURRENT LIMIT	CHARGER STATUS
0	0	0	100mA (1X)	ON
0	0	1	100mA (1X)	OFF
0	1	0	500mA (5X)	ON
0	1	1	500mA (5X)	OFF
1	0	0	1A (10X)	ON
1	0	1	1A (10X)	OFF
1	1	0	500uA (SUSP)	OFF
1	1	1	2.5mA (SUSP)	OFF

NOTE: UNLESS OTHERWISE NOTED,
ALL CAPACITORS AND RESISTORS ARE 0402,
ALL RESISTORS ARE 1%

CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

CONTRACT NO.

APPROVALS

DRAWN: M. Merchant

CHECKED:

APPROVED:

ENGINEER:

DESIGNER: M. Merchant

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		TITLE: SCHEMATIC LTC4088EDE-2 High Efficiency Battery Charger/USB Power Manager	
SIZE A	DWG NO. DC1377A	REV A	
DATE: Thursday, February 28, 2008		SHEET 1 OF 1	