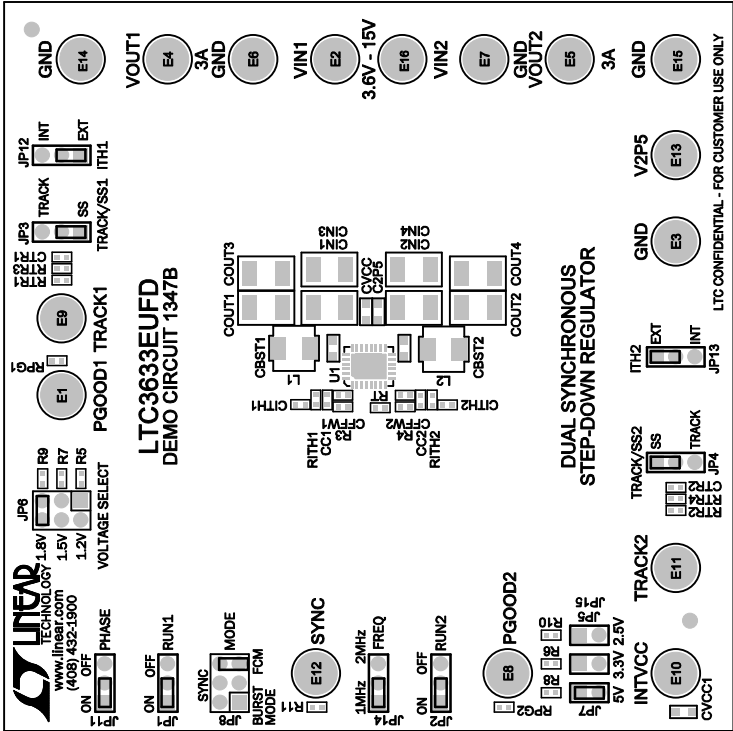
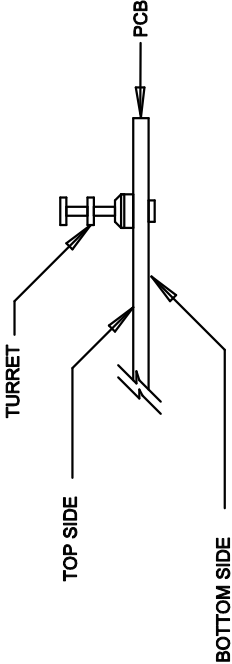


| REVISION HISTORY | | | |
|------------------|-----|-------------|----------|
| ECO | REV | DESCRIPTION | APPROVAL |
| | 1 | PRODUCTION | TOM G. |
| | | | DATE |
| | | | 05/17/11 |

NOTES: UNLESS OTHERWISE SPECIFIED,

1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. ASSEMBLY PROCESS SHALL INCLUDE: REFLOW SOLDER TOP SIDE SMD.
3. PARTS TO OMIT WILL BE SPECIFIED ON THE BILL OF MATERIALS.
LOCATIONS OF OMITTED PARTS SHALL BE FREE OF SOLDER.
MASK THE SOLDER STENCIL WHERE SMT PARTS ARE OMITTED.
4. INSTALL SHUNTS AS SHOWN ON ASSY DRAWING.
5. DEPAKELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON FOUR SIDES OF THE BOARD EDGE.
6. DO NOT APPLY ANY KIND OF ASSEMBLY STAMP OR QA STAMP TO ANY BOARD.
7. INSTALL TURRETS AS SHOWN.



| | | | | | | | | | |
|-------------|--------|--|--------------------|-------------|------------|--|--|--|--|
| APPROVALS | | <div><div><div><div>1630 MCCARTHY BLVD</div><div>MILPITAS, CA 95035</div><div>PH: (408)432-1900</div><div>www.linear.com</div><div>LTC CONFIDENTIAL-</div><div>FOR CUSTOMER USE ONLY</div></div></div><div><div><div><div></div><div>LINEAR</div><div>TECHNOLOGY</div></div></div></div></div> | | | | TITLE: TOP ASSEMBLY DRAWING, DUAL SYNCHRONOUS STEP -DOWN REGULATOR | | | |
| PCB DES. | MI | SIZE | IC NO. | LTC3633EUFD | REV | | | | |
| APP. ENG. | TOM G. | N/A | DEMO CIRCUIT 1347B | | 1 | | | | |
| | | | | | | | | | |
| | | | | | | | | | |
| | | | | | | | | | |
| SCALE: NONE | | FILENAME: 1347b-1.PCB | | | SHT 1 of 2 | | | | |