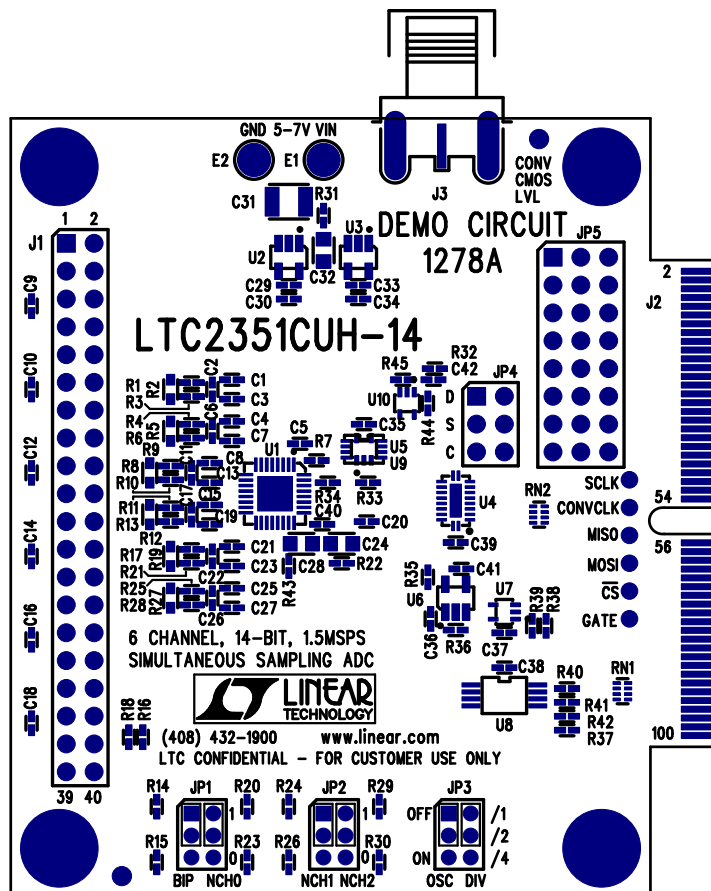


REVISIONS

REV	DESCRIPTION	APPR	DATE
A	1ST PROTOTYPE	GH	05-22-07

CENTER PIN OF J3
SHALL BE FREE OF SOLDER.
0.04" MIN. FROM THE EDGE
OF THE BOARD ON TOP SIDE.

J3 CONNECTOR
MOUNTING POSITION



NOTES: UNLESS OTHERWISE SPECIFIED

1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. INSTALL SHUNTS ON JP1-JP3 PIN 1&3, PIN 2&4.
3. DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON FOUR SIDES OF THE BOARD EDGE.
4. ASSY PROCESSES SHALL INCLUDE: REFLOW SOLDER TOP SIDE SMD.

TOP SILKSCREEN

LINEAR TECHNOLOGY DATE: 05-22-07

DC1278A * LTC2351CUH-14

6 CHANNEL, 14-BIT, 1.5MSPS

SIMULTANEOUS SAMPLING ADC

APPROVALS

	INIT	DATE
DRAWN		
CHECK		
DESIGN	KIM T.	05-22-07
ENGR	GUY H.	05-22-07



1630 MCCATHY BLVD
MILPITAS, CA 95035
PH: (408)432-1900
LTC CONFIDENTIAL-
FOR CUSTOMER USE ONLY

TITLE: TOP ASSEMBLY DRAWING

6 CHANNEL, 14-BIT, 1.5MSPS
SIMULTANEOUS SAMPLING ADC

SIZE	REV.
A	A

SCALE = NONE

SHT 1 of 1