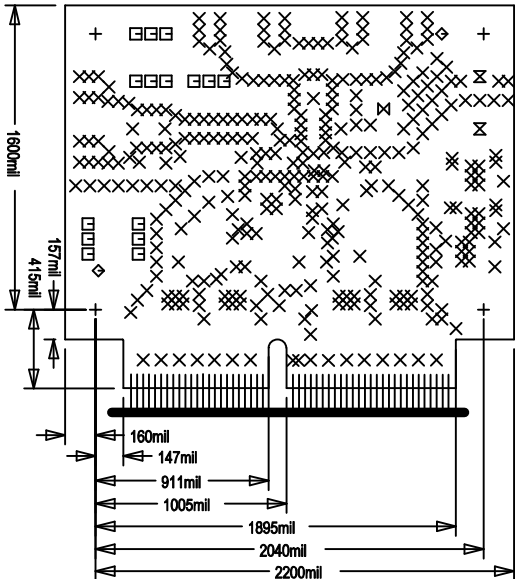


REVISIONS			
REV		APPR	DATE

Linear Tech. Corp.
Demo Circuit 1250A

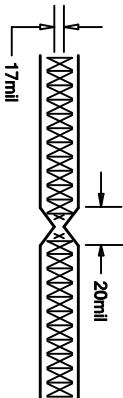
Component Side



SIZE	QTY	SYM	PLATED	TOL
187	4	+	YES	+/-3mil
10	367	X	YES	+/-3mil
31	15	□	YES	+/-3mil
70	2	◇	NO	+/-3mil
63	1	⊗	YES	+/-3mil
94	2	⊗	YES	+/-3mil

NOTES : Unless Otherwise Specified

- FAB PER IPC-A-600.
- MATERIAL: EPOXY FIBERGLASS, NEMA GRADE FR-4.
2 OZ. COPPER FINISH ON TOP LAYER.
1 OZ. COPPER FINISH ON REMAINING LAYERS.
THICKNESS .060 +/- .005 TOTAL OF 4 LAYERS.
FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: DIMENSIONS SHOWN AND TOLERANCES +/-0.010
0.00 ARE PRIMARY DATUMS.
- DRILLING: DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER. .001 INCH THICK MIN. ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
HOLE LOCATION TOLERANCES ARE +/-3mil
INCH IN RELATION TO CENTER
- FINISH: SMOBC USING LPI BOTH SIDES COLOR GREEN.
GOLD IMMERSION BOTH SIDES.
- SILKSCREEN : USING WHITE NON-CONDUCTIVE EPOXY INK.
- DO NOT ALTER ARTWORK TO ADD LOGO OR DATE CODE BUT YOU MAY MODIFY PAD SIZE TO MEET END FINISH.
- PCBS SHALL BE ROHS COMPLIANT.
- SCORING:



- IMPEDANCE CONTROL: 30milS TRACE = 50 OHMS
- FINGER AREA, GOLD PLATE TO MIN. OF 0.0008mm PER MIL-G-45204, TYP II, CL1, GRADE C, OVER 0.005mm LOW STRESS NICKEL PER QQ-N-290, CL2.

Fabrication Drawing

APPROVALS			
DRAWN	INIT	DATE	
CHECK			
DESIGN	RudgB		
ENGR	ChengP		
TITLE:			
16-Bit Receiver Subsystem, CMOS Outputs			
FB	Demo Circuit 1250A	REV. 1	
SCALE = NONE		DES-24xxxx	SHT 1 of 1

LINEAR
TECHNOLOGY

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