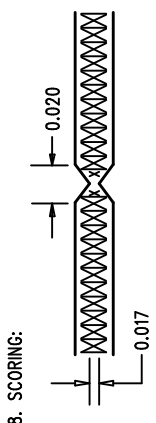


SIZE	QTY	SYM	PLATED	TOL
95	8	+	YES	+/-0.0
15	62	X	YES	+/-0.0
10	12	□	YES	+/-0.0
70	2	◇	NO	+/-0.0
215	8	⊠	YES	+/-0.0
187	4	⊞	NO	+/-0.0

REVISIONS		
REV	DESCRIPTION	DATE

- NOTES : Unless Otherwise Specified**
- FAB PER IPC-A-600. PCBs ARE TO BE ROHS COMPLIANT.
 - MATERIAL: EPOXY FIBERGLASS, NEMA GRADE FR-4.
2 OZ. COPPER FINISH ON OUTER LAYERS.
THICKNESS .062 +/--.006 TOTAL OF 2 LAYERS.
FLAMMABILITY RATING: 94 V-0 MINIMUM .
 - SIZE: DIMENSIONS AND TOLERANCES SHOWN.
0.00 ARE PRIMARY DATUMS.
 - DRILLING: DRILL HOLES PER SCHEDULE. PLATE THROUGH
HOLES WITH COPPER, .001 INCH THICK MIN. ALL
HOLE SIZES ARE SPECIFIED AFTER PLATING.
HOLE LOCATION TOLERANCES ARE +/--.003
INCH IN RELATION TO CENTER
 - FINISH: SMOBC USING LPI BOTH SIDES COLOR GREEN.
GOLD IMMERSION.
 - SILKSCREEN : USING WHITE NON-CONDUCTIVE EPOXY INK.
 - DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE
BUT YOU MAY MODIFY PAD SIZE TO MEET END FINISH.



FAB DRAWING

UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES
TOLERANCES ON ANGLE ± 1
2 PLACE ± .01 3 PLACE ± .005
INTERPRET DIM AND TOL
PER ASME Y14.5M - 1994

APPROVALS		DATE	
PCB DESIGNER	J. WU	1/18/07	
ENGINEER	M. Lee	1/18/07	
CHECK			

SCALE = NONE		SHT 1 of 1	
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LINEAR TECHNOLOGY
1630 McCarthy Blvd.
Milpitas, CA 95035
PH: (408)432-1900

TITLE: LTC4358CDE
5A Ideal Diode

SIZE A DEMO DC1204A REV. 1