


NOTES : Unless Otherwise Specified

- 1. BREAK BOARDS FROM THE PANEL WHEN ASSEMBLY IS COMPLETE.
- 2. OPTIONAL COMPONENTS ARE UNSTUFFED AND SHOULD BE FREE OF SOLDER AFTER ASSEMBLY.
- 3. JUMPER INSTALLATION: JP1: GND AND GND JP2: VDD JP3: VDD JP4: 1/3

ASSY DRAWING

<div>APPROVALS</div>			<div>LINEAR TECHNOLOGY</div> <div>1630 McCarthy Blvd. Milpitas, CA 95035 Ph: (408)432-1900</div>			TITLE: LTC2242 FAMILY HIGH SPEED ADC/CMOS OUTPUT		
DRAWN	INIT	DATE	SIZE	A	DEMO	DC1133A	REV. A	
CHECK								
LAYOUT	J. WU	8/8/06						
ENGR	E. Trelewicz	8/8/06						
SCALE = NONE			SHT 1 of 1					