


NOTES : Unless Otherwise Specified

1. PLEASE TEST SOLDER SAMPLE BEFORE ASSEMBLE ALL THE BOARDS.
2. BREAK BOARDS FROM THE PANEL WHEN ASSEMBLY IS COMPLETE.
3. OPTIONAL COMPONENTS ARE UNSTUFFED AND SHOULD BE COVERED WITH SOLDER AFTER ASSEMBLY (NO EXPOSED PADS).
4. SHUNTS INSTALLATION:
JP1, JP2, JP4, JP5, JP6, JP7: L

ASSY DRAWING

APPROVALS					1630 McCarthy Blvd. Milpitas, CA 95035 PH: (408)432-1900	
DRAWN		INIT	DATE	TITLE: LTM4601EV HIGH EFFICIENCY DC/DC MODULES		
CHECK						
DESIGN		J. WU	9/25/06			
ENGR		M. Xie	9/25/06	SIZE A DEMO DC118A REV. A		
SCALE = NONE				SHT 1 of 1		