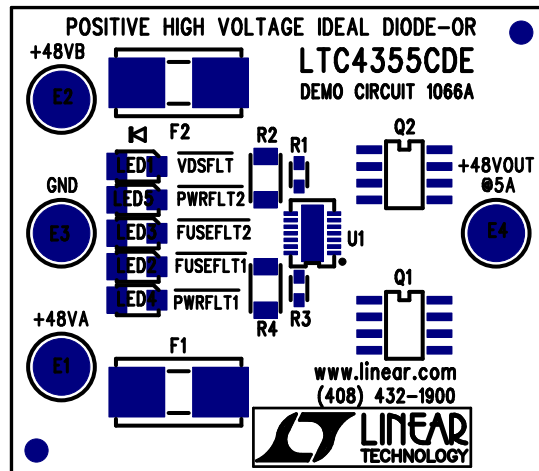


REVISIONS			
REV	DESCRIPTION	APPR	DATE
A-1	FINAL FABRICATION	ML	03-23-07



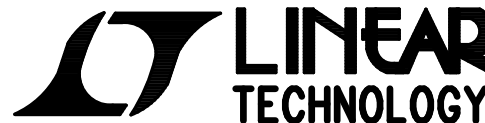
## NOTES: UNLESS OTHERWISE SPECIFIED

1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. NO SHUNT.
3. DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON FOUR SIDES OF THE BOARD EDGE.
4. ASSY PROCESS SHALL INCLUDE: REFLOW SOLDER TOP SIDE SMD.

## APPROVALS

	INIT	DATE
DRAWN		
CHECK		
DESIGN	KIM T.	03-23-07
ENGR	MITCHELL L.	03-23-07

SCALE = NONE



1630 MCCATHY BLVD  
MILPITAS, CA 95035  
PH: (408)432-1900  
LTC CONFIDENTIAL-  
FOR CUSTOMER USE ONLY

TITLE: TOP ASSEMBLY DRAWING

POSITIVE HIGH VOLTAGE IDEAL DIODE-OR

SIZE	DEMO	REV.
A	DC1066A-1 * LTC4355CDE	A-1

SHT 1 of 2