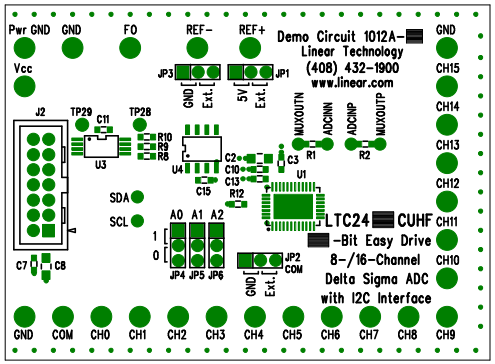
REVISIONS			
REV		APPR	DATE

Silkscreen Top



Install Shunts: JP1 = 5V
JP2 = GND
JP3 = GND
JP4 = 1
JP5 = 1
JP6 = 1

Table

ASSEMBLY	U1	BITS
DC1012A-A	LTC2499CUHF	24
DC1012A-B	LTC2497CUHF	16
DC1012A-C	LTC2495CUHF	16/PGA

NOTES :

1. Unused SMD components should be free of Solder.
2. Fill up all vias with solder.

Assembly Drawing Top

APPROVALS		
	INIT	DATE
DRAWN		
CHECK		
DESIGN		
ENGR		
Scale = None		

LINEAR TECHNOLOGY
1630 McCarthy Blvd.
Milpitas, CA 95035
PH: (408)432-1900

Title: 24-/16-Bit Easy Drive
8-/16-Channel Delta Sigma ADC with I2C Interface

Size: None	Demo Board: DC1012A-A/B/C	Rev. A
Design #		Sht: 1 of 1