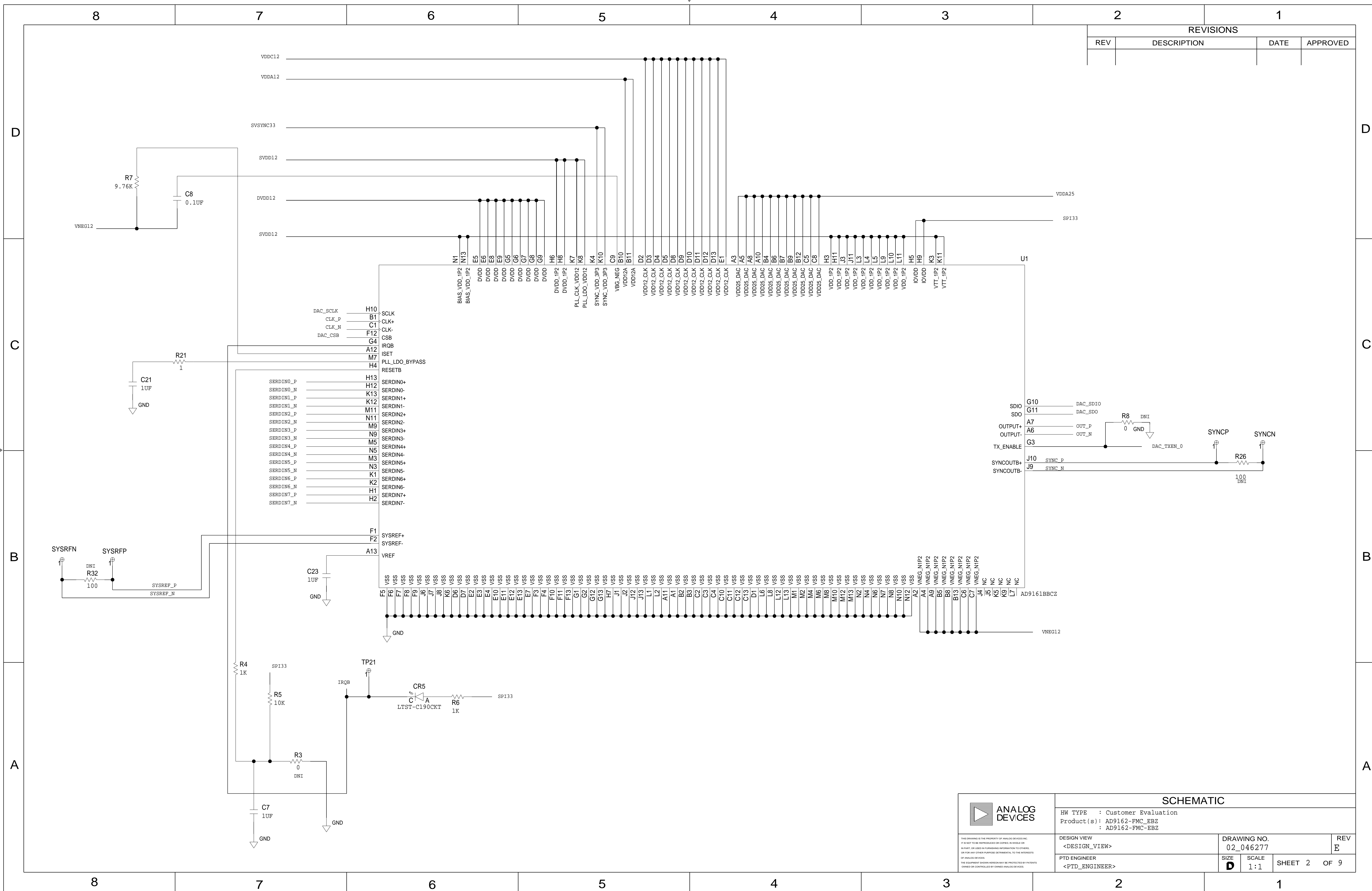
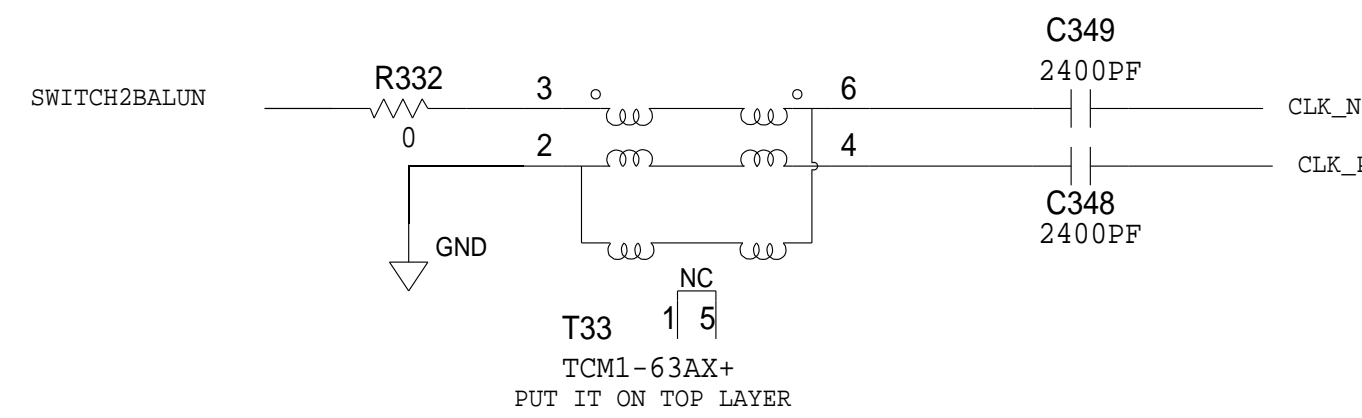


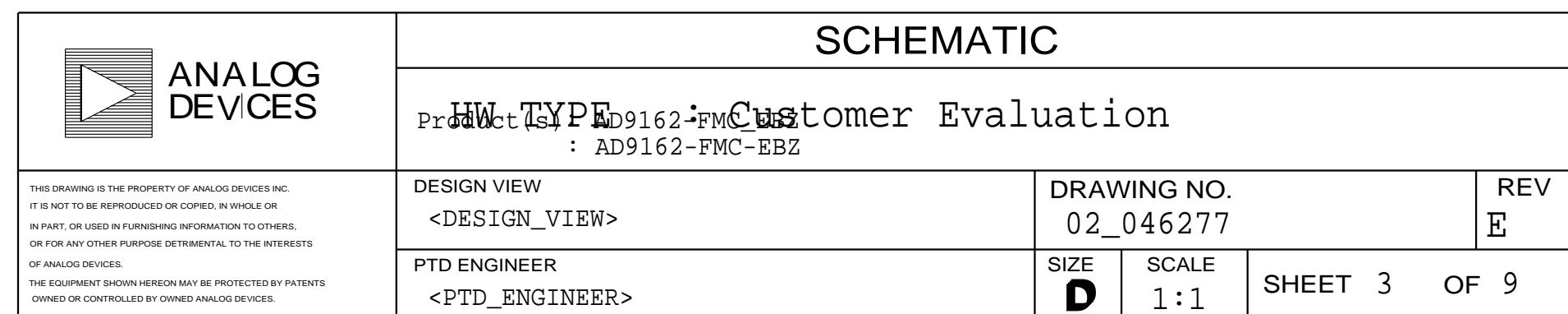
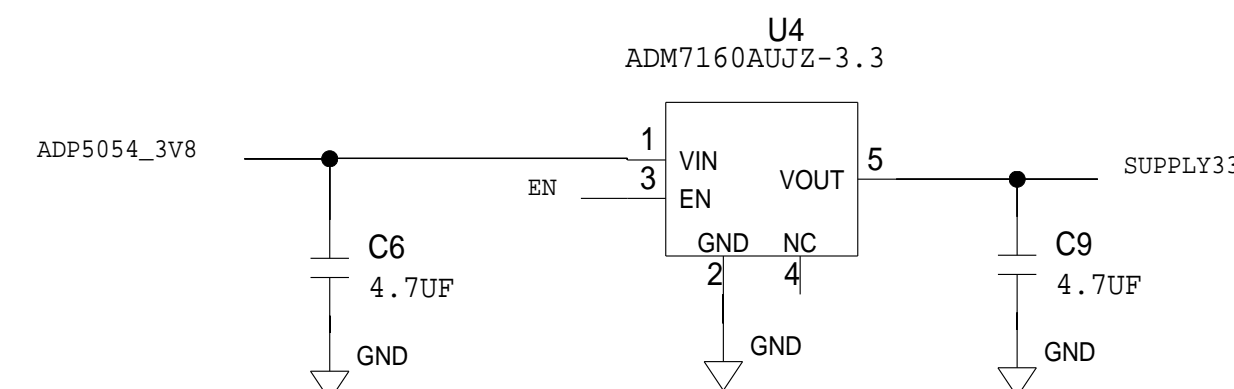
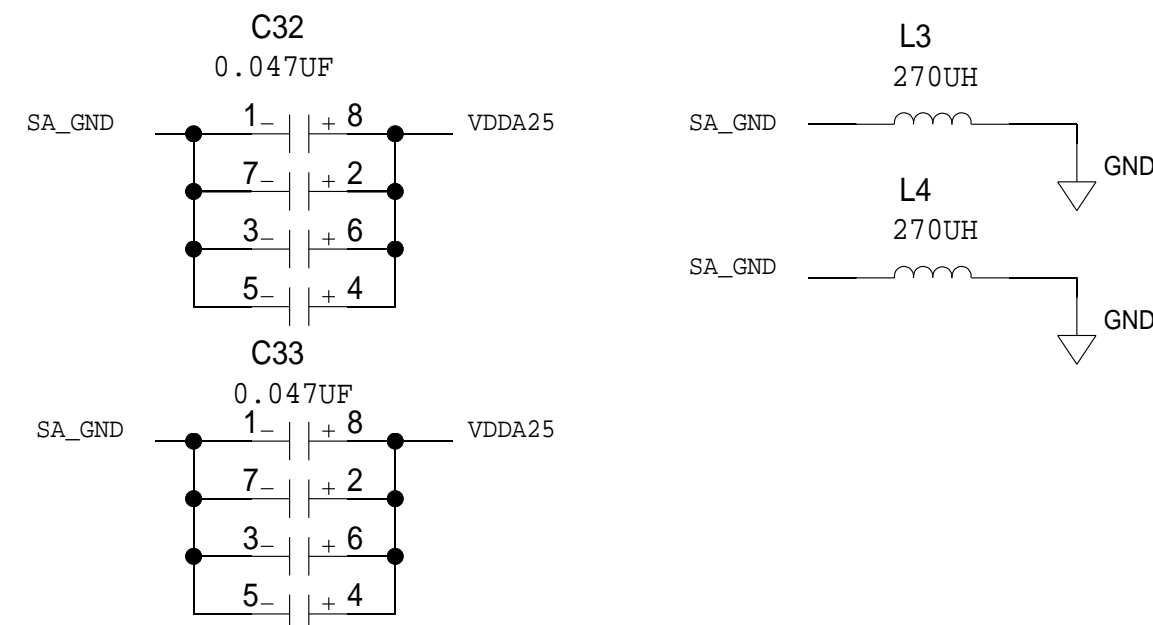
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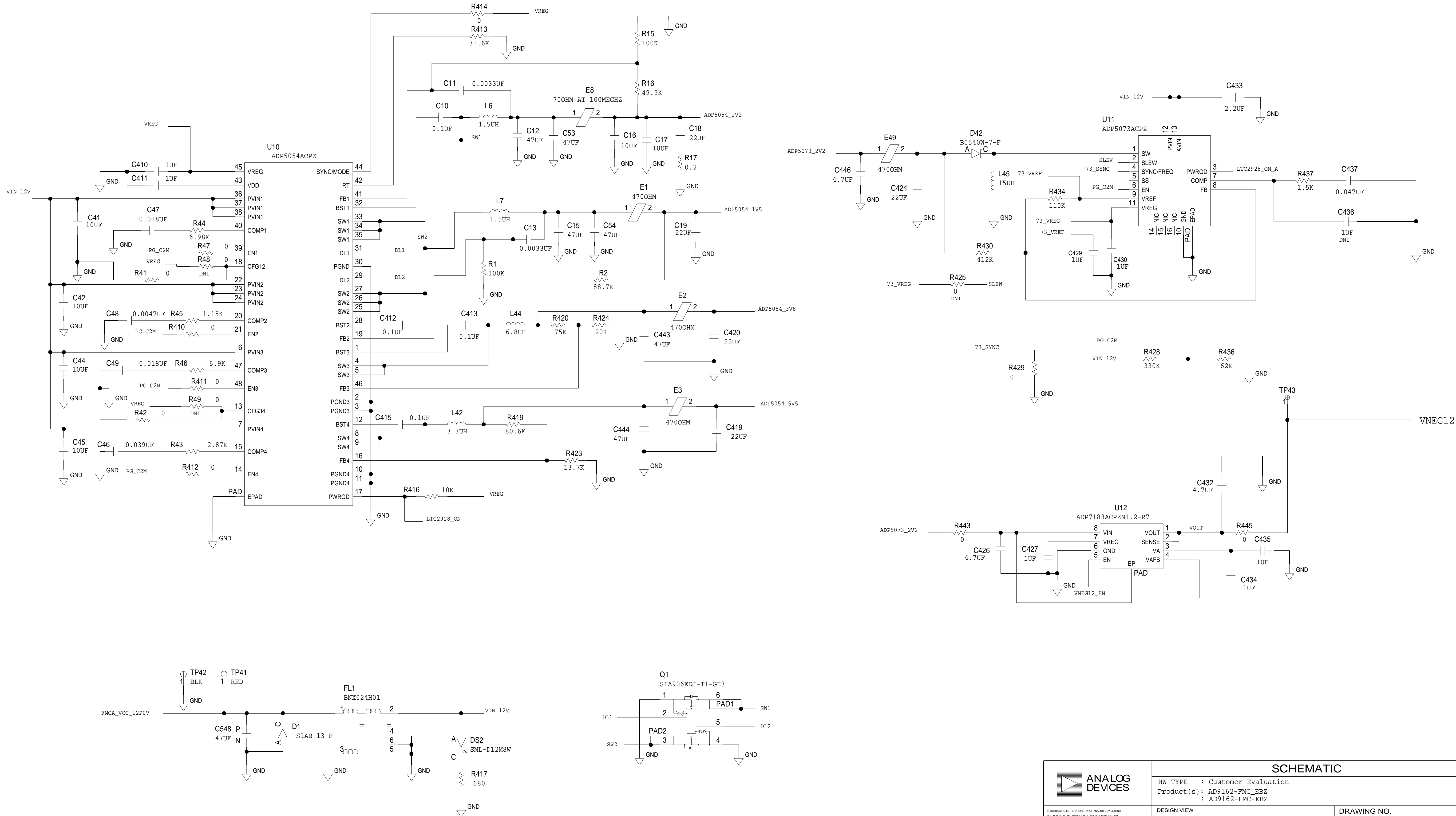
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



The schematic shows the output stage of the TCM1-83X+ transceiver. The input signal from SA_GND is connected to pin 1 of J32 through a 1000PF capacitor (C14). Pin 1 is also connected to VDDA25 via R33 (49.9 ohms). The output of the device is taken from pins 2 and 3, which are connected to OUT_N and OUT_P respectively. The output impedance is 50 OHM SINGLE-ENDED. The output signal is also connected to VDDA25 via R34 (49.9 ohms). The output impedance is 50 OHM DIFFERENTIAL.



POWER - REGULATOR & NEG LDO

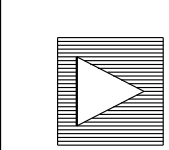
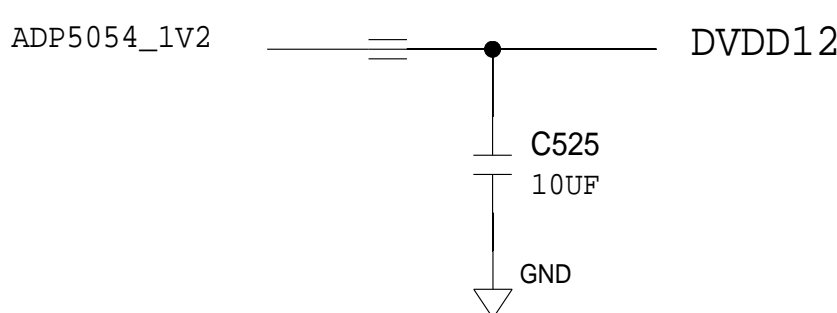
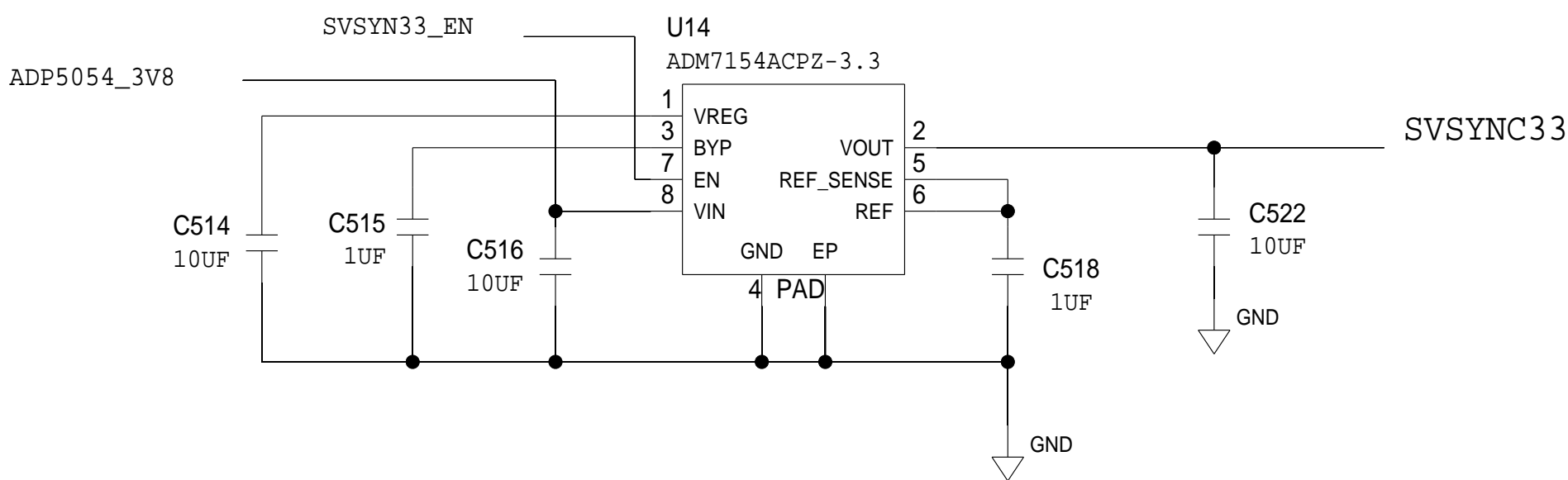
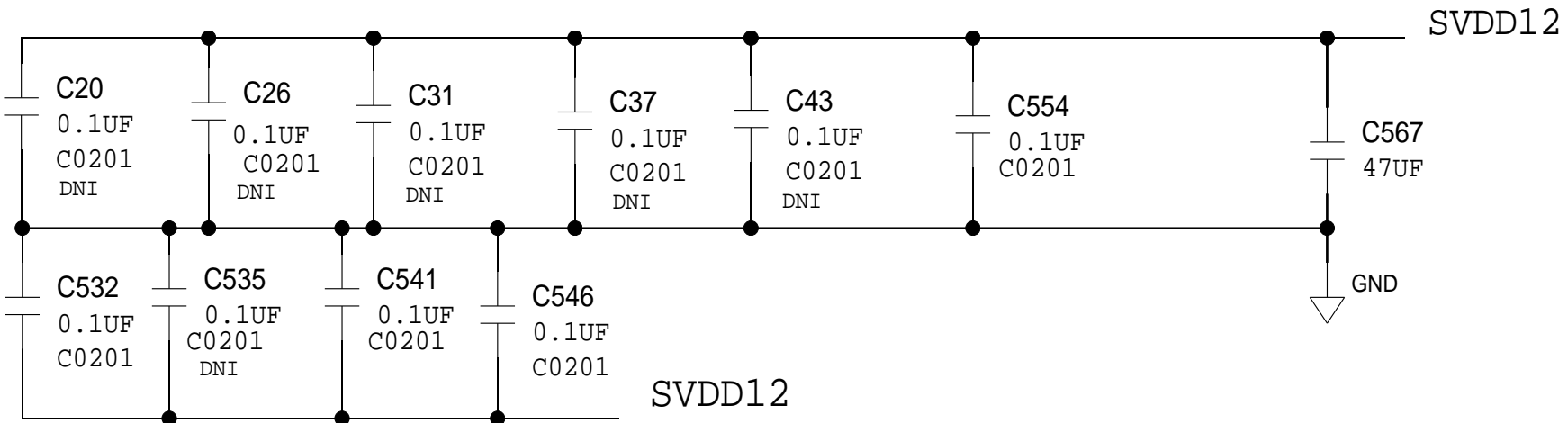
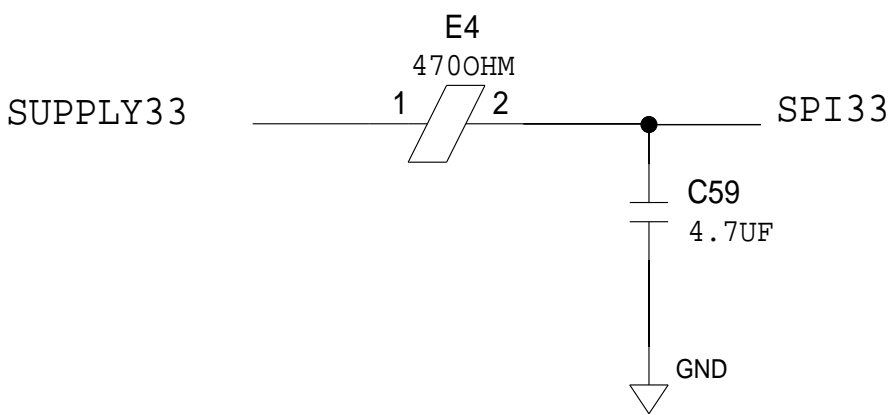
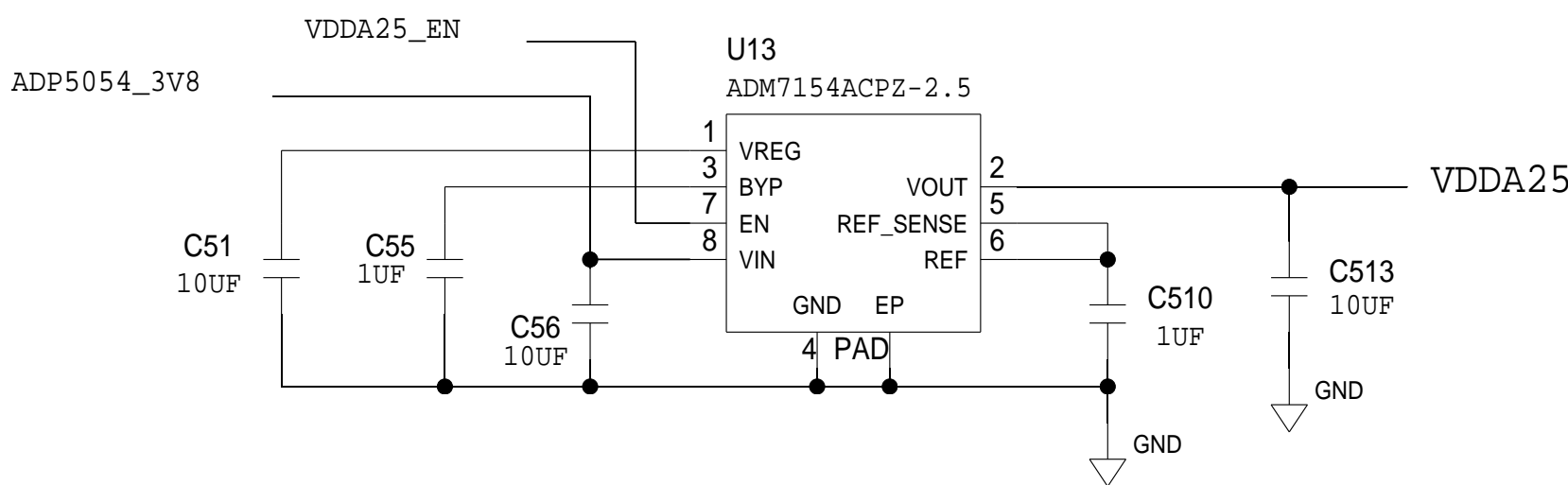
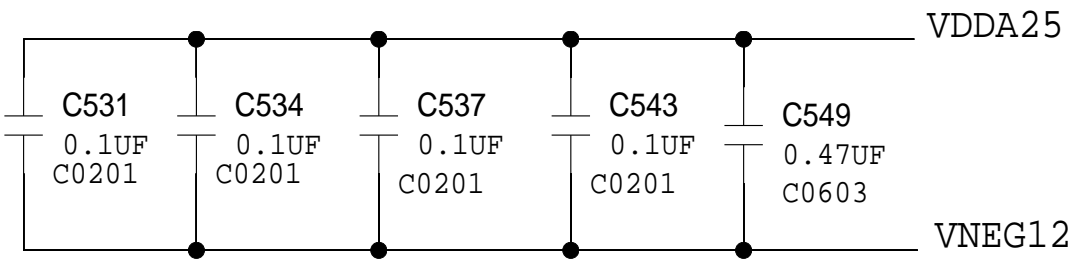
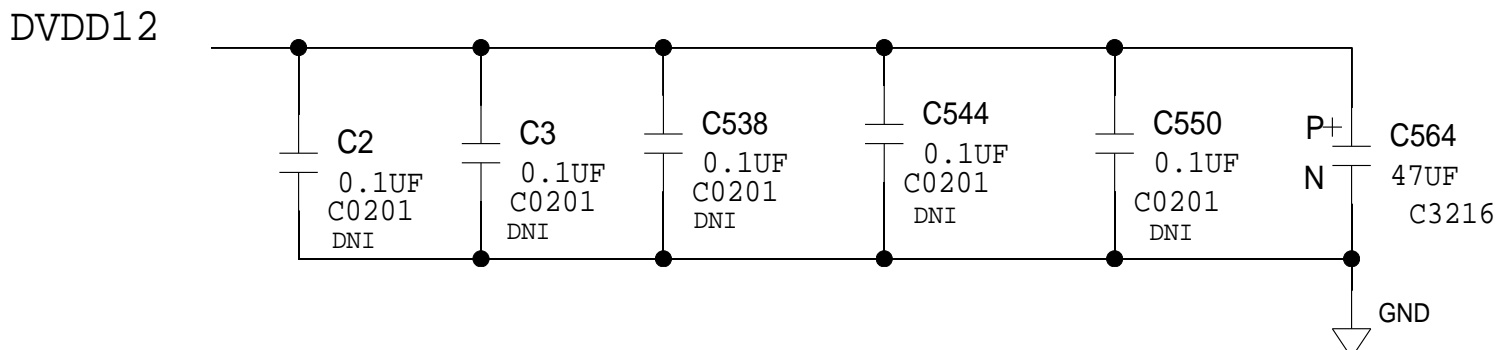
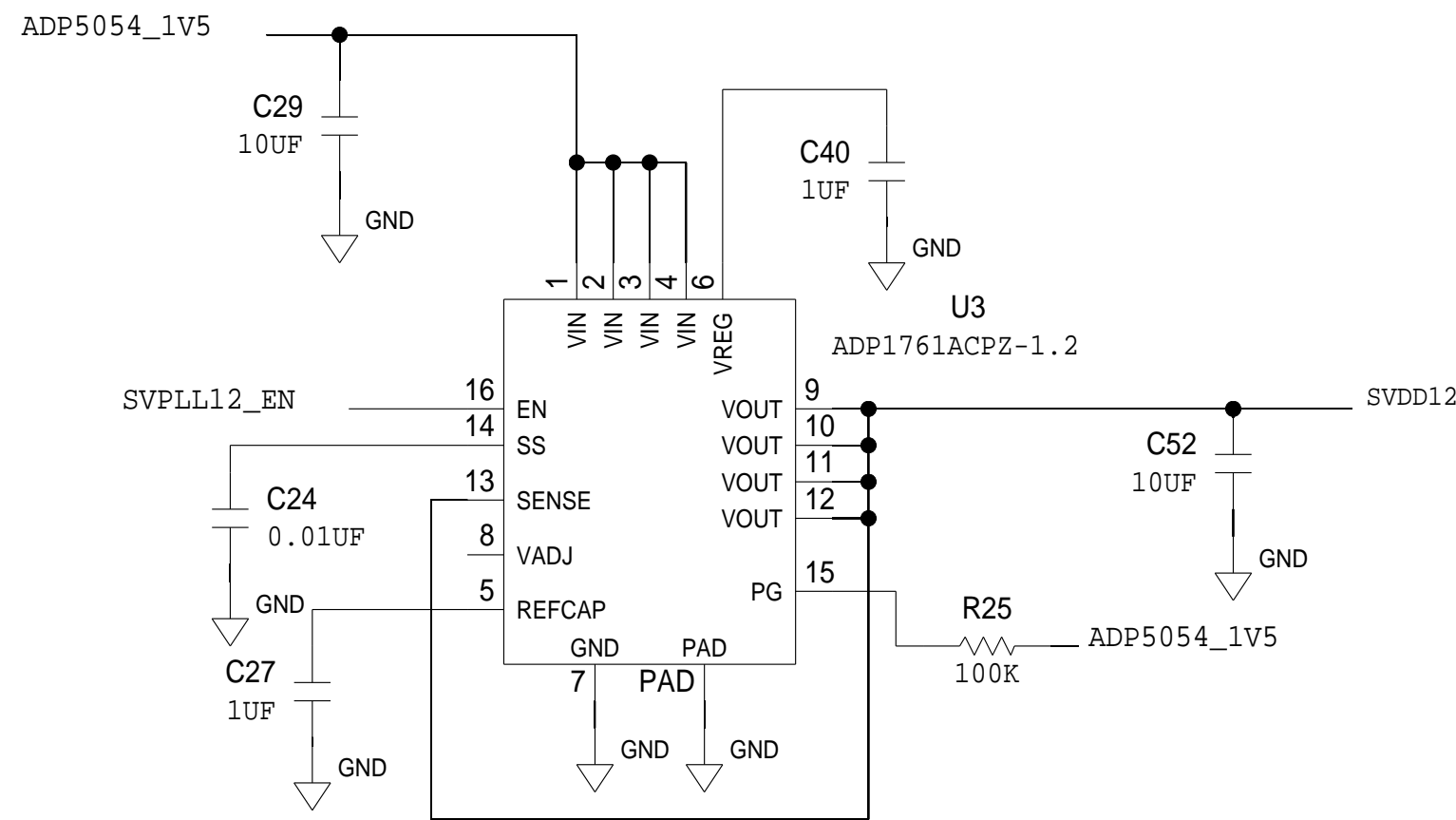
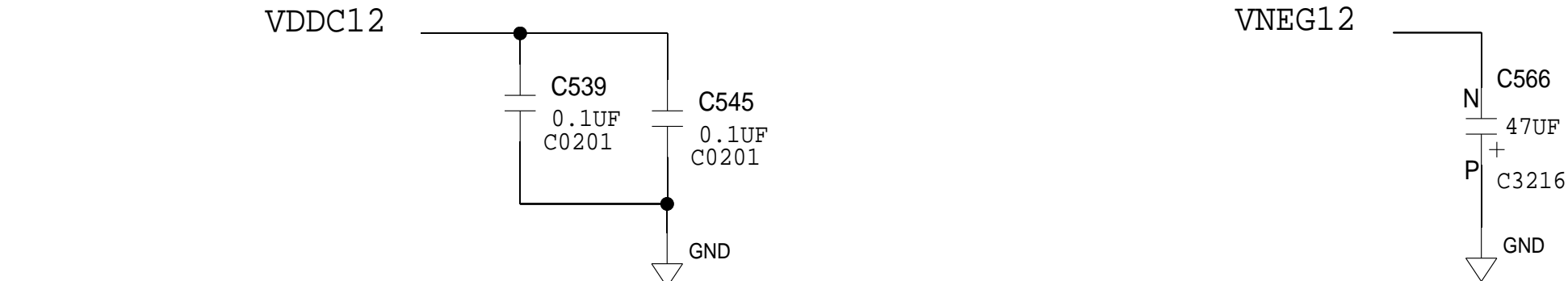
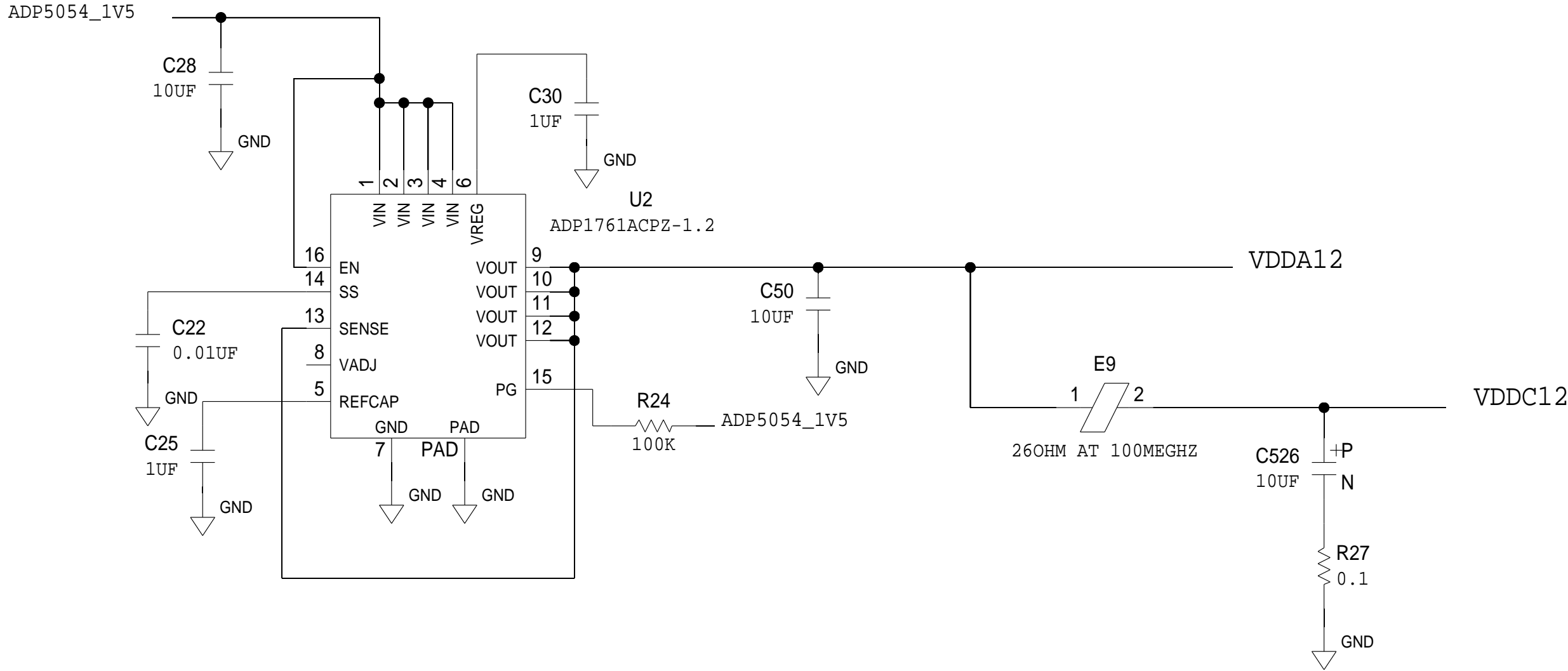


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REV	DESCRIPTION	DATE	APPROVED

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POWER - LDO

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



ANALOG
DEVICES

SCHEMATIC

HW TYPE : Customer Evaluation
Product(s): AD9162-FMC_EBZ
: AD9162-FMC-EBZ

DESIGN VIEW
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DRAWING NO.
02_046277

REV
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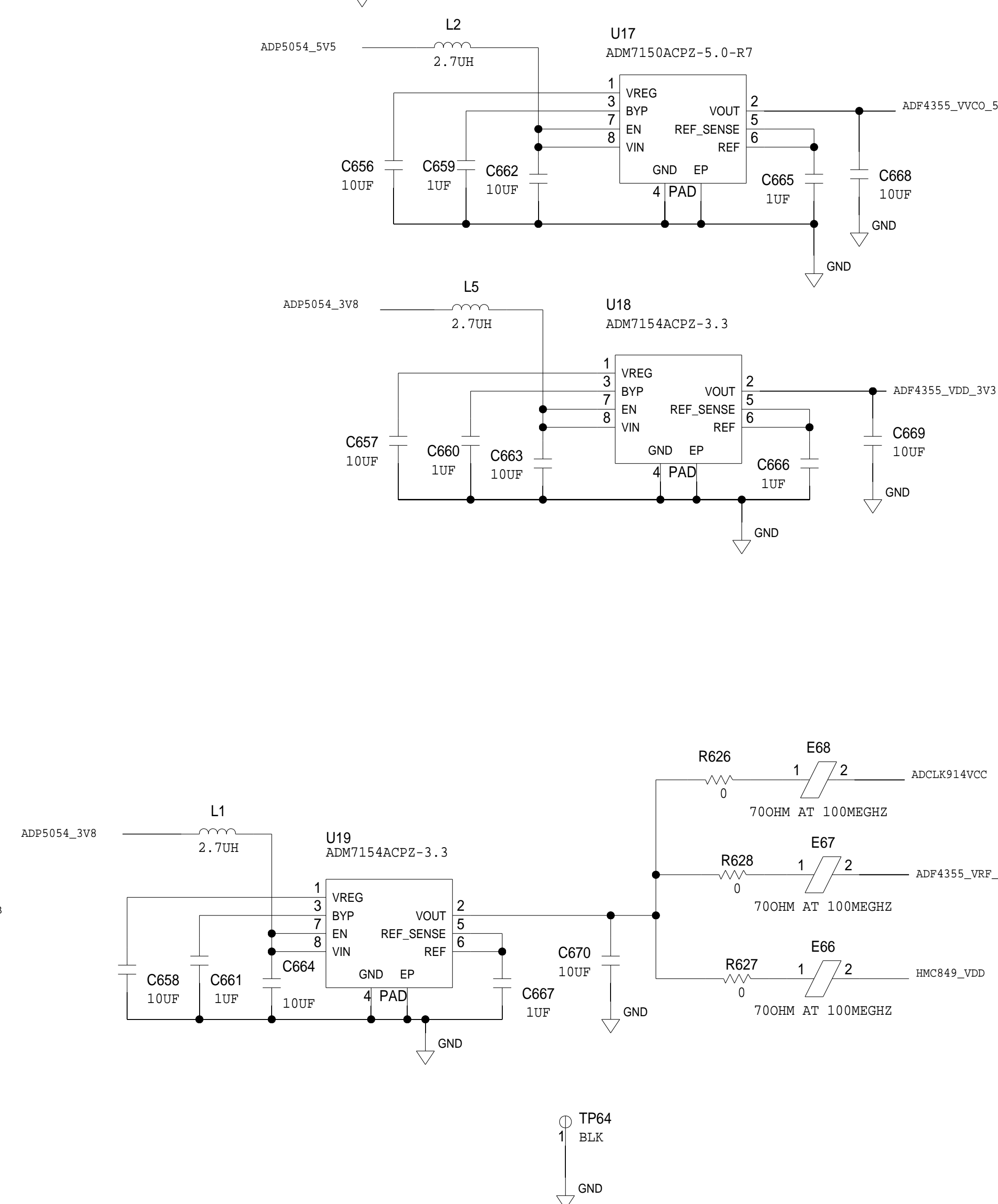
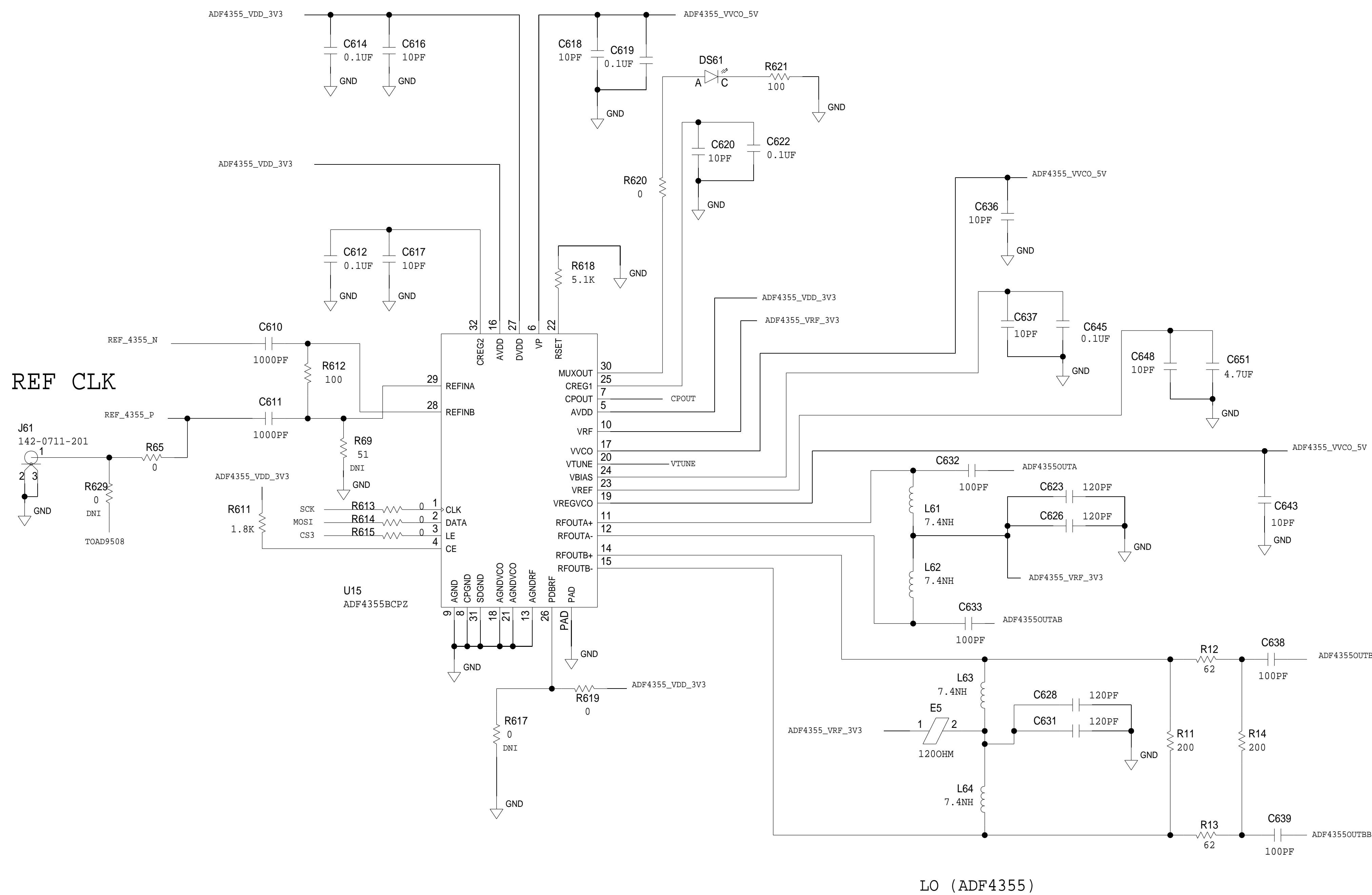
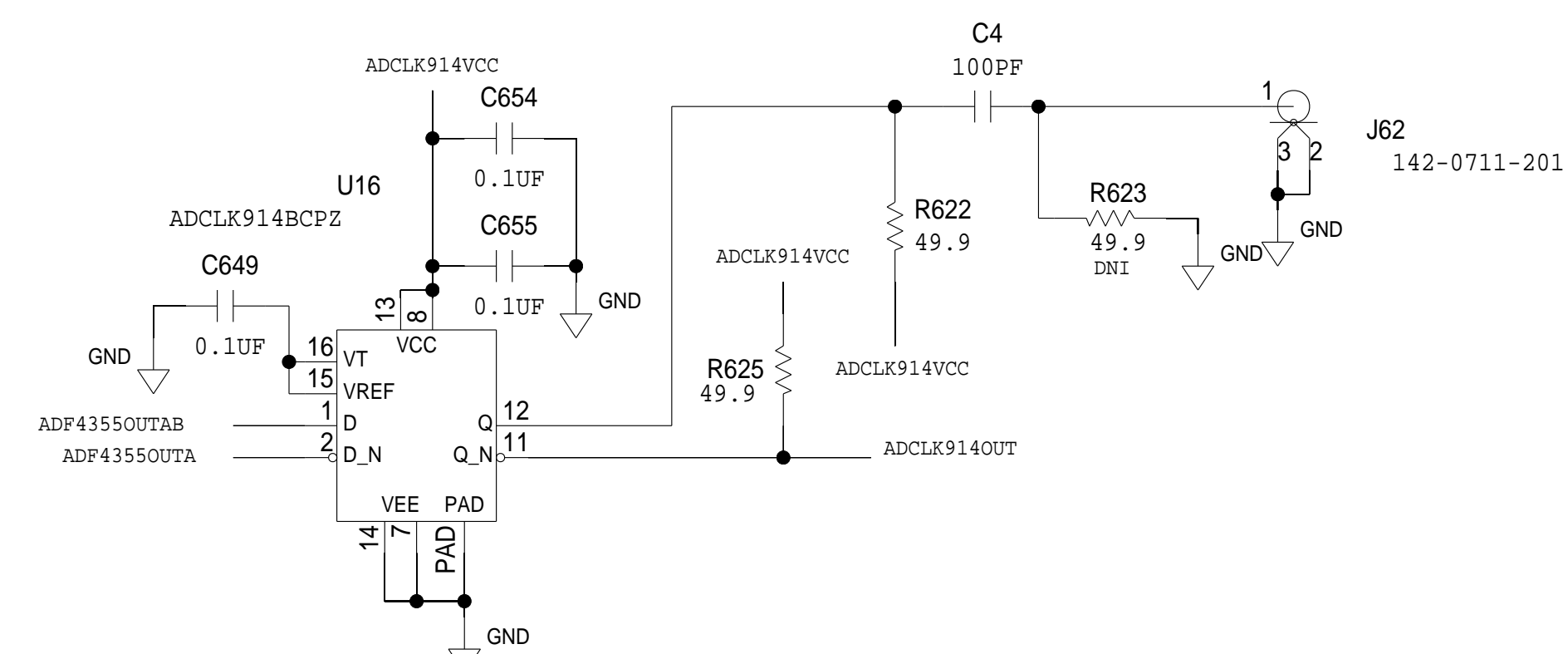
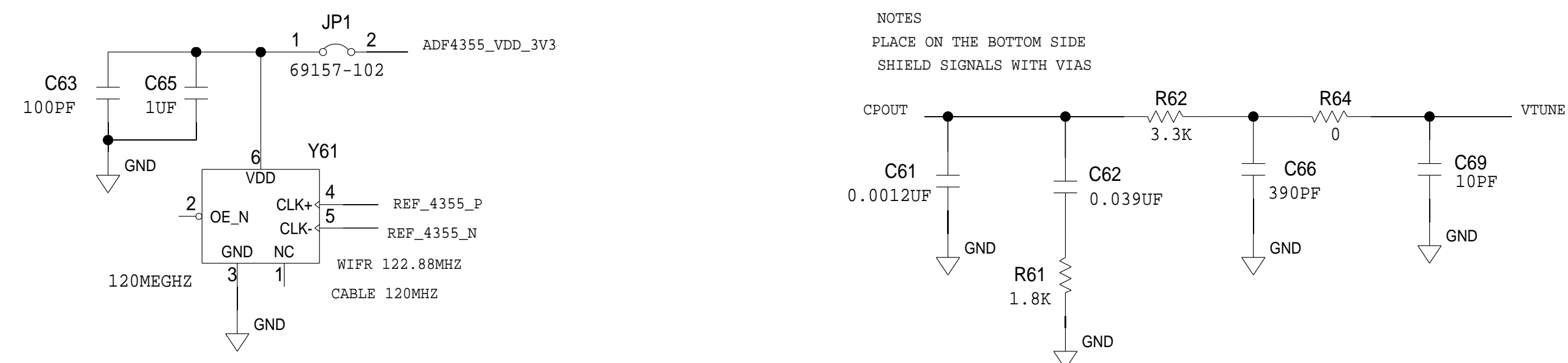
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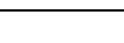
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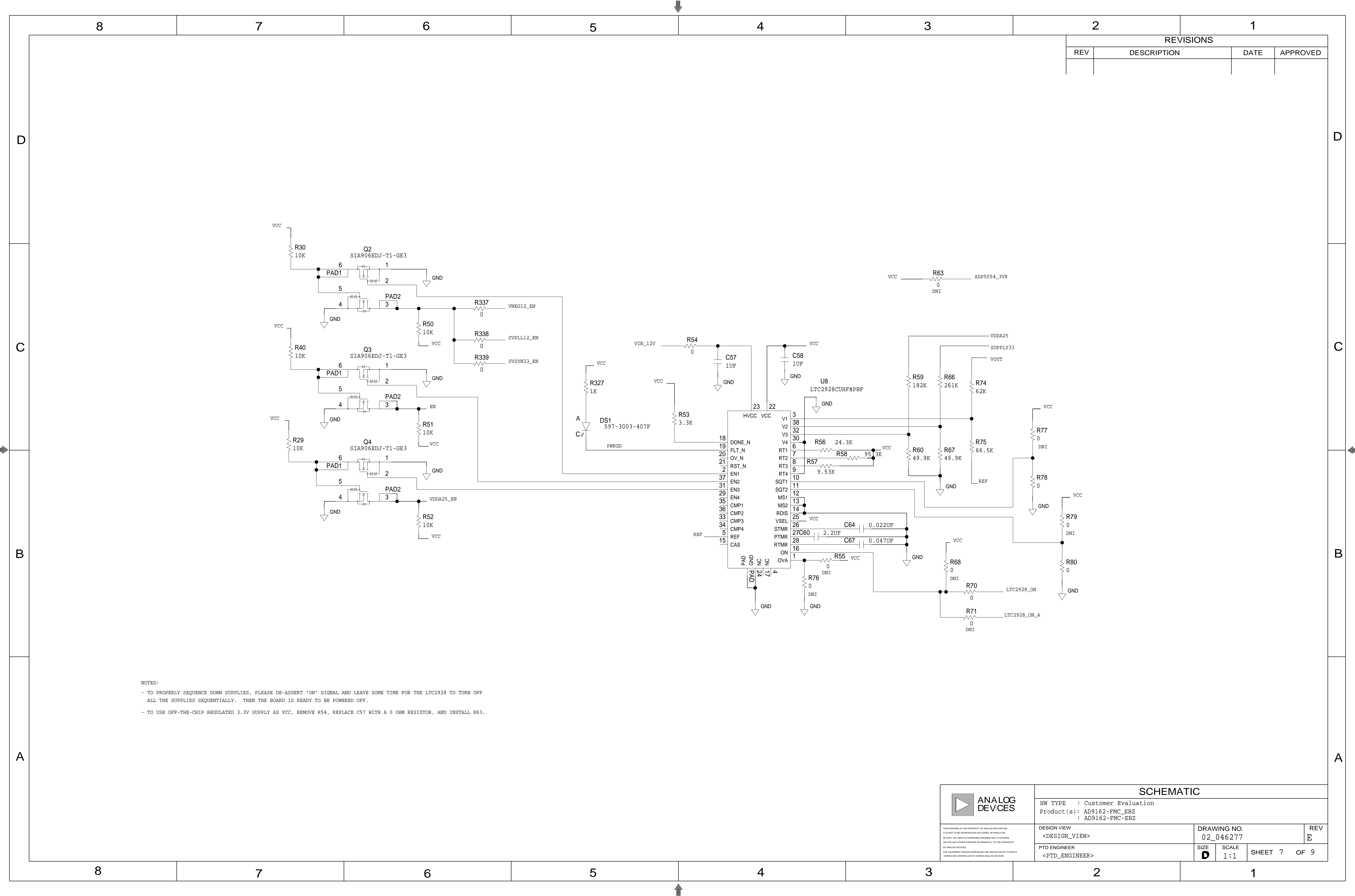
SHEET 5 OF 9

ONBOARD CLK - ADF4355-2 & CLK BUFFER



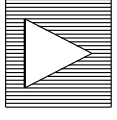
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	SCHEMATIC					
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	PTD ENGINEER <PTD_ENGINEER>		SIZE D		SCALE 1:1	
					SHEET 6 OF 9	



NOTES:

- TO PROPERLY SEQUENCE DOWN SUPPLIES, PLEASE DE-ASSERT "ON" SIGNAL AND LEAVE SOME TIME FOR THE LTC2928 TO TURN OFF ALL THE SUPPLIES SEQUENTIALLY. THEN THE BOARD IS READY TO BE POWERED OFF.
- TO USE OFF-THE-CHIP REGULATED 3.3V SUPPLY AS VCC, REMOVE R54, REPLACE C57 WITH A 0 OHM RESISTOR, AND INSTALL R63.

 ANALOG DEVICES	SCHEMATIC			
	HW TYPE : Customer Evaluation Product(s): AD9162-FMC_EBZ : AD9162-FMC-EBZ			
	DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_046277		REV E
	PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE 1:1	SHEET 7 OF 9

GPIO & MISC

REVISIONS

REV	DESCRIPTION	DATE	APPROVED
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