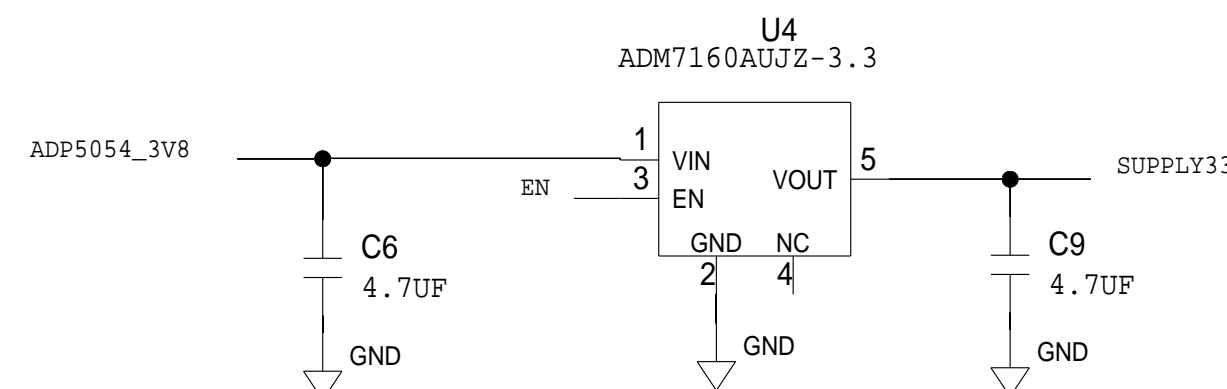
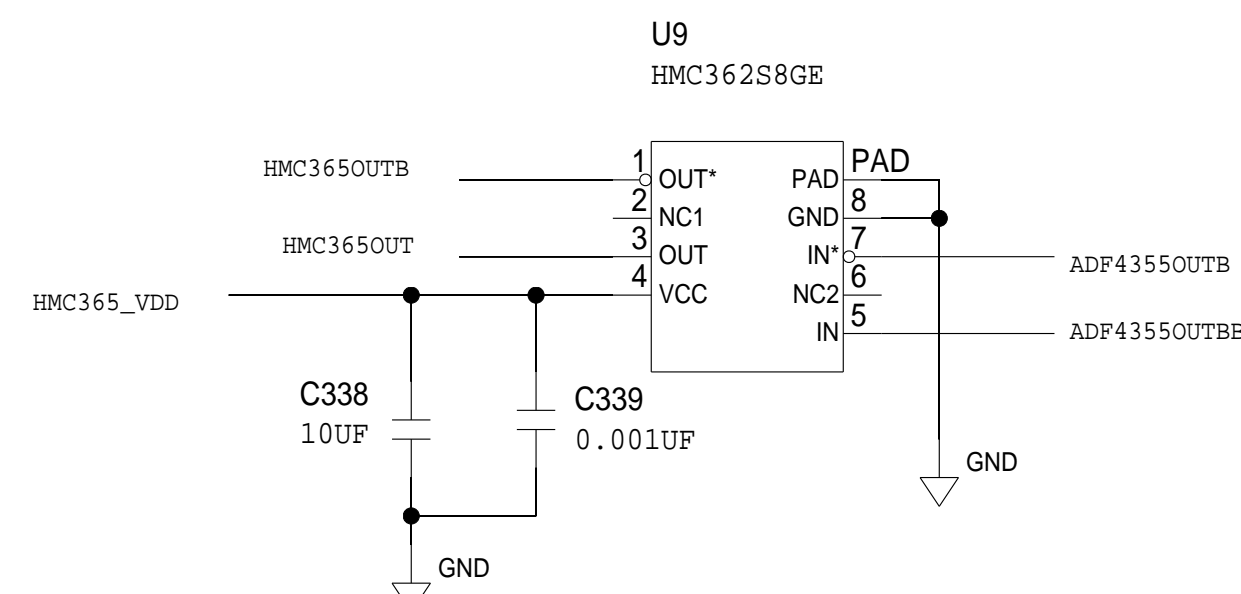
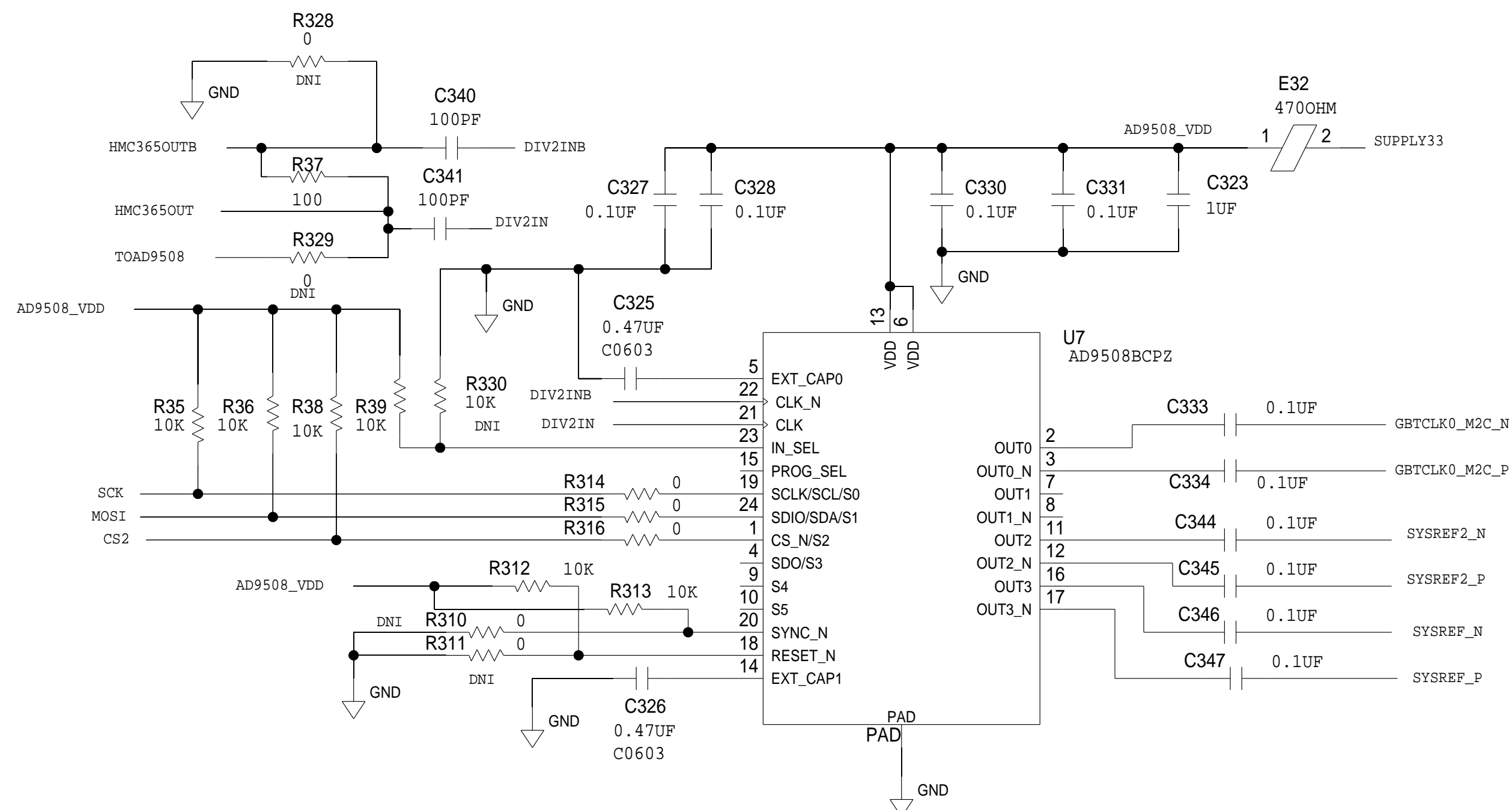
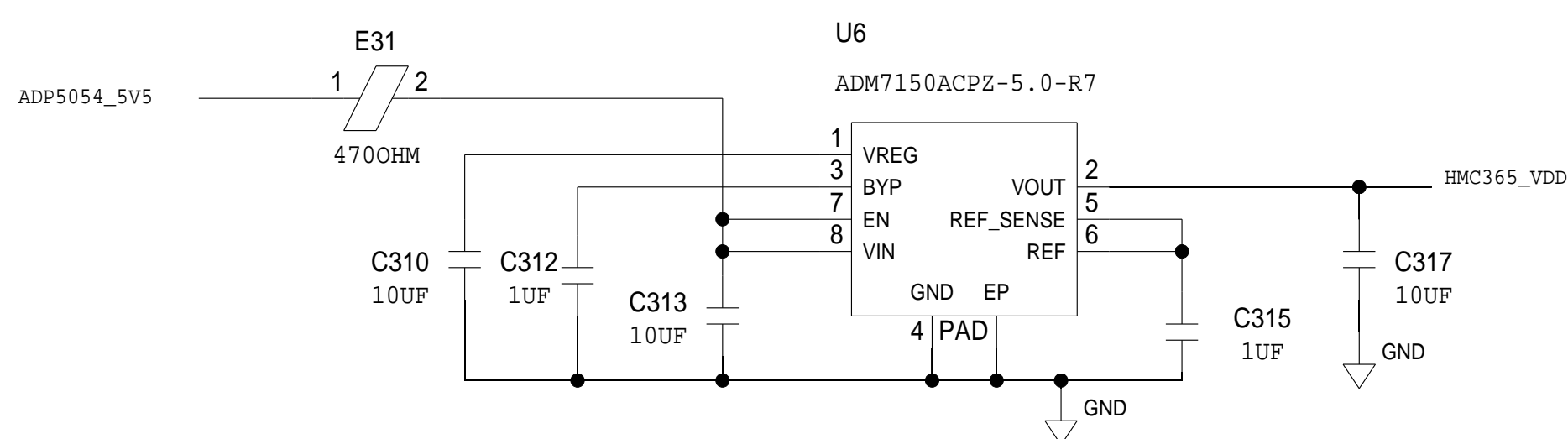
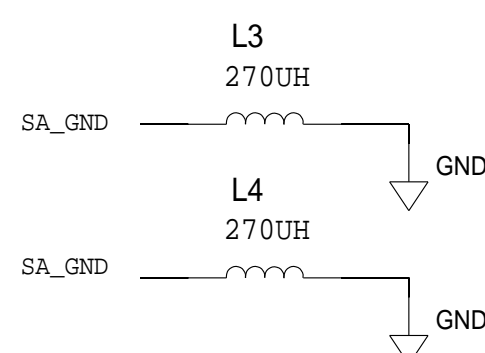
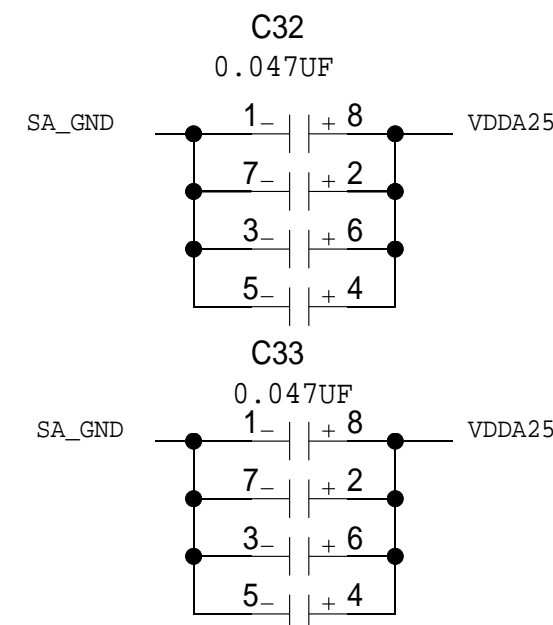
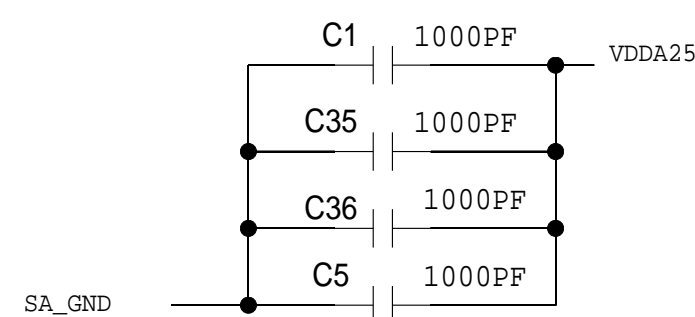
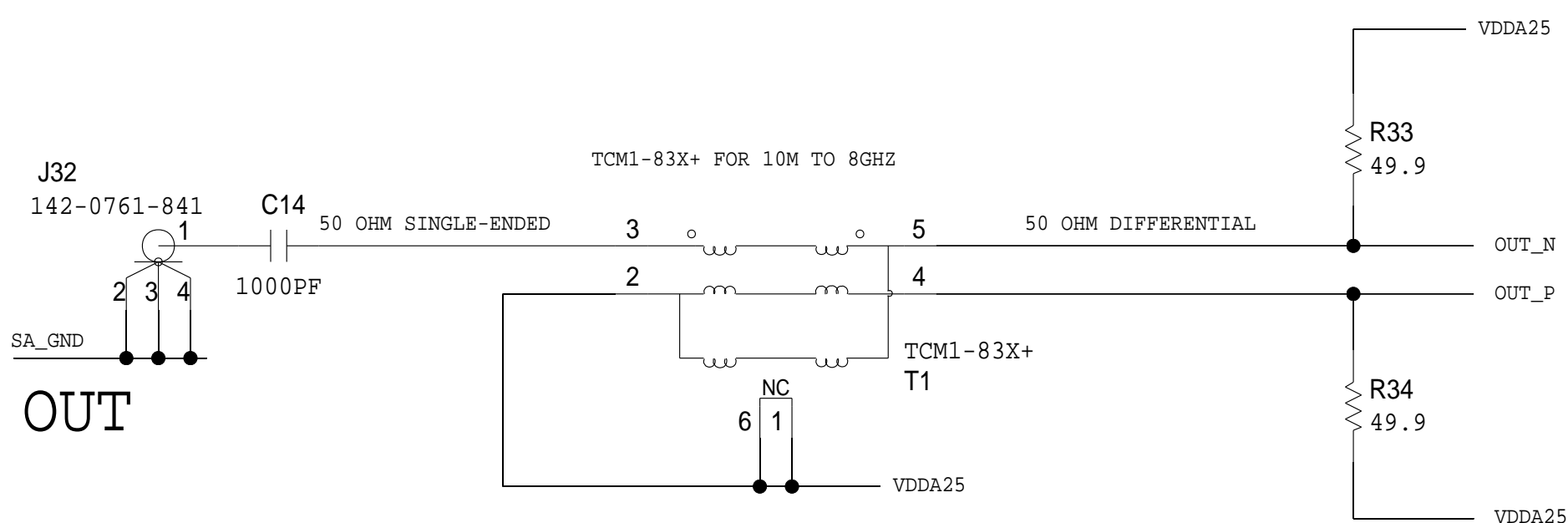
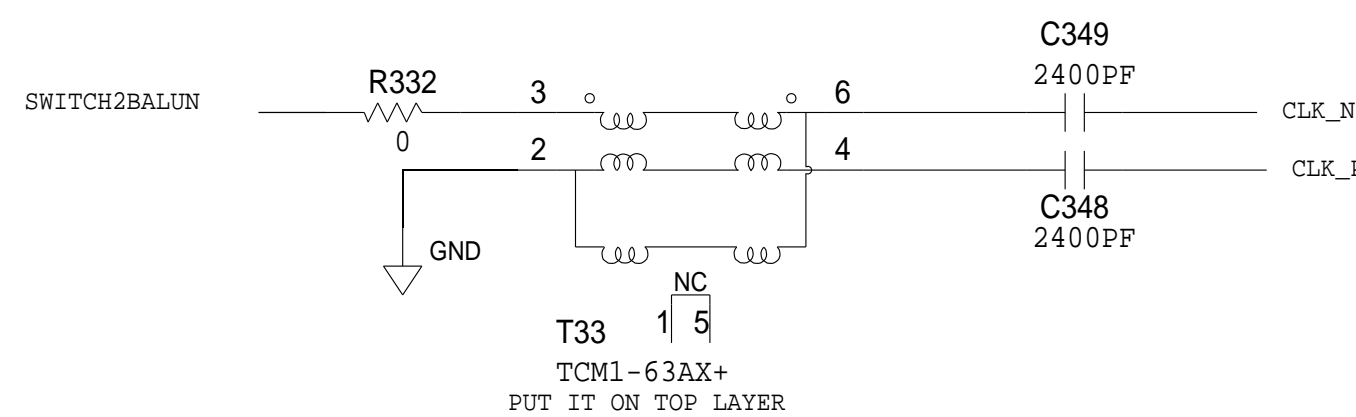
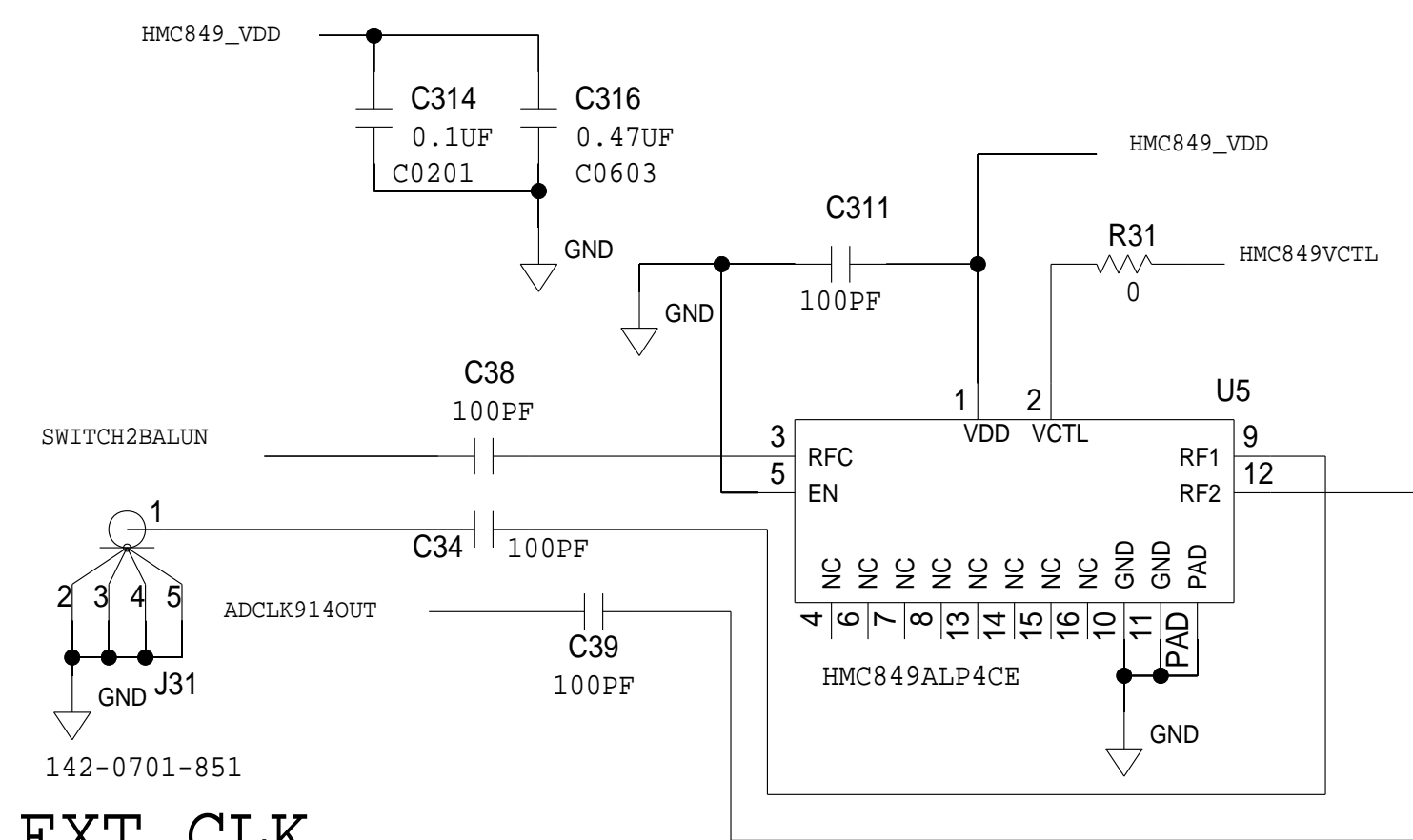


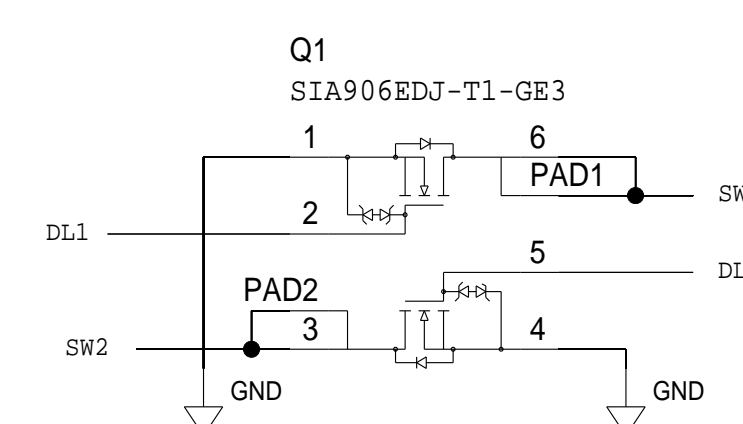
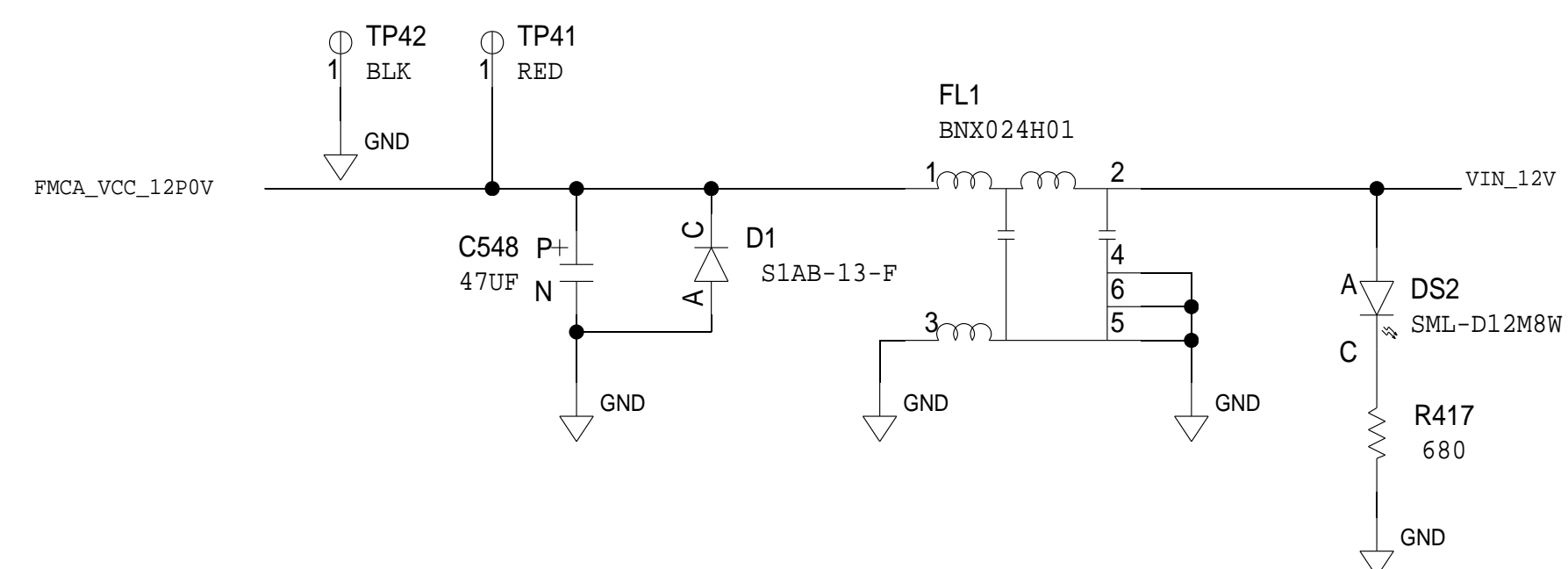
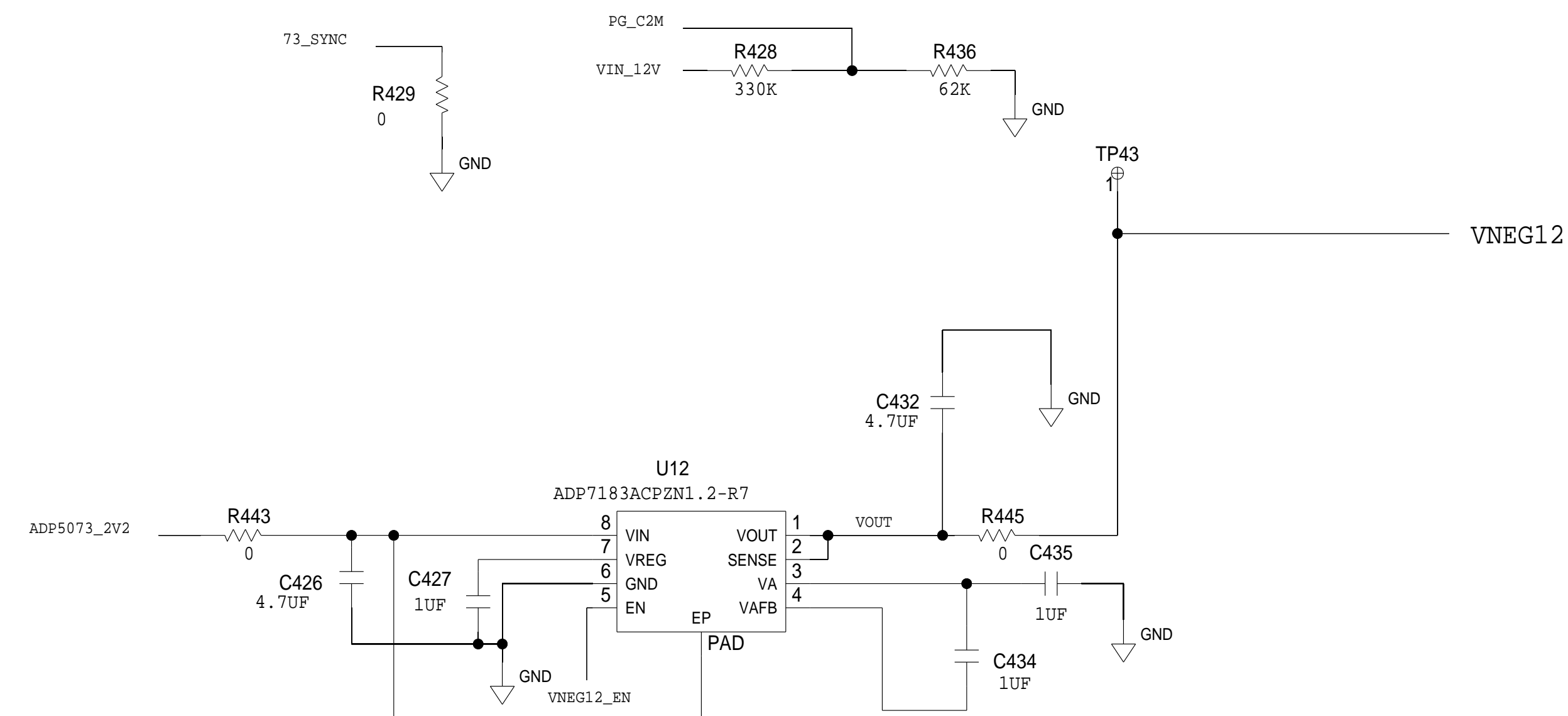
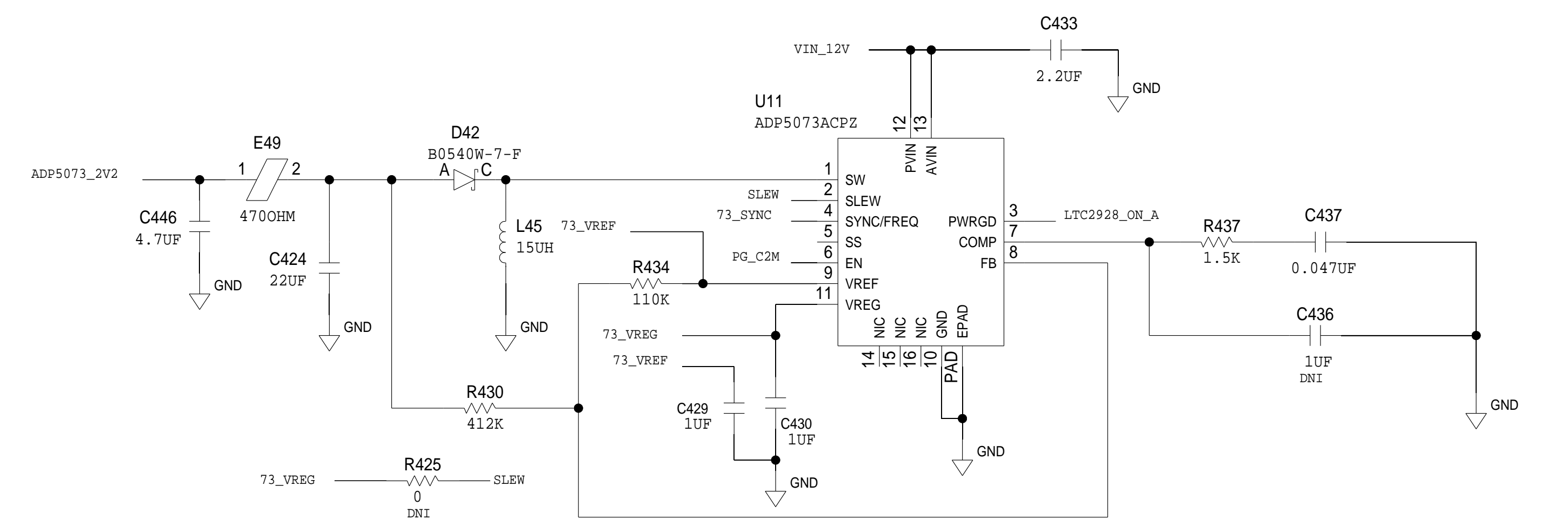
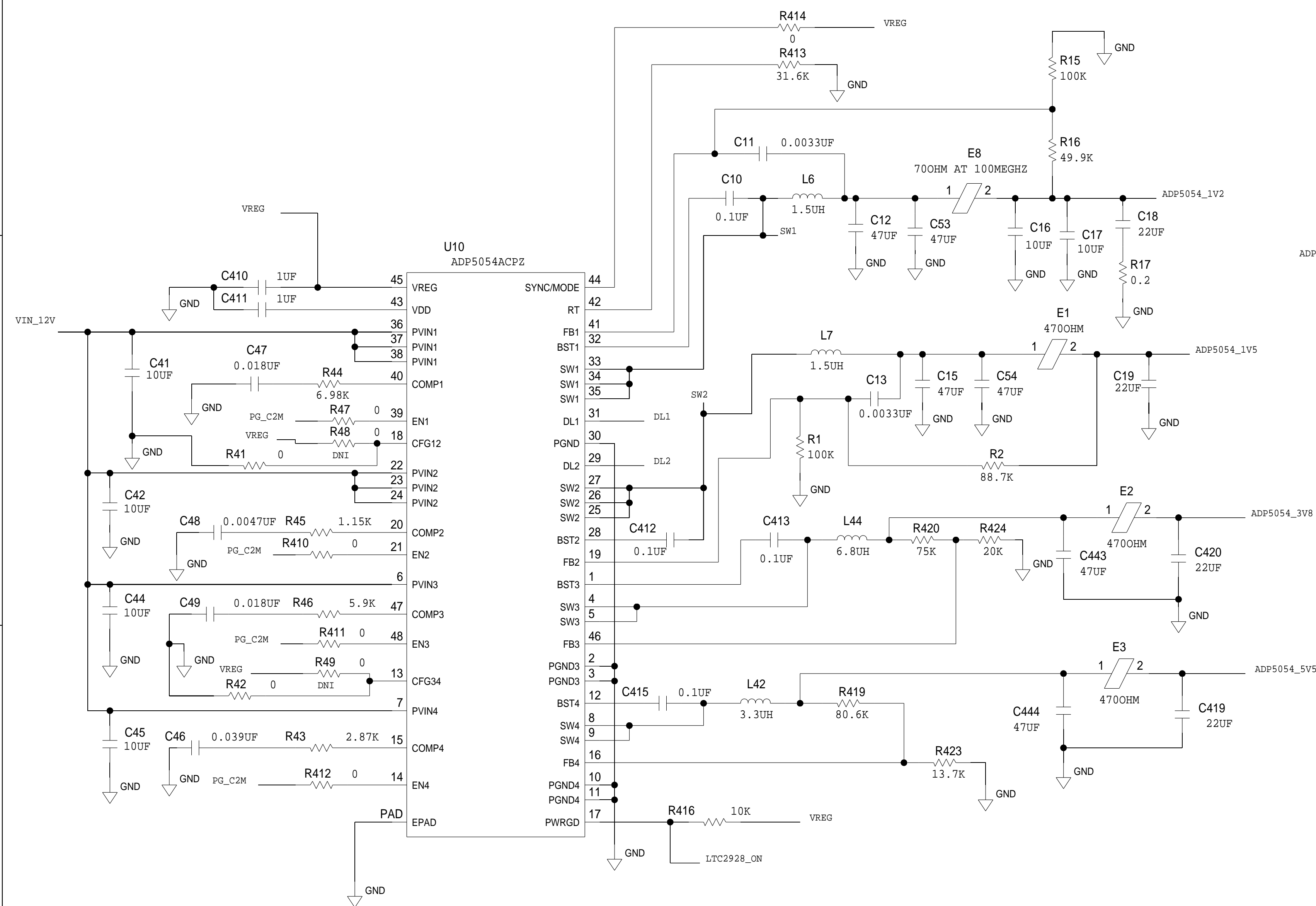
DAC OUTPUT & CLK & POWER SEQUENCE

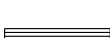
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



ANALOG DEVICES	SCHEMATIC			
	HW TYPE: AD9162-FWC-EBZ Product ID: AD9162-FWC-EBZ			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_046277		REV E	
PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE 1:1	SHEET 3	OF 9

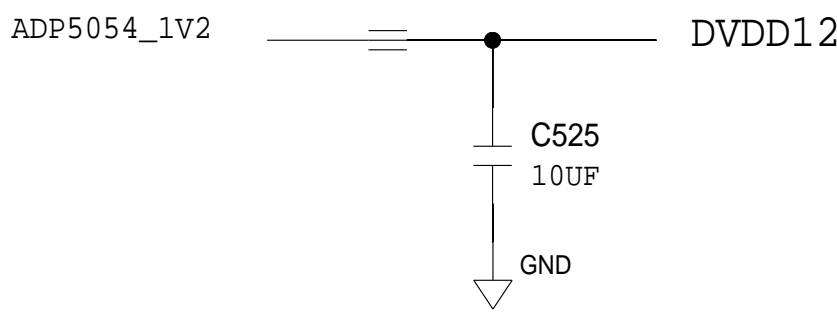
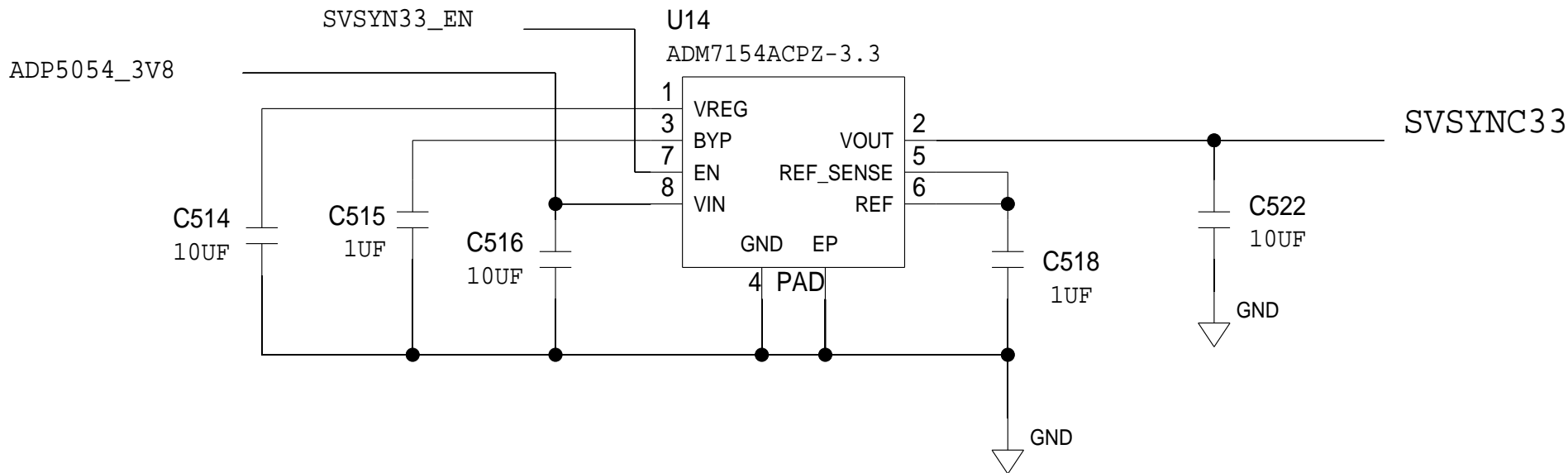
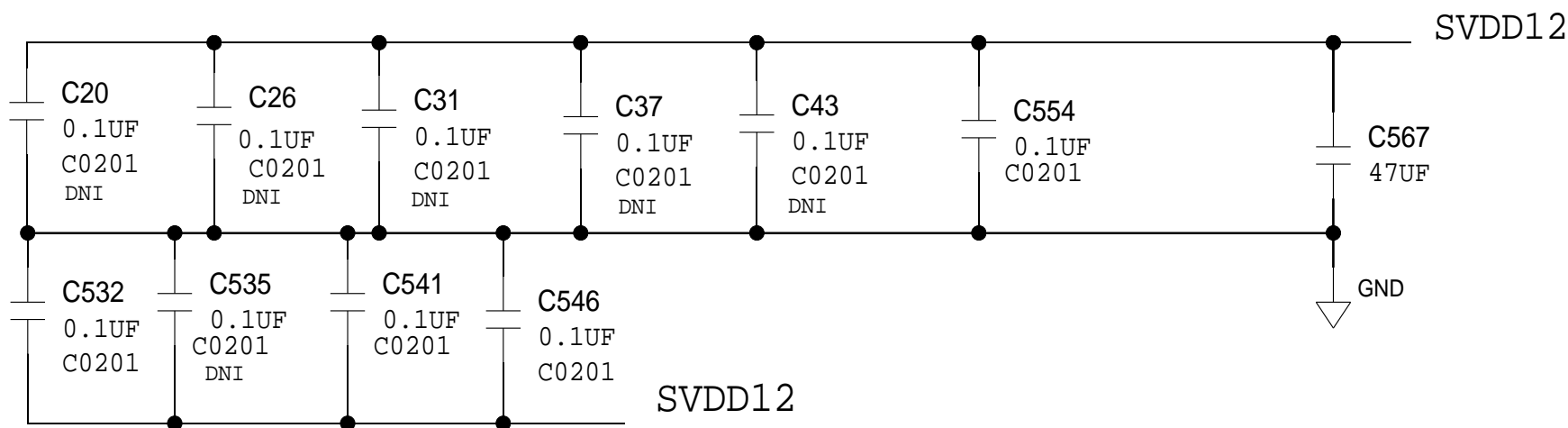
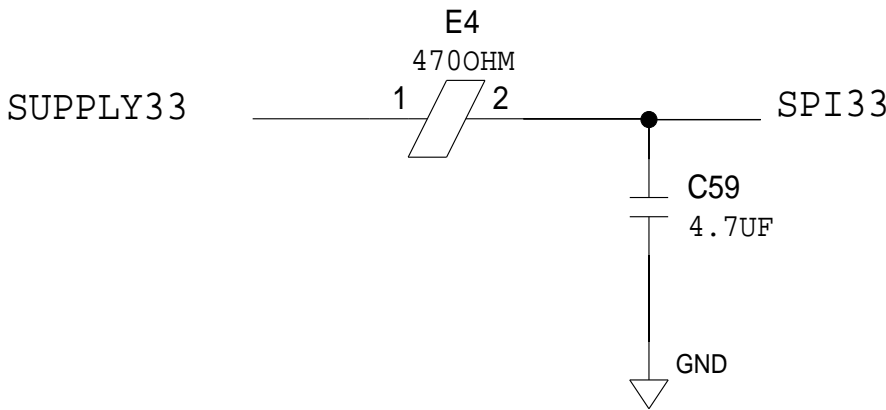
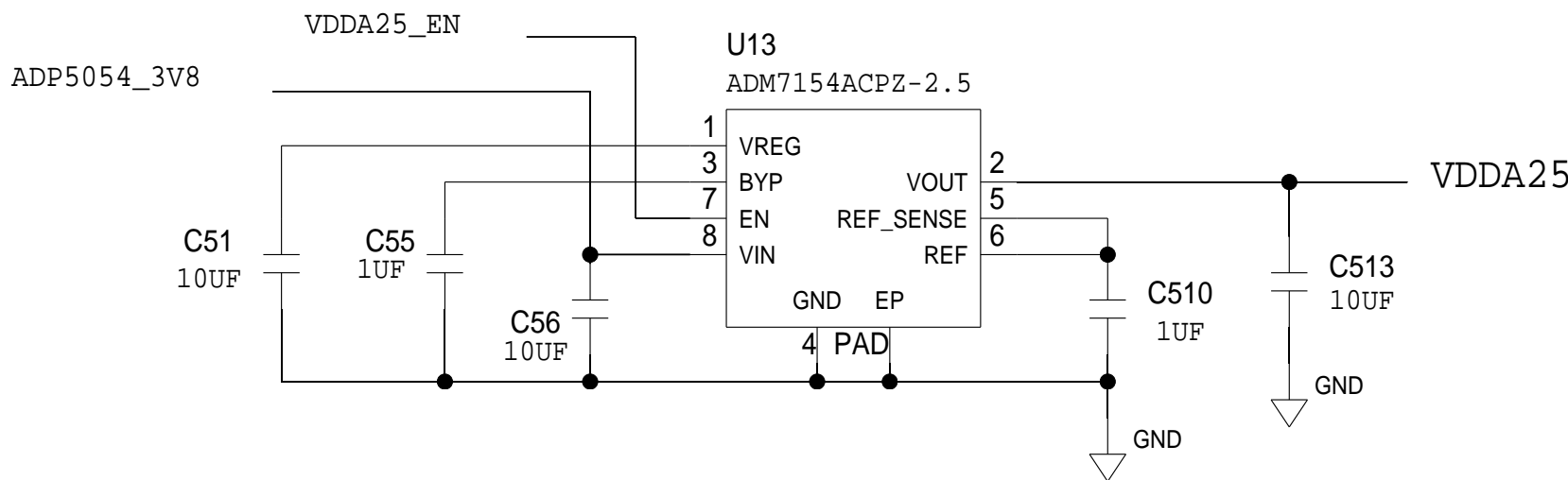
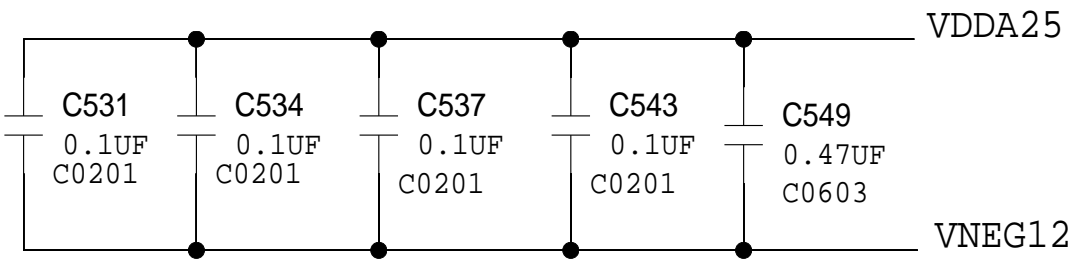
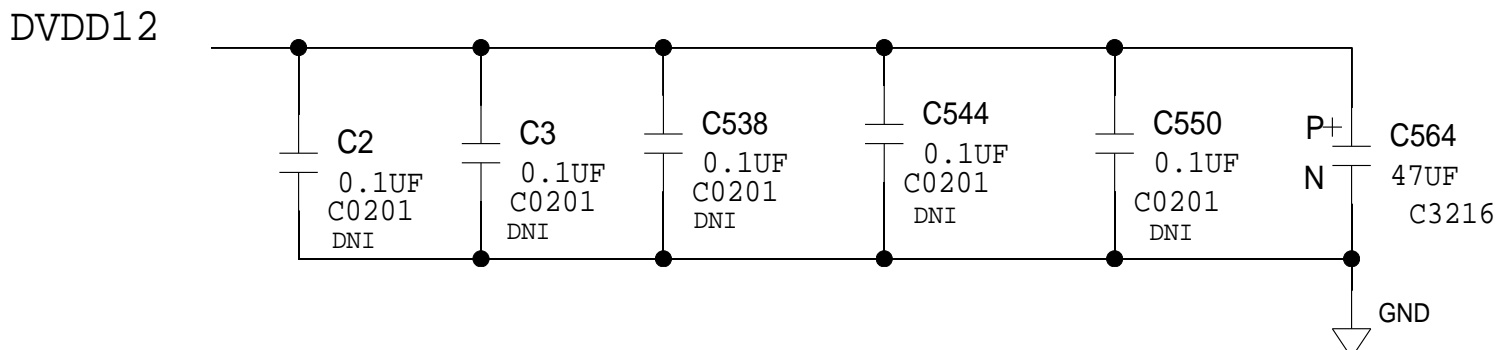
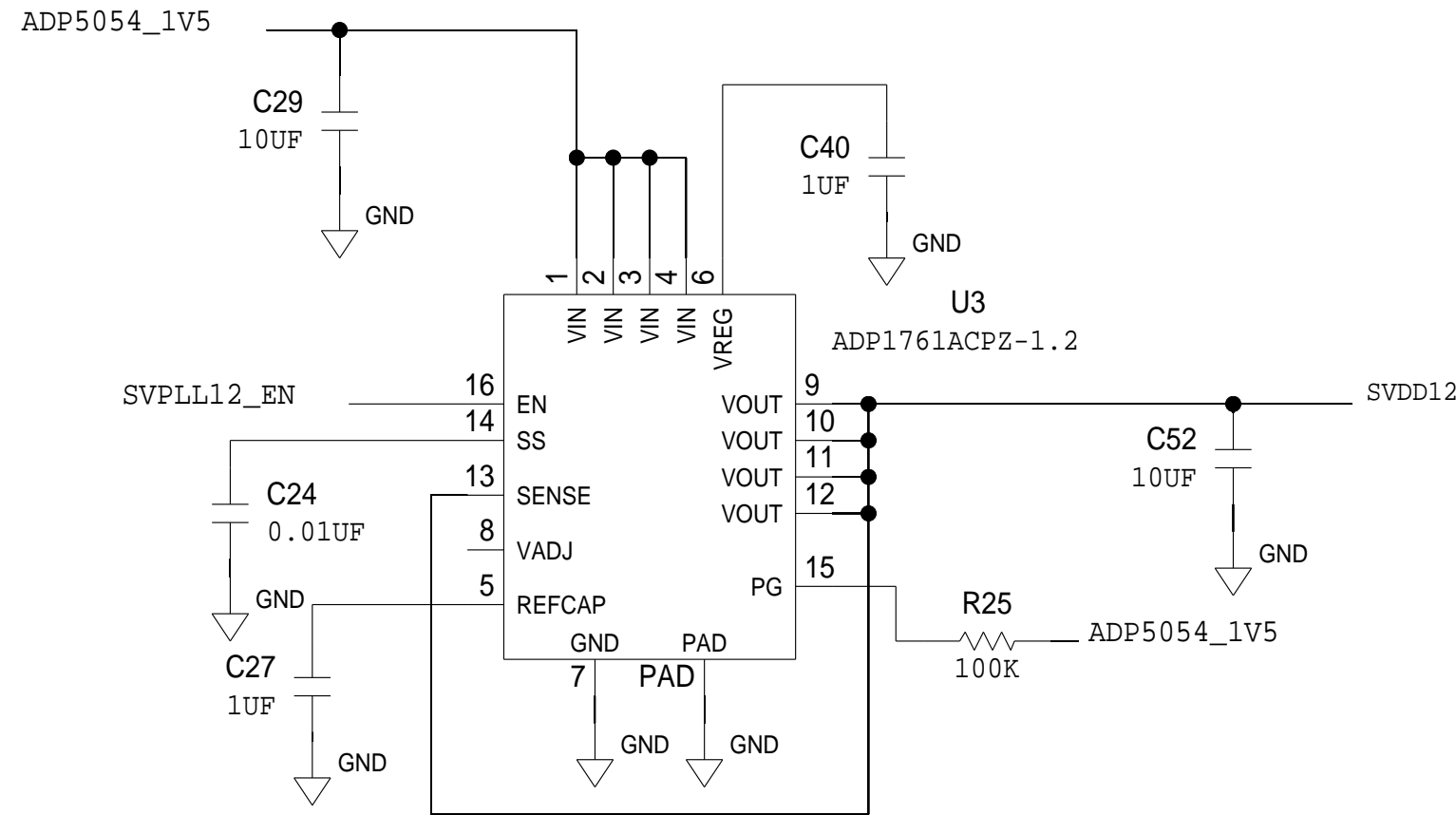
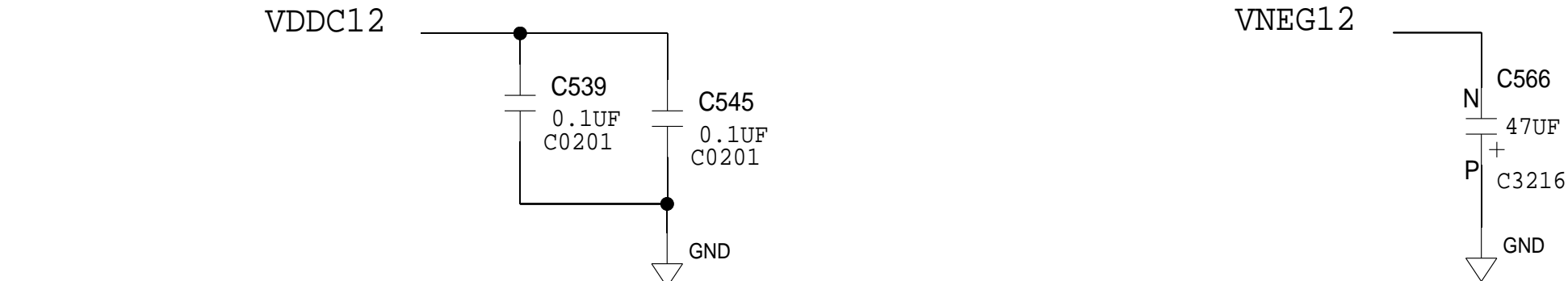
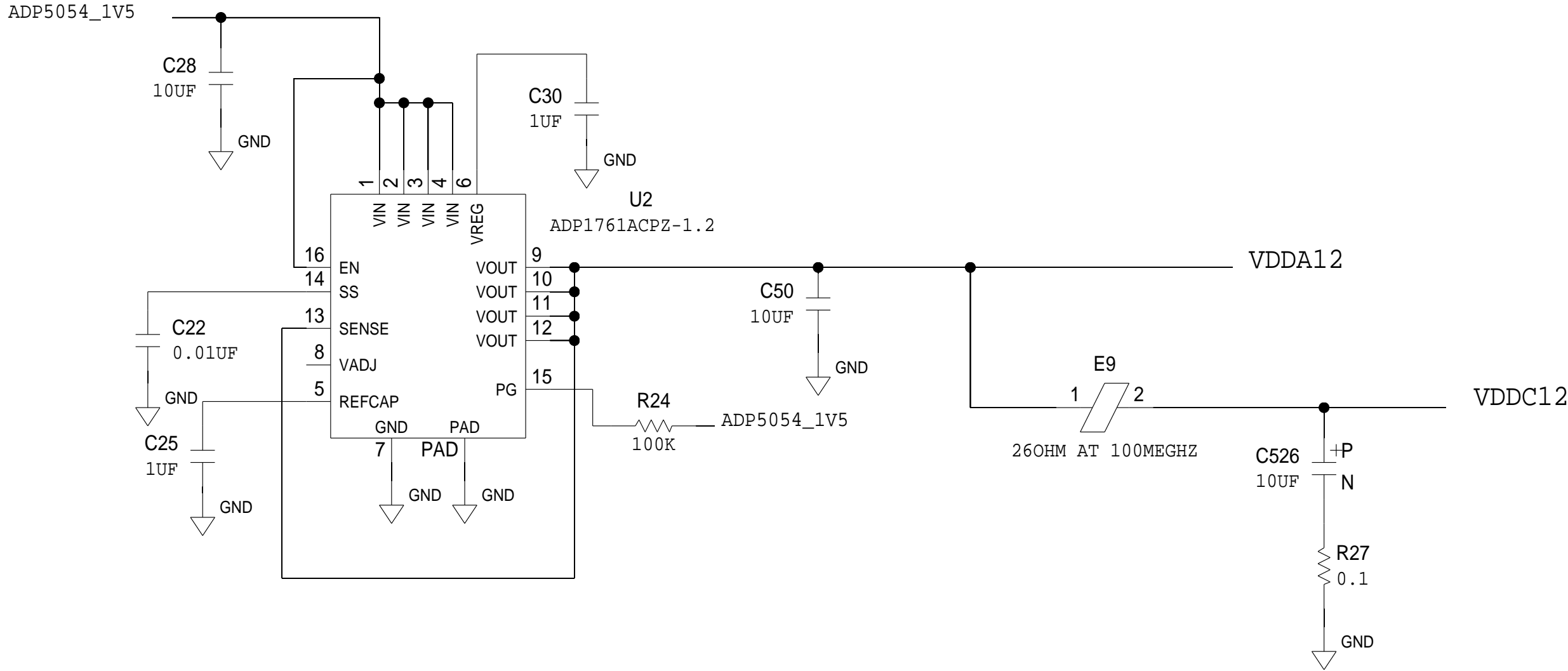
POWER – REGULATOR & NEG LDO



 ANALOG DEVICES	<h1 style="text-align: center;">SCHEMATIC</h1>			
	<p>HW TYPE : Customer Evaluation</p> <p>Product(s) : AD9162-FMC_EBZ</p> <p>: AD9162-FMC-EBZ</p>			
<small>THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED, IN WHOLE OR IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS, OR FOR ANY OTHER PURPOSE, WITHOUT THE WRITTEN CONSENT OF ANALOG DEVICES. THE EQUIPMENT DESIGNATION HEREIN MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY OWNED ANALOG DEVICES.</small>	DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_046277	REV E	
	PDT ENGINEER <PDT_ENGINEER>	SIZE 1:1	SHEET 4	OF 9

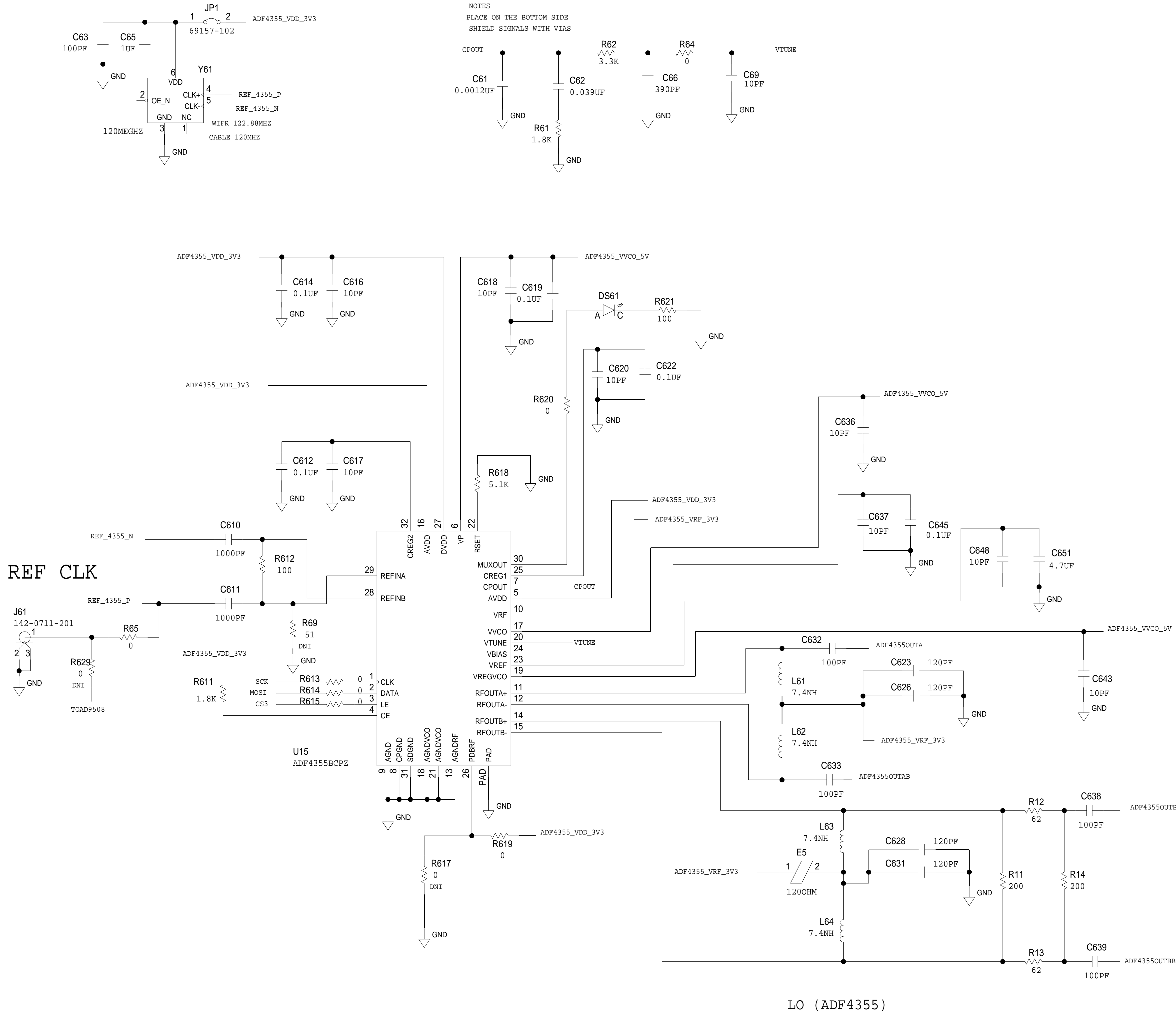
POWER - LDO

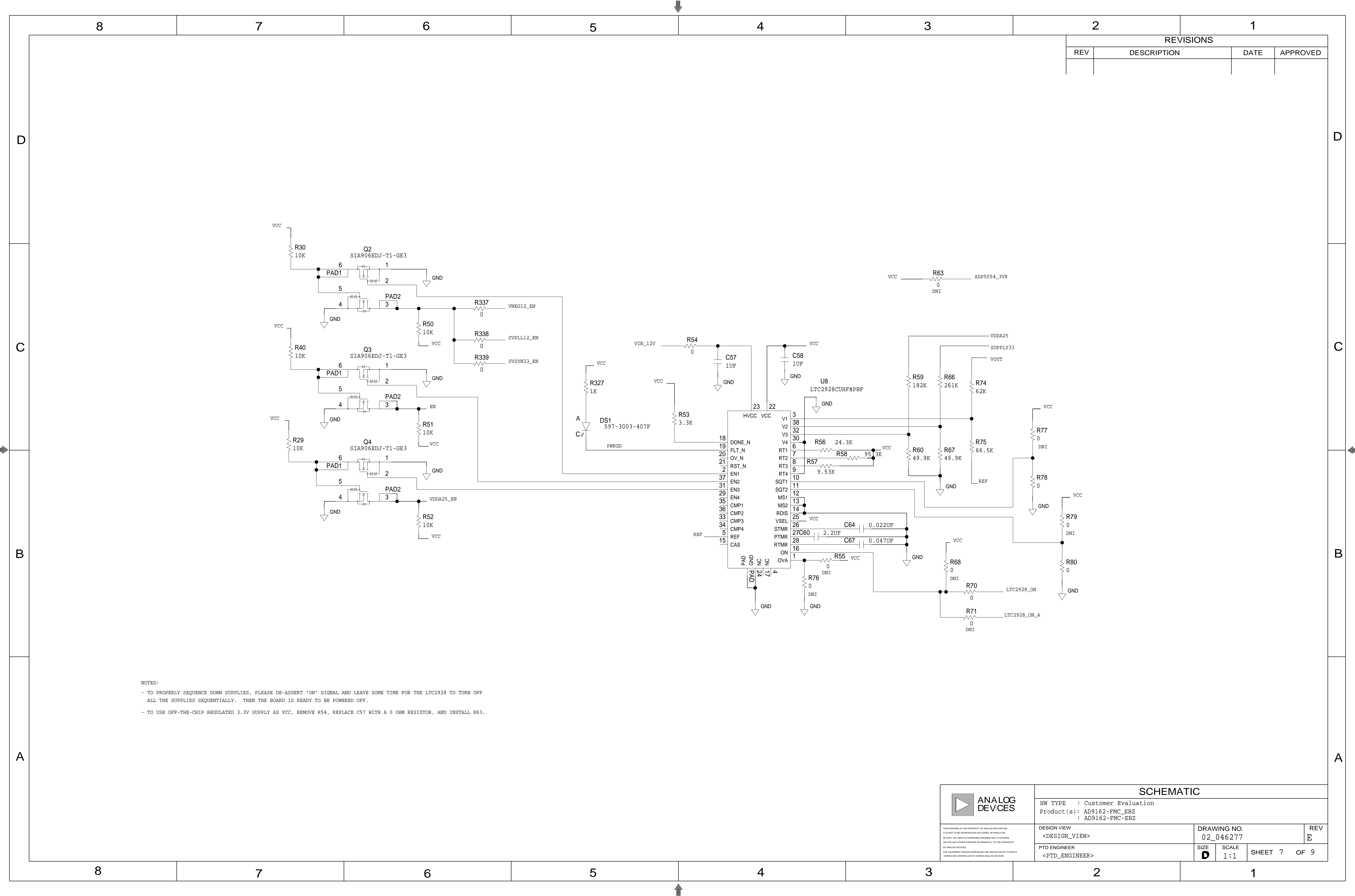
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



SCHEMATIC			
HW TYPE : Customer Evaluation Product(s): AD9162-FMC_EBZ : AD9162-FMC_EBZ			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_046277	REV E	
PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE 1:1	SHEET 5 OF 9

ONBOARD CLK - ADF4355-2 & CLK BUFFER





NOTES:

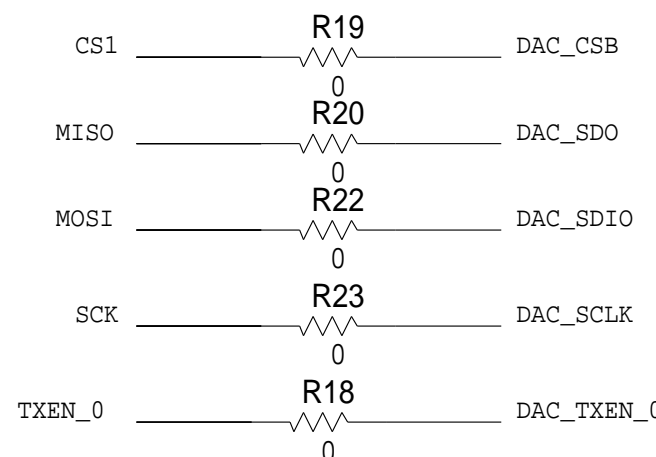
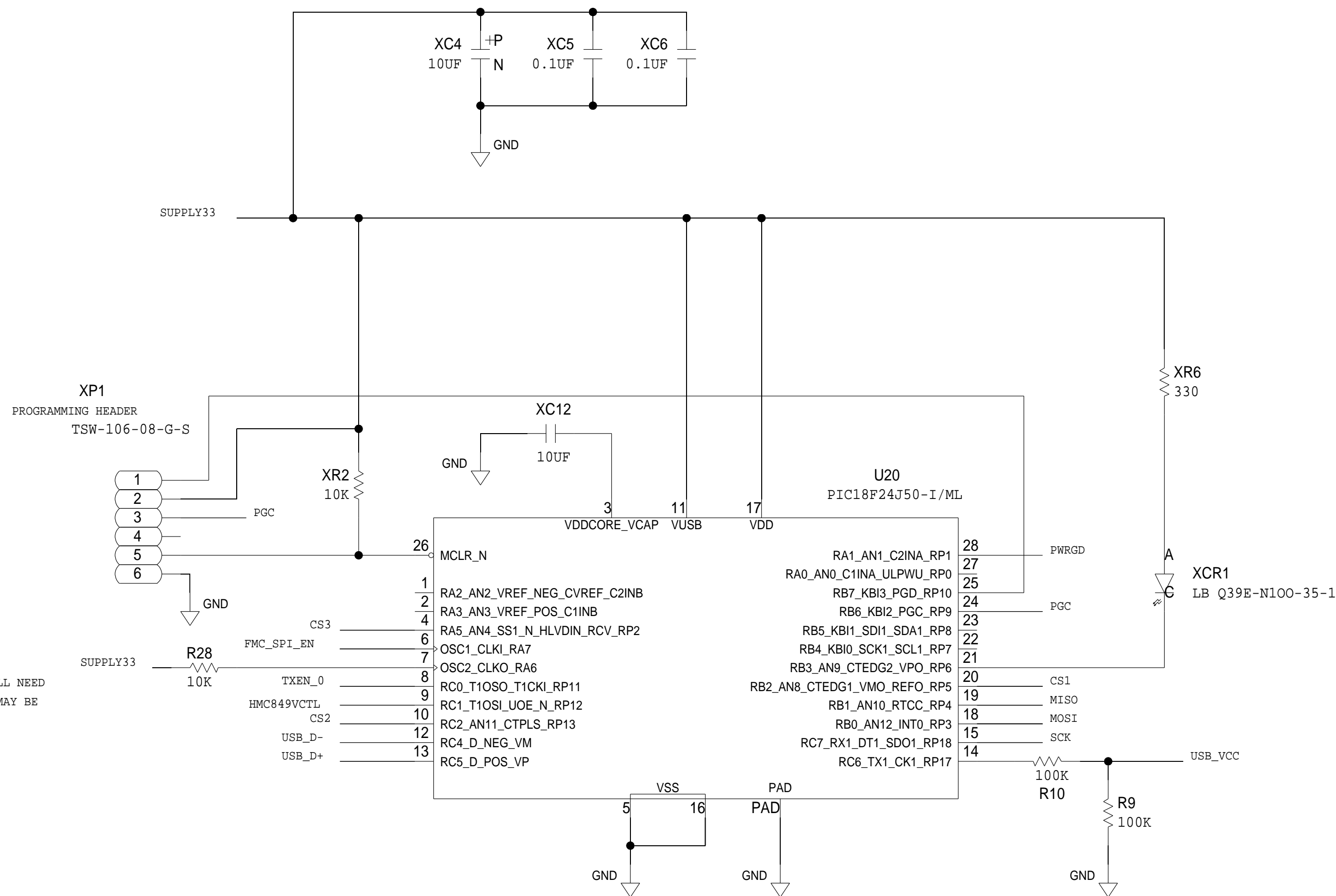
- TO PROPERLY SEQUENCE DOWN SUPPLIES, PLEASE DE-ASSERT "ON" SIGNAL AND LEAVE SOME TIME FOR THE LTC2928 TO TURN OFF ALL THE SUPPLIES SEQUENTIALLY. THEN THE BOARD IS READY TO BE POWERED OFF.
- TO USE OFF-THE-CHIP REGULATED 3.3V SUPPLY AS VCC, REMOVE R54, REPLACE C57 WITH A 0 OHM RESISTOR, AND INSTALL R63.

	SCHEMATIC			
	HW TYPE : Customer Evaluation Product(s): AD9162-FMC_EBZ : AD9162-FMC-EBZ			
	DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_046277		REV E
	PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE 1:1	SHEET 7 OF 9

GPIO & MISC

REVISIONS

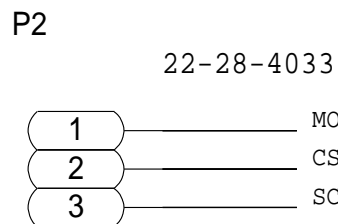
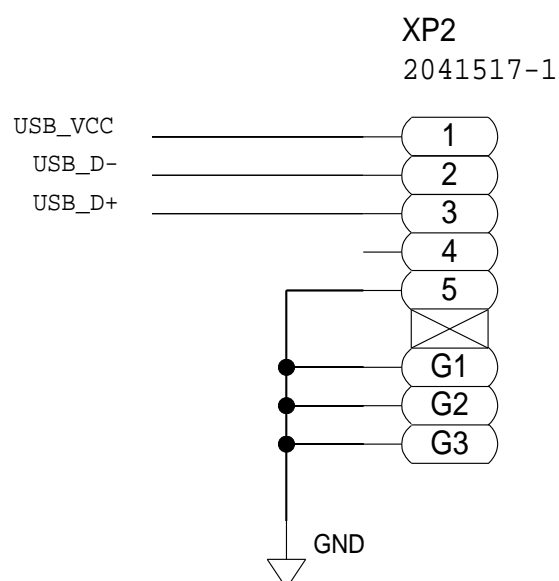
REV	DESCRIPTION	DATE	APPROVED
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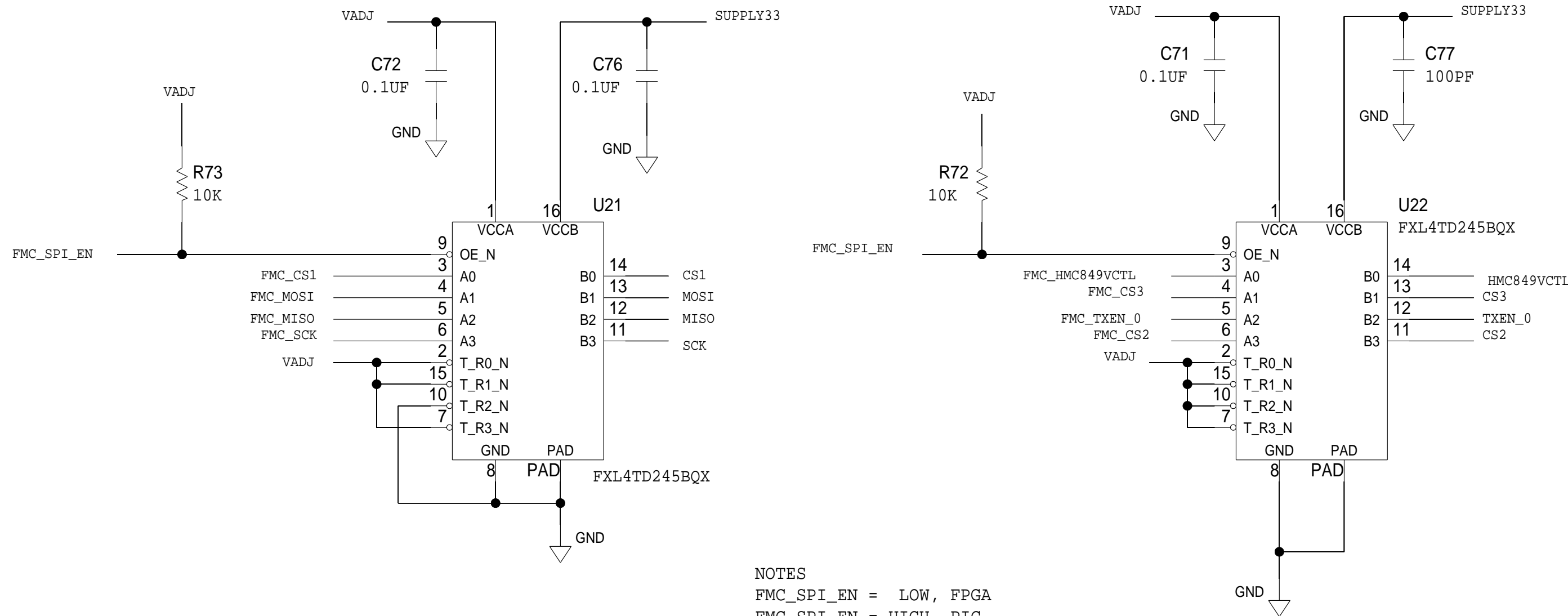
NOTE:

IF USB CABLE IS PLUGGED IN PIC IS THE SOURCE OF SPI BUS AND TXEN SIGNALS

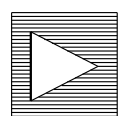
IF USB IS UNPLUGGED FMC INTERFACE IS THE SOURCE OF SPI BUS AND TXEN SIGNAL.



CONNECTIONS WILL PASS RIGHT THROUGH THE CONNECTOR



NOTES
FMC_SPI_EN = LOW, FPGA
FMC_SPI_EN = HIGH, PIC



ANALOG
DEVICES

SCHEMATIC

HW TYPE : Customer Evaluation
Product(s): AD9162-FMC_EBZ
: AD9162-FMC-EBZ

DESIGN VIEW
<DESIGN_VIEW>

PTD ENGINEER
<PTD_ENGINEER>

DRAWING NO.
02_046277

SIZE
D

SCALE
1:1

SHEET 8 OF 9

REV
E

