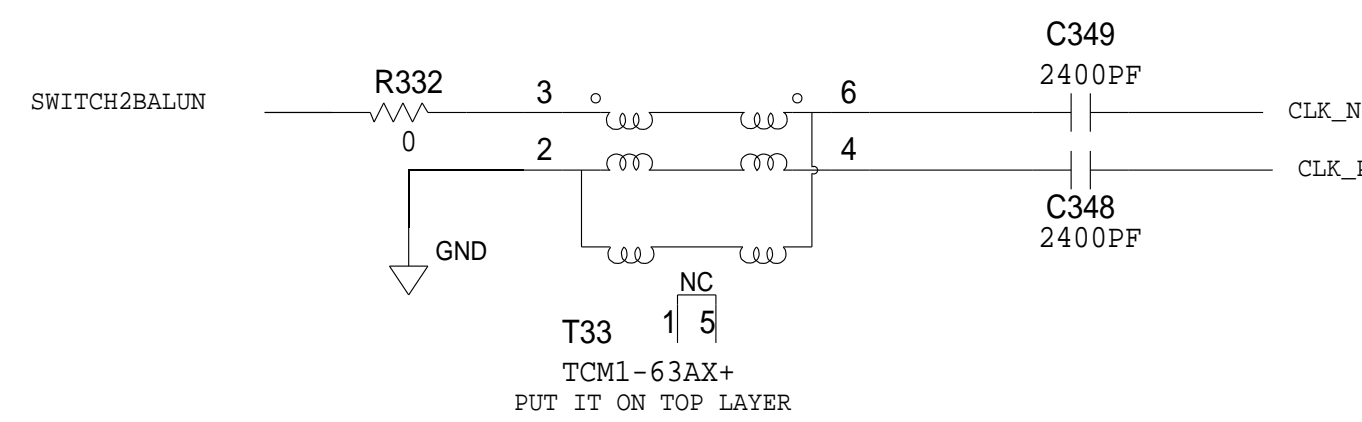


REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



J32
142-0761-841

1
2
3
4

SA_GND

OUT

C14
1000PF

50 OHM SINGLE-ENDED

3
2

TCM1-83X+ FOR 10M TO 8GHZ

50 OHM DIFFERENTIAL

5
4

TCM1-83X+
T1

NC
6
1

VDDA25

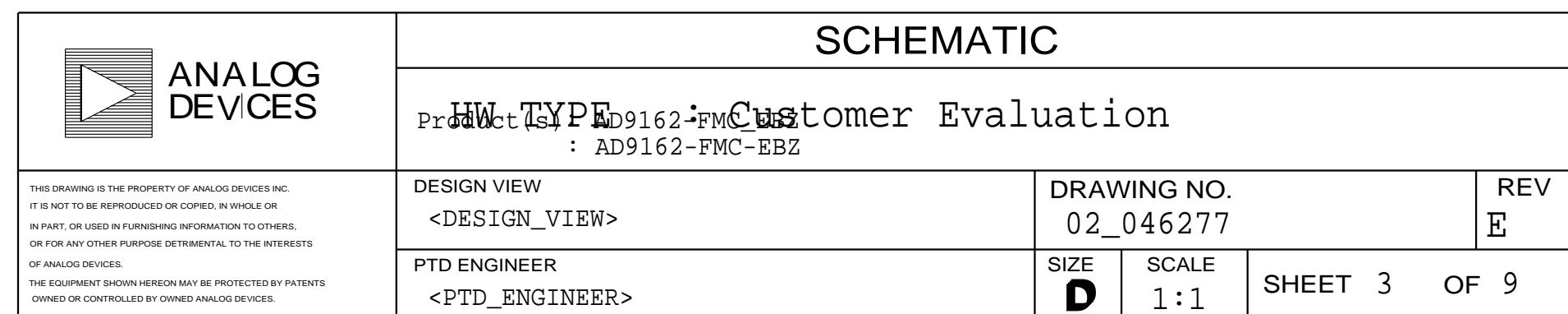
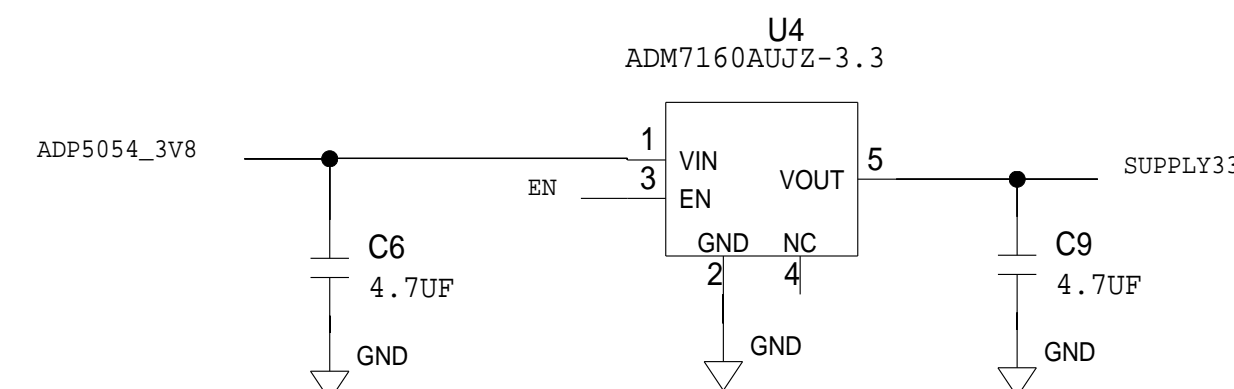
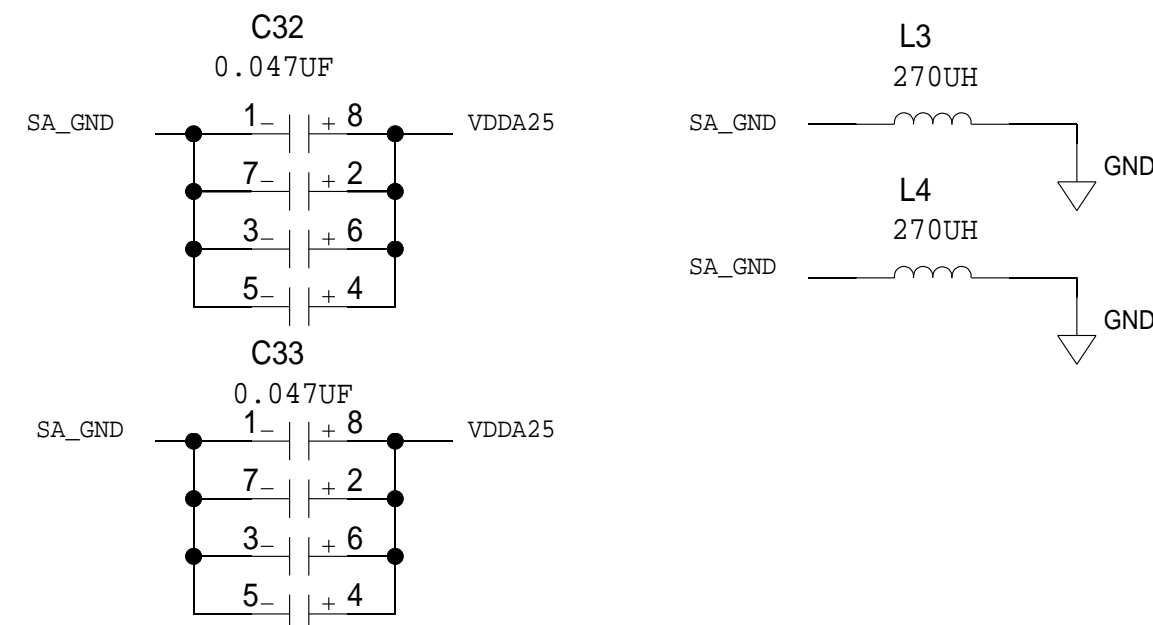
R33
49.9

OUT_N

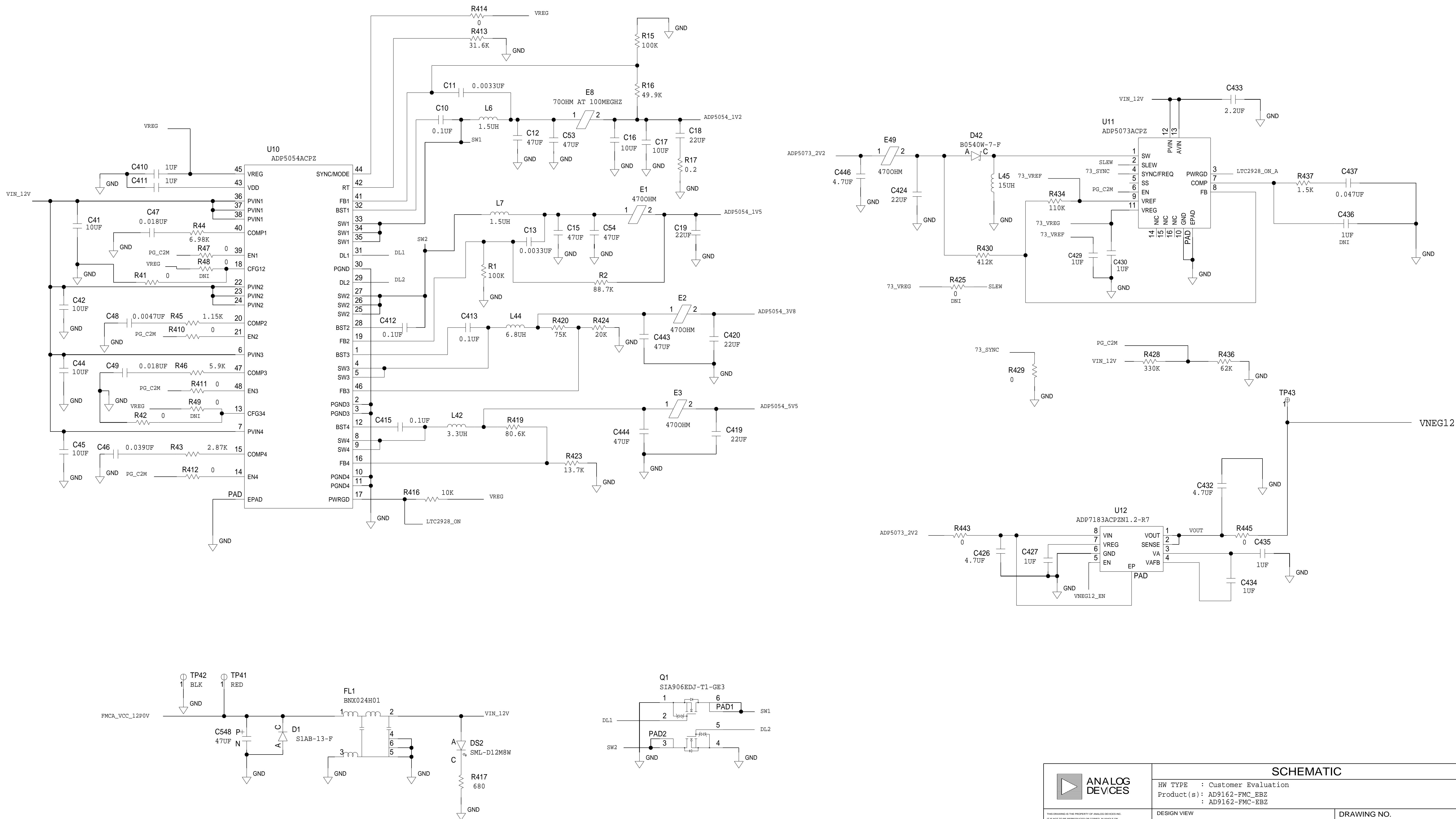
OUT_P

R34
49.9

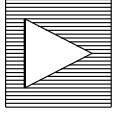
VDDA25



POWER - REGULATOR & NEG LDO

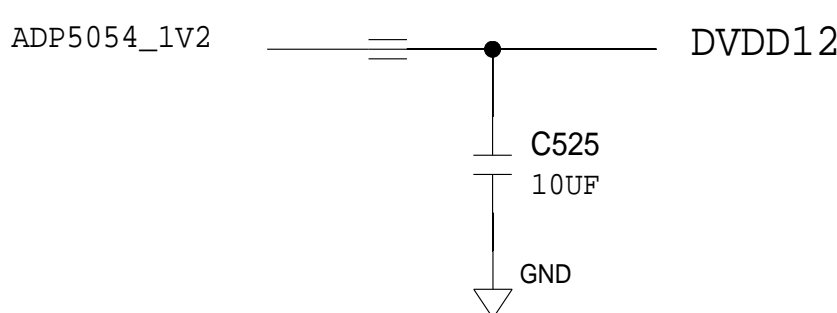
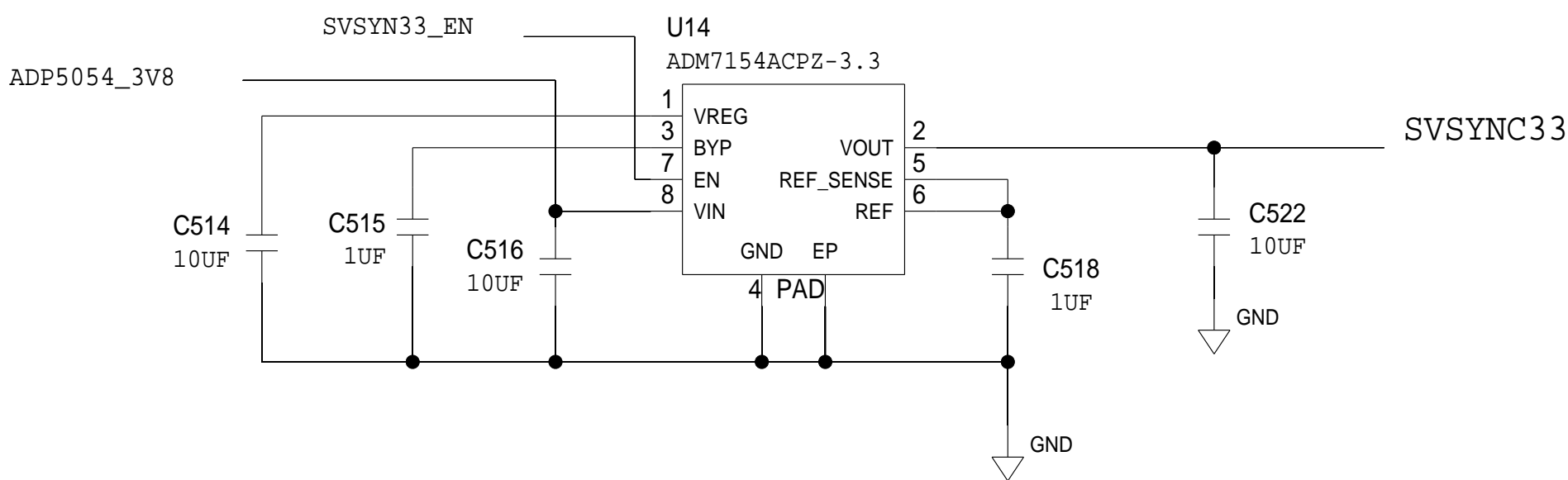
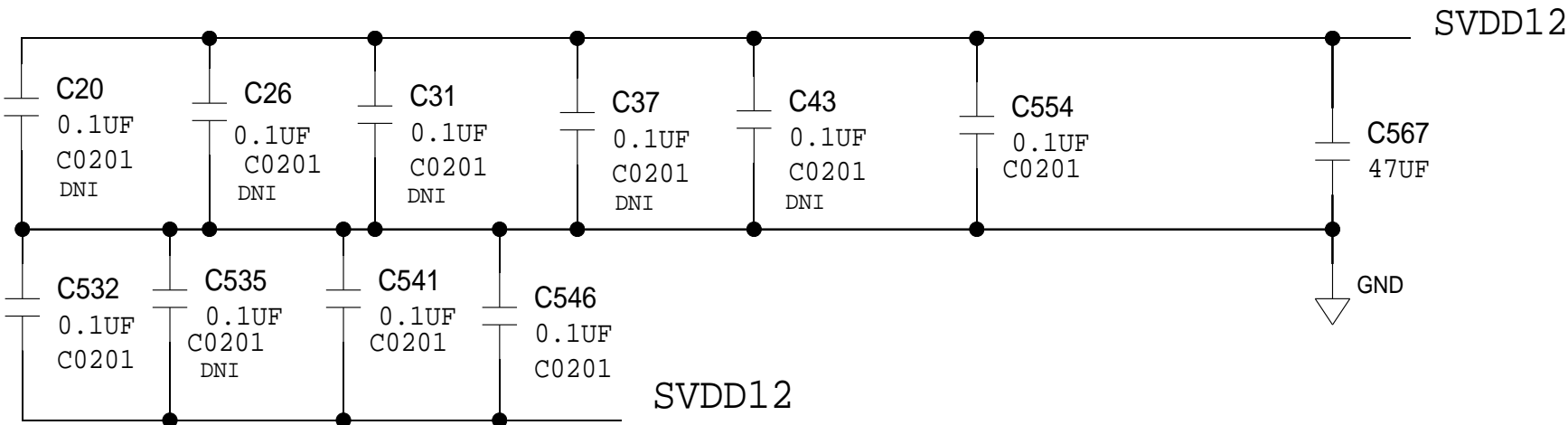
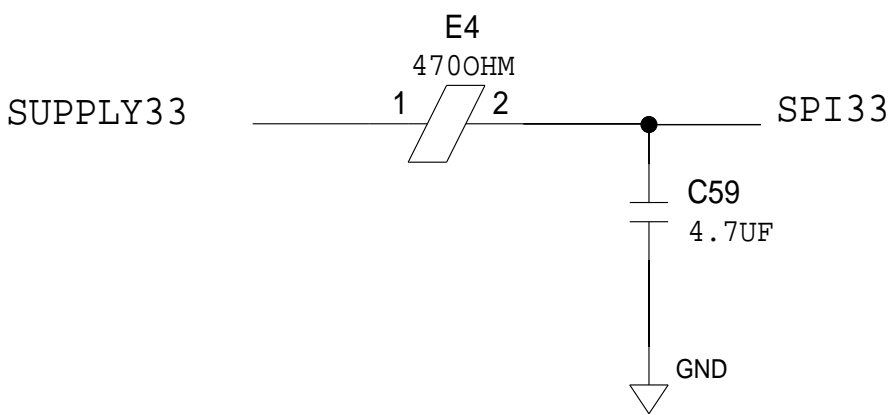
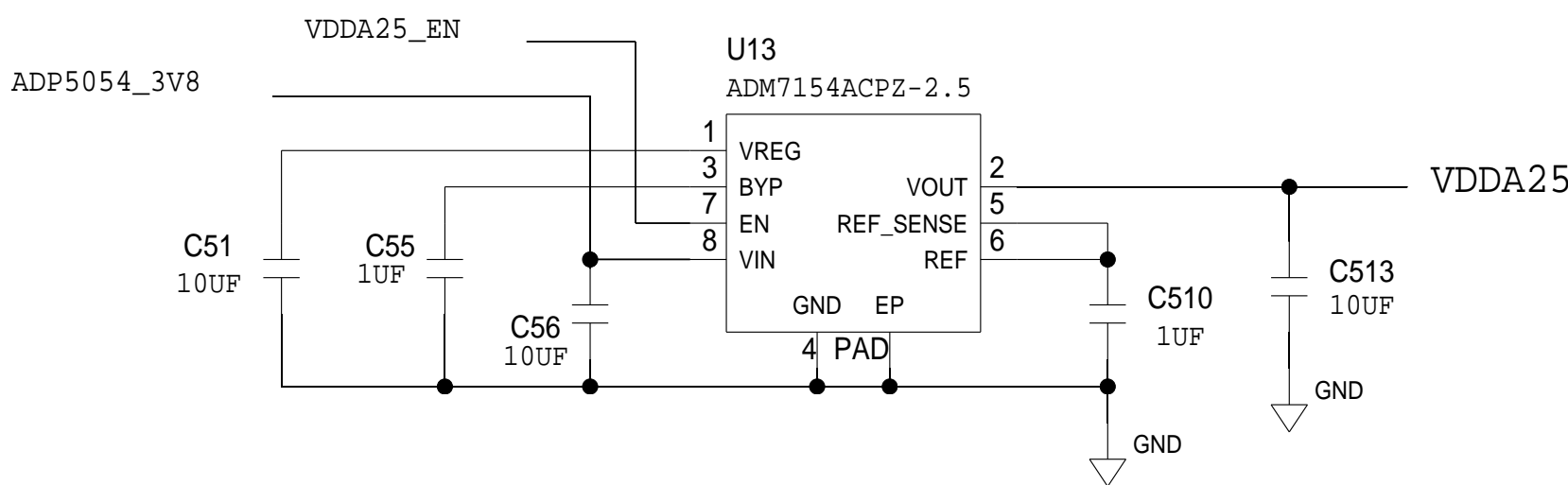
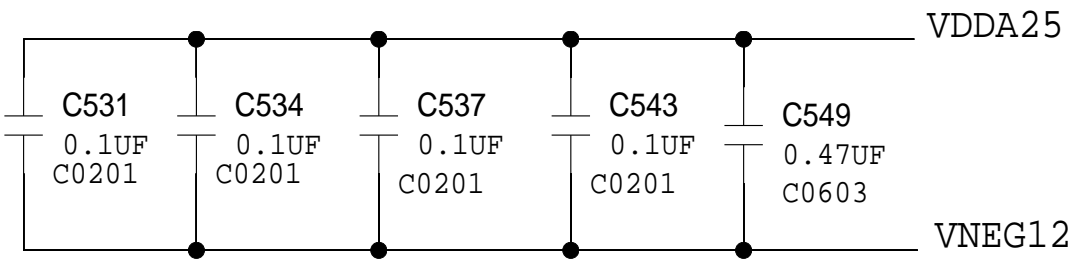
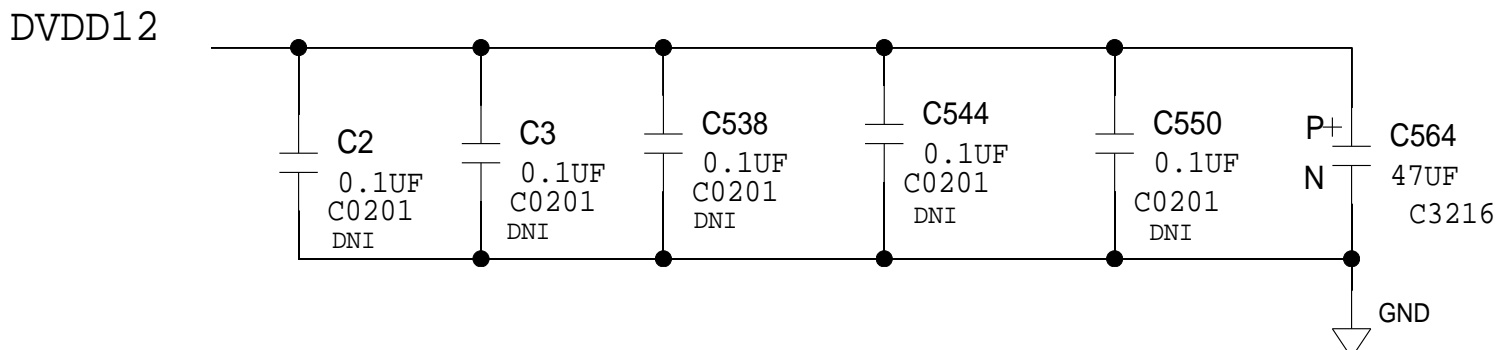
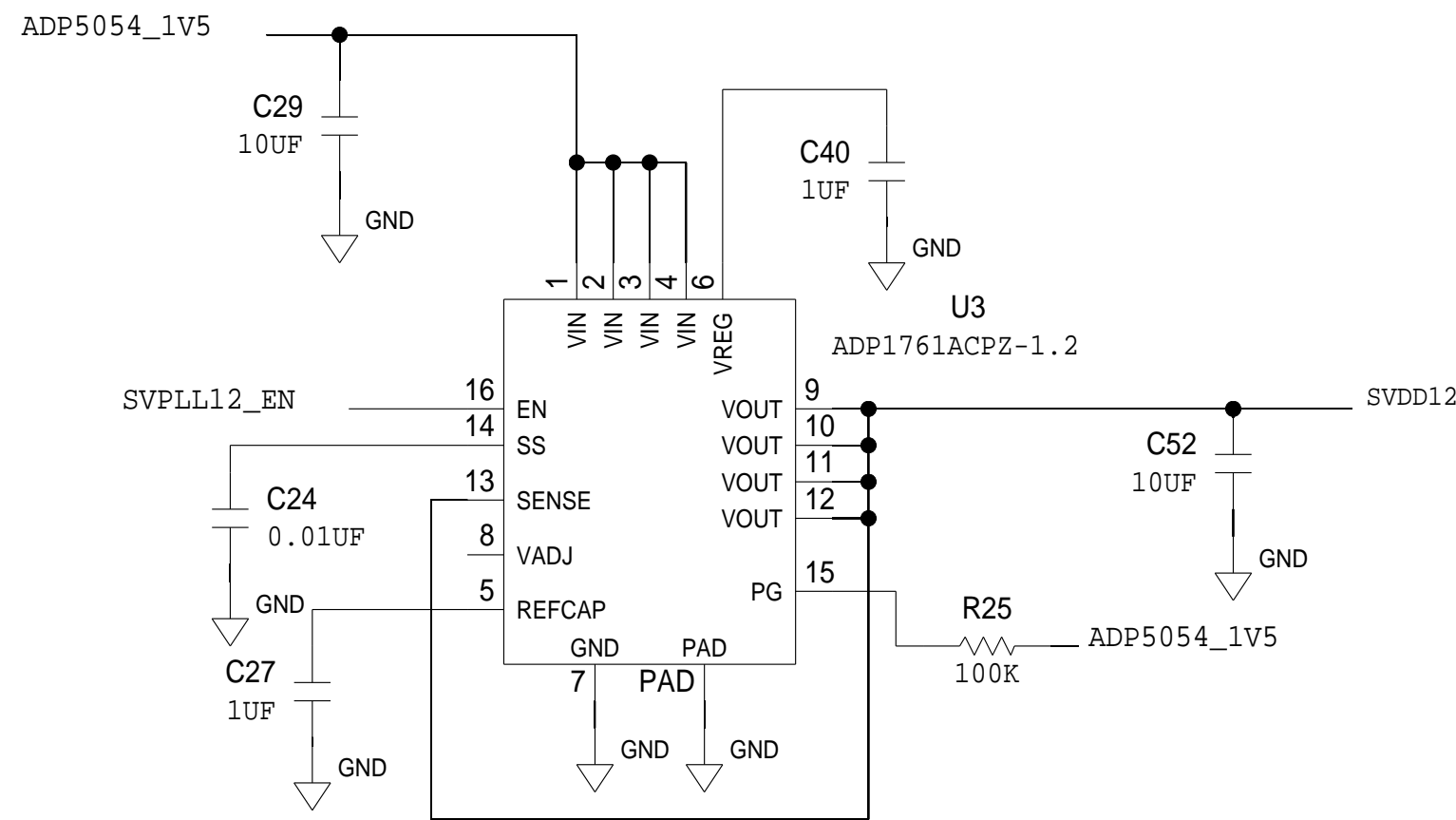
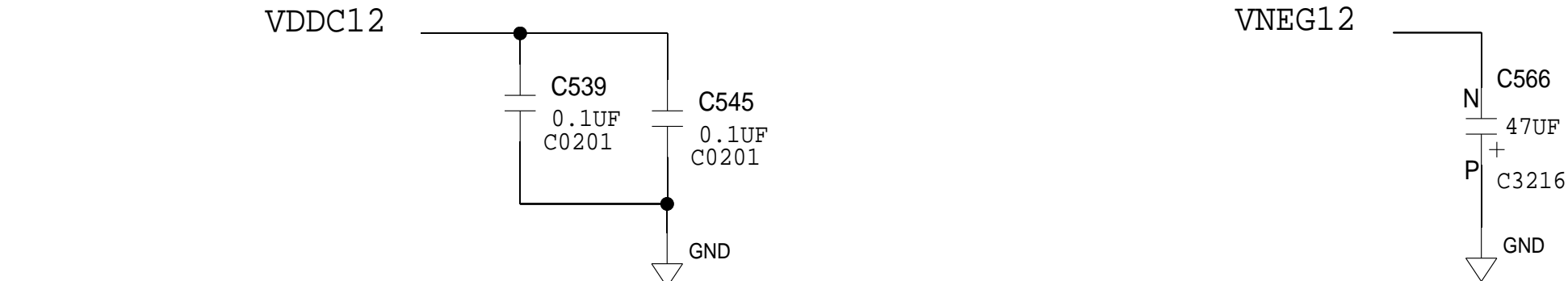
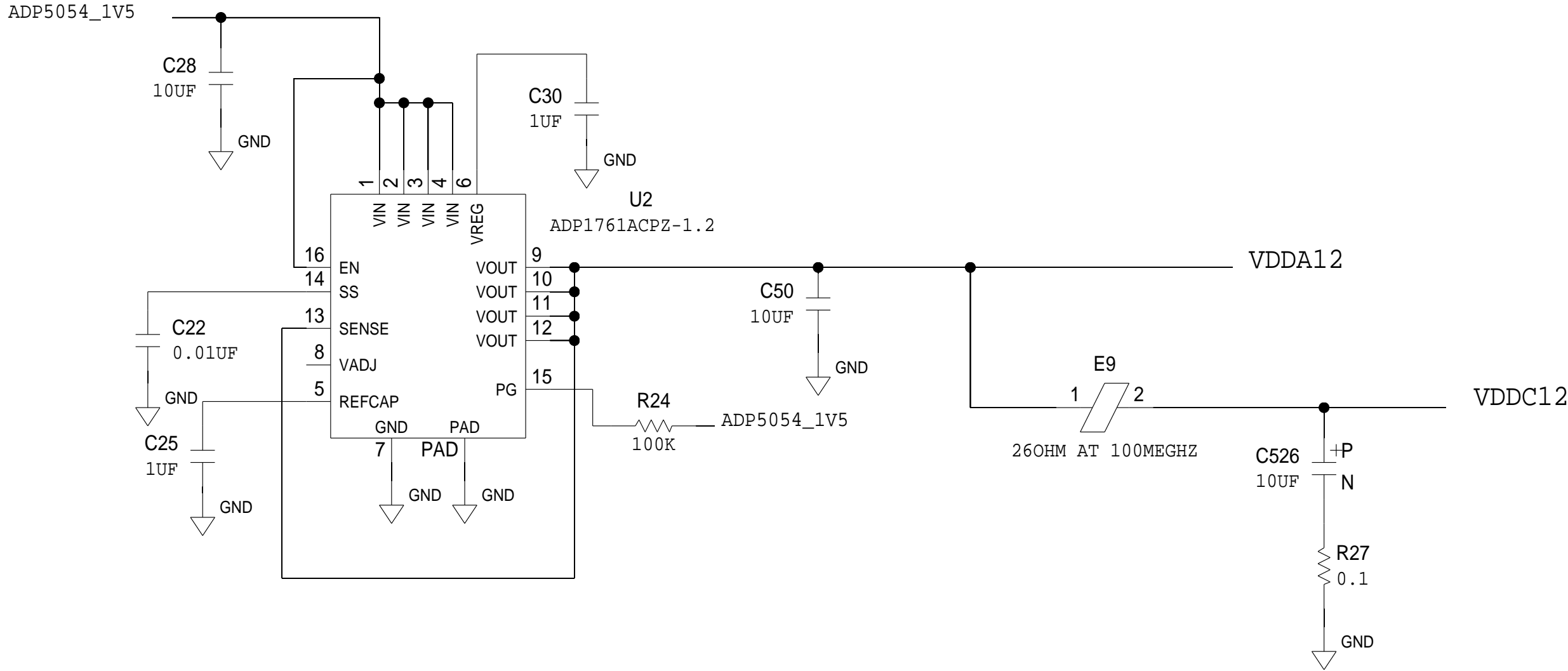


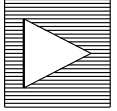
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

 ANALOG DEVICES		SCHEMATIC	
HW TYPE : Customer Evaluation Product(s) : AD9162-FMC_EBZ : AD9162-FMC_EBZ		DESIGN VIEW <DESIGN_VIEW>	
PTD ENGINEER <PTD_ENGINEER>		DRAWING NO. 02_046277	REV E
SIZE D	SCALE 1:1	SHEET 4	OF 9

POWER - LDO

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



**ANALOG
DEVICES**

SCHEMATIC

HW TYPE : Customer Evaluation
Product(s): AD9162-FMC_EBZ
: AD9162-FMC-EBZ

DESIGN VIEW
<DESIGN_VIEW>

PTD ENGINEER
<PTD_ENGINEER>

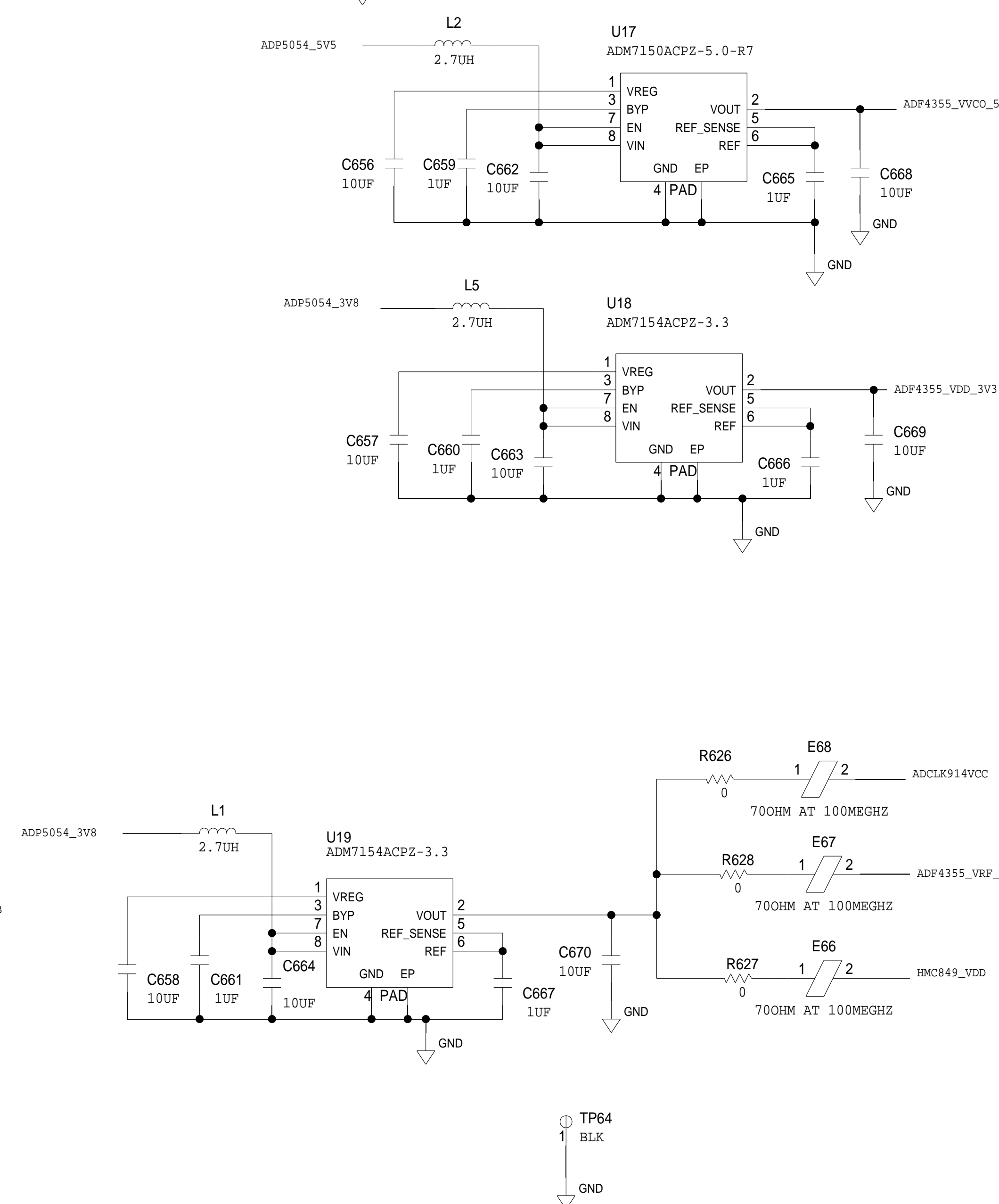
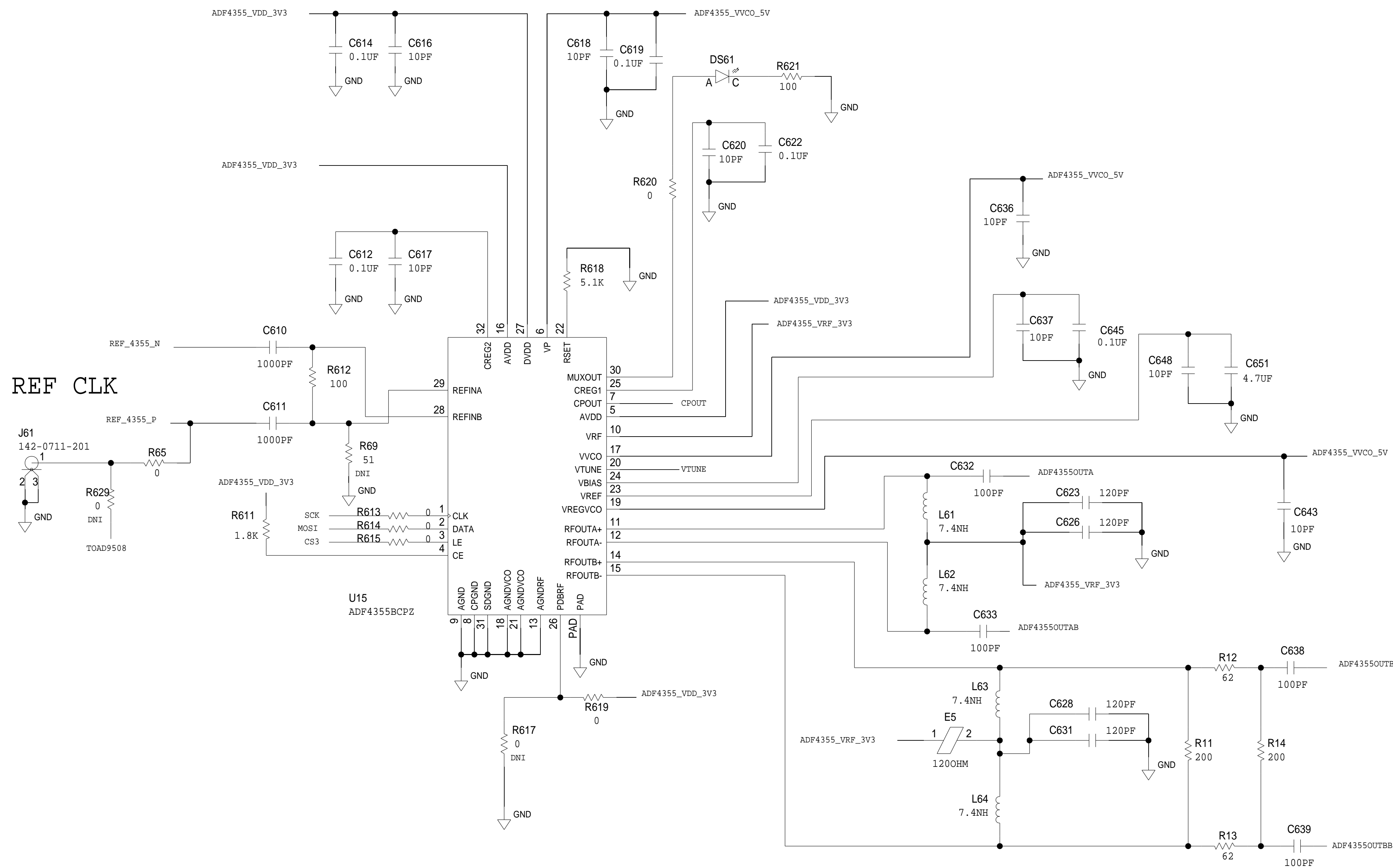
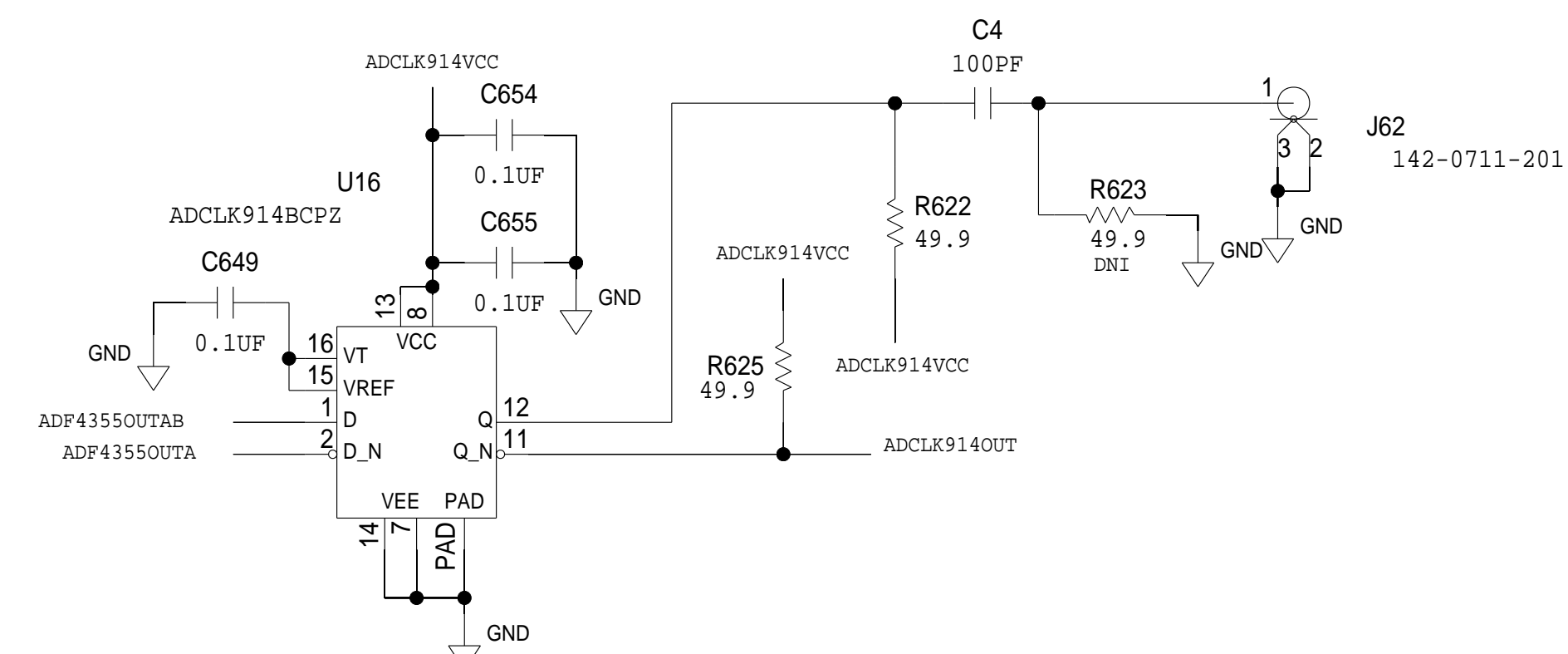
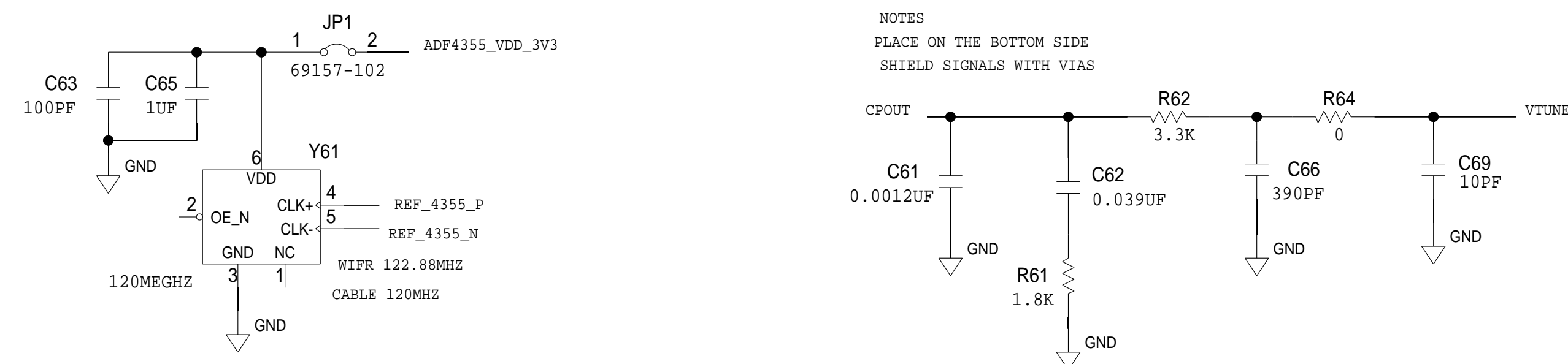
DRAWING NO.
02_046277

SIZE **D**
SCALE 1:1

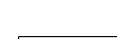
REV
E

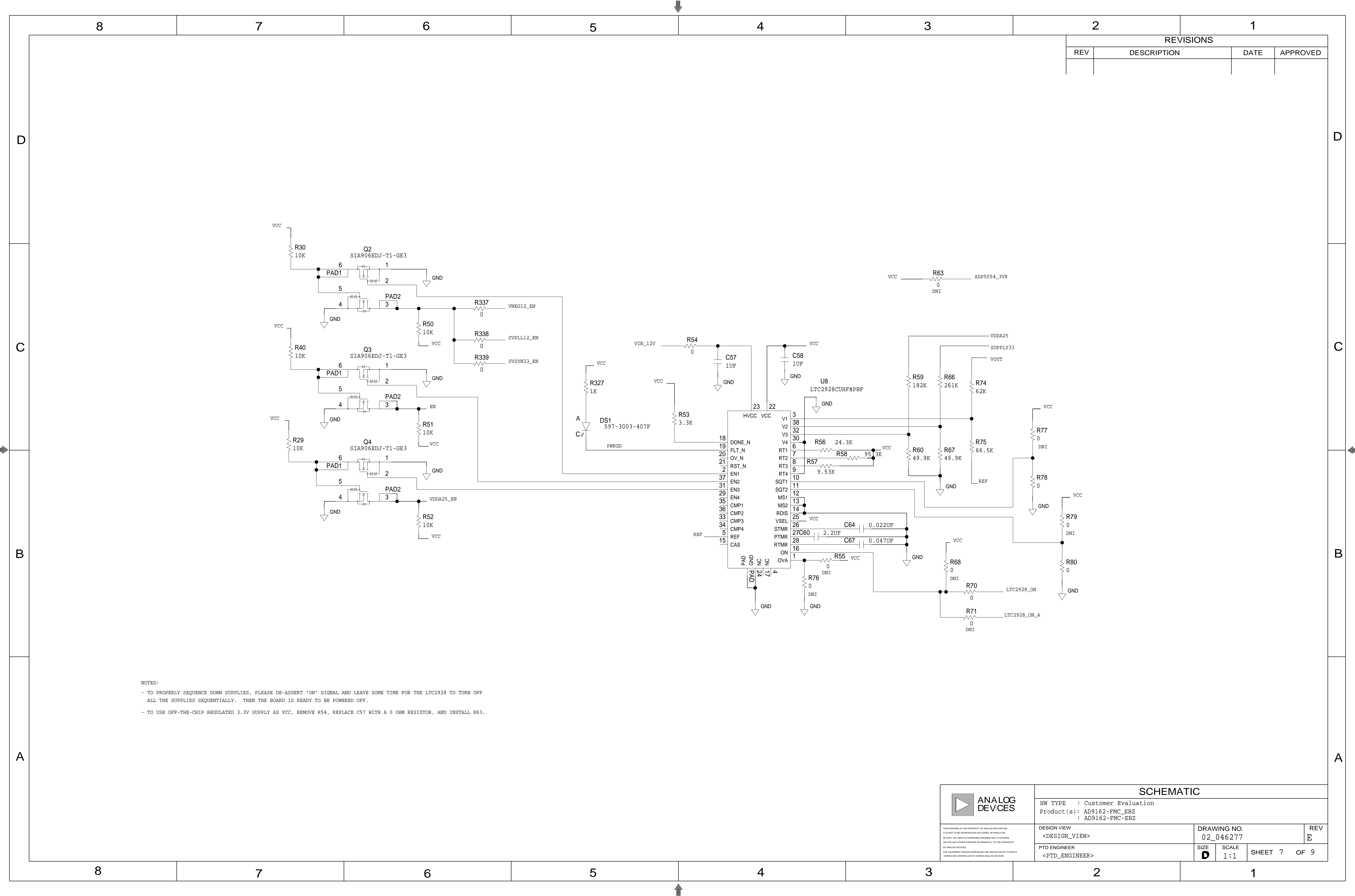
SHEET 5 OF 9

ONBOARD CLK - ADF4355-2 & CLK BUFFER




LO (ADF4355)

 <div>ANALOG DEVICES</div>	<div>SCHEMATIC</div>				
	HW TYPE : Customer Evaluation Product(s) : AD9162-FMC_EBZ : AD9162-FMC-EBZ				
<div><div>THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED, IN WHOLE OR IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS, OR FOR ANY OTHER PURPOSES DETERMINED TO BE IN THE INTERESTS OF ANALOG DEVICES.</div><div>THE EQUIPMENT DESIGNATION SHOWN ON THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES OR CONTROLLED BY OWNED ANALOG DEVICES.</div></div>	DESIGN VIEW <DESIGN_VIEW>		DRAWING NO. 02_046277		REV E
	PTD ENGINEER <PTD_ENGINEER>		SIZE D	SCALE 1:1	SHEET 6 OF 9



NOTES:

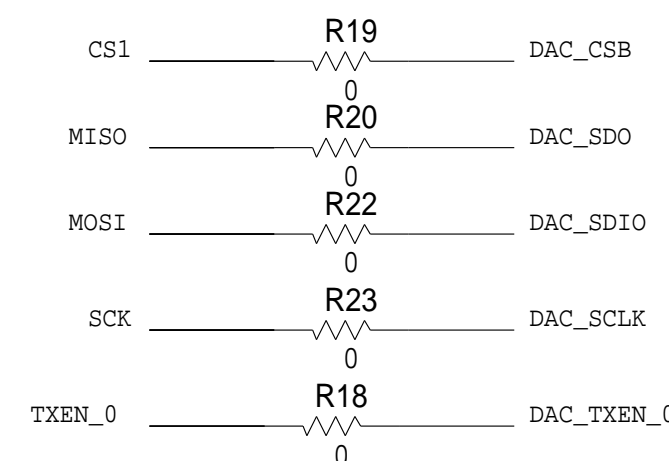
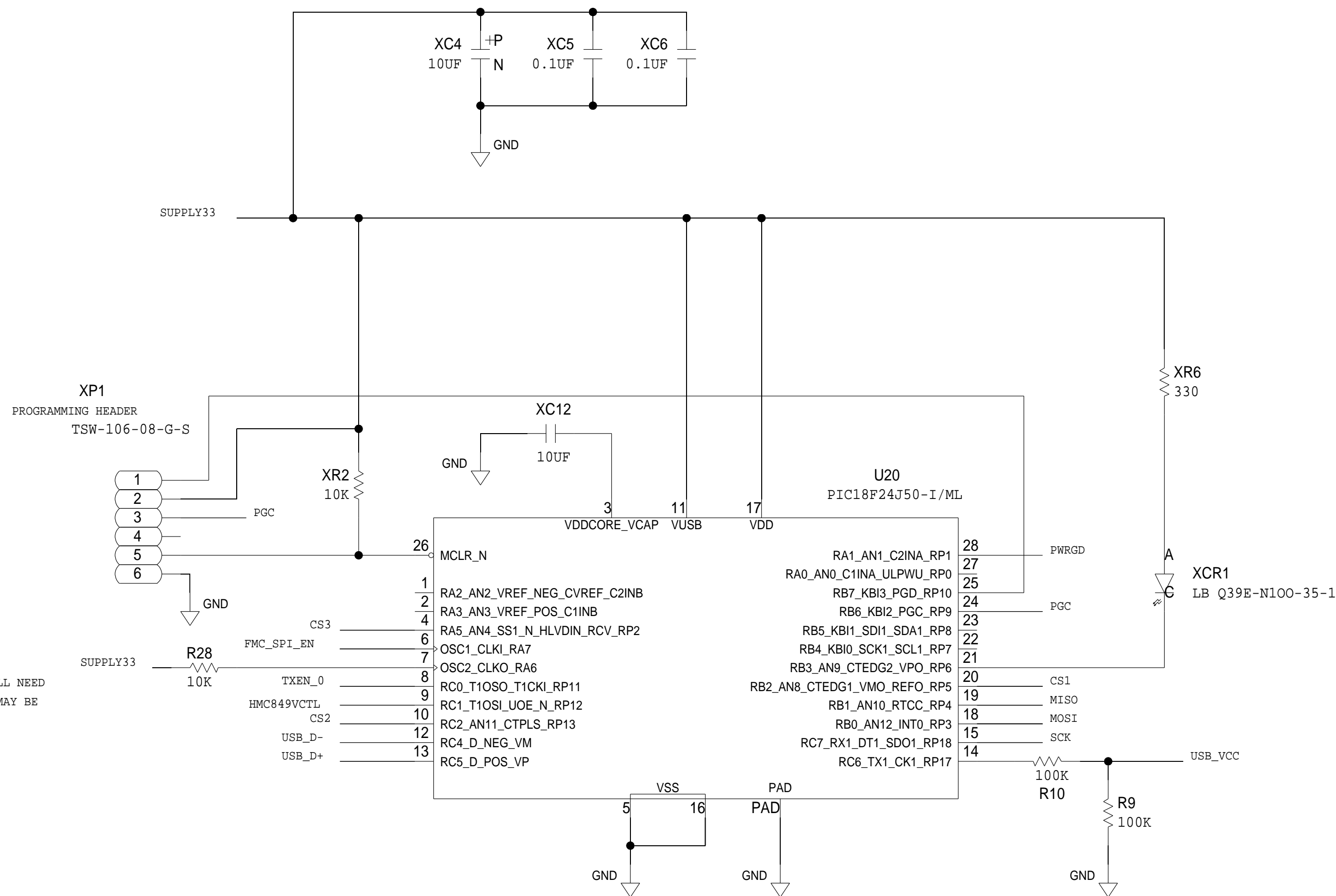
- TO PROPERLY SEQUENCE DOWN SUPPLIES, PLEASE DE-ASSERT "ON" SIGNAL AND LEAVE SOME TIME FOR THE LTC2928 TO TURN OFF ALL THE SUPPLIES SEQUENTIALLY. THEN THE BOARD IS READY TO BE POWERED OFF.
- TO USE OFF-THE-CHIP REGULATED 3.3V SUPPLY AS VCC, REMOVE R54, REPLACE C57 WITH A 0 OHM RESISTOR, AND INSTALL R63.

 <div>ANALOG DEVICES</div>	SCHEMATIC			
	HW TYPE : Customer Evaluation Product(s) : AD9162-FMC_EBZ : AD9162-FMC-EBZ			
THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED, IN WHOLE OR IN PART, OR USED IN FORWARDING INFORMATION TO OTHERS, OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS OF ANALOG DEVICES. THE EQUIPMENT DESIGN HEREON MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY OWNED ANALOG DEVICES.	DESIGN VIEW <div><DESIGN_VIEW></div>	DRAWING NO. 02_046277		REV E
	PTD ENGINEER <div><PTD_ENGINEER></div>	SIZE D	SCALE 1:1	SHEET 7 OF 9

GPIO & MISC

REVISIONS

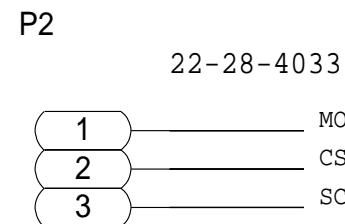
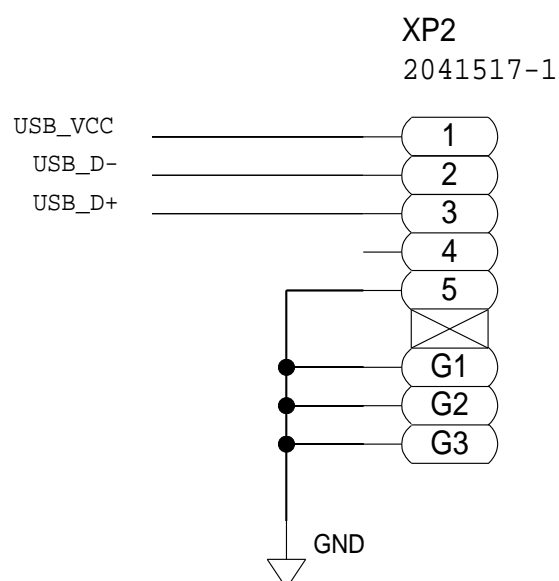
REV	DESCRIPTION	DATE	APPROVED



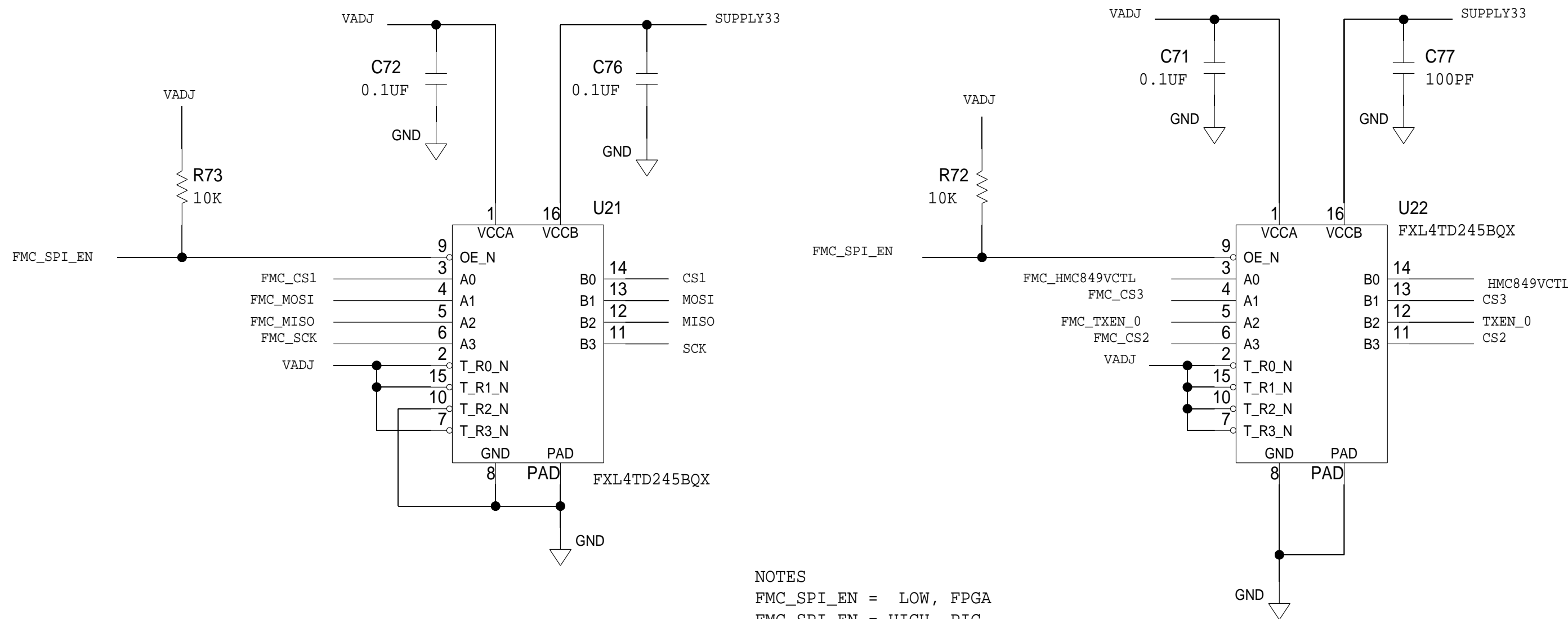
NOTE:

IF USB CABLE IS PLUGGED IN PIC IS THE SOURCE OF SPI BUS AND TXEN SIGNALS

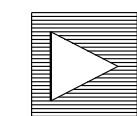
IF USB IS UNPLUGGED FMC INTERFACE IS THE SOURCE OF SPI BUS AND TXEN SIGNAL.



CONNECTIONS WILL PASS RIGHT THROUGH THE CONNECTOR



NOTES
FMC_SPI_EN = LOW, FPGA
FMC_SPI_EN = HIGH, PIC



ANALOG
DEVICES

SCHEMATIC

HW TYPE : Customer Evaluation
Product(s): AD9162-FMC_EBZ
: AD9162-FMC-EBZ

DESIGN VIEW
<DESIGN_VIEW>

PTD ENGINEER
<PTD_ENGINEER>

DRAWING NO.
02_046277

SIZE
D

SCALE
1:1

SHEET 8 OF 9

REV
E

