

8

7

6

5

4

3

2

1

THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED, IN WHOLE OR IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS, OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS OF ANALOG DEVICES. THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY ANALOG DEVICES.

RELAY CONTROL CHART

CONTROL	CODE	DEVICE	FUNCTION	CONNECTOR

JUMPER TABLE		
JP#	ON	OFF
1		
2		
3		
4		
5		

* SEE ASSEMBLY INSTRUCTIONS

REVISIONS

REV	DESCRIPTION	DATE	APPROVED
C	REMOVE T32 R44 C318	2/1/2018	S.B.

TEMPLATE ENGINEER
B.PHILLIPS

HARDWARE SERVICES
R.MACDONALD

HARDWARE SYSTEMS
D.WILLIAMS

TEST ENGINEER
N/A

COMPONENT ENGINEER
N/A

TEST PROCESS
R.DELGADO

HARDWARE RELEASE
N/A

DESIGNER
P. ALONGI

PTD ENGINEER
A.ALVARAN

CHECKER
-

DATE

SCHEMATIC

HW TYPE : AD9162 & 64-FMCB EBZ
Product(s) :
:
PACKAGE : N/A
: N/A
TESTER : N/A
CONTACTOR : N/A
HANDLER : N/A

MASTER PROJECT TEMPLATE
TBD

TESTER TEMPLATE
040127_b

DRAWING NO.
02_046215

REV.
C

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES

TOLERANCES

DECIMALS
X.XX +0.010
X.XXX +0.005

FRACTIONS
+1/32

ANGLES
+2

SIZE
D

SCALE
1:1

CODE ID NO.
CodeID

SHEET 1 OF 9

8

7

6

5

4

3

2

1

A

B

C

D

8

7

6

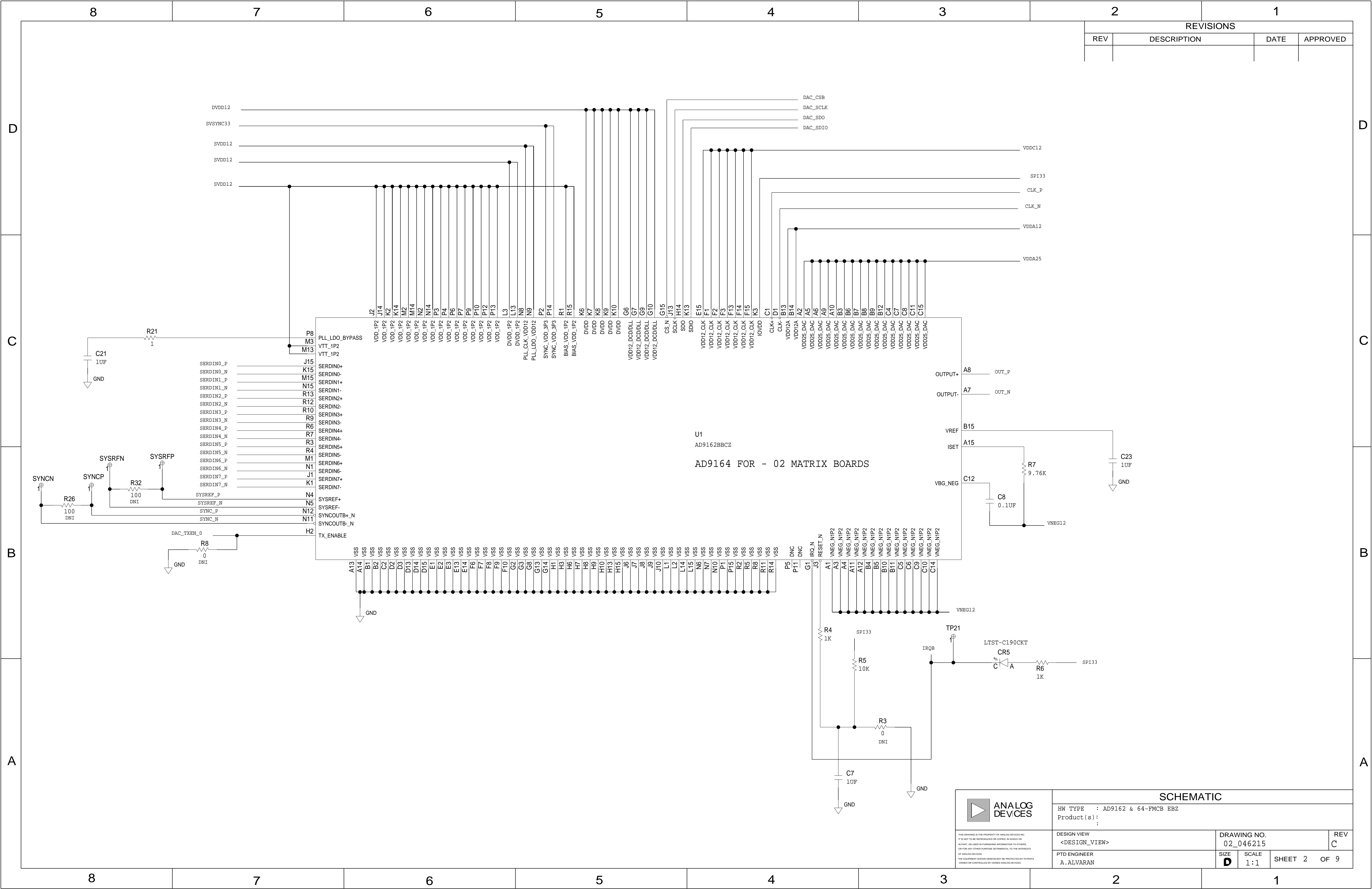
5

4

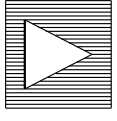
3

2

1



REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



**ANALOG
DEVICES**

SCHEMATIC

HW TYPE : AD9162 & 64-FMCMC EBZ

Product(s) :

DESIGN VIEW
<DESIGN_VIEW>

PTD ENGINEER
A. ALVARAN

DRAWING NO.
02_046215

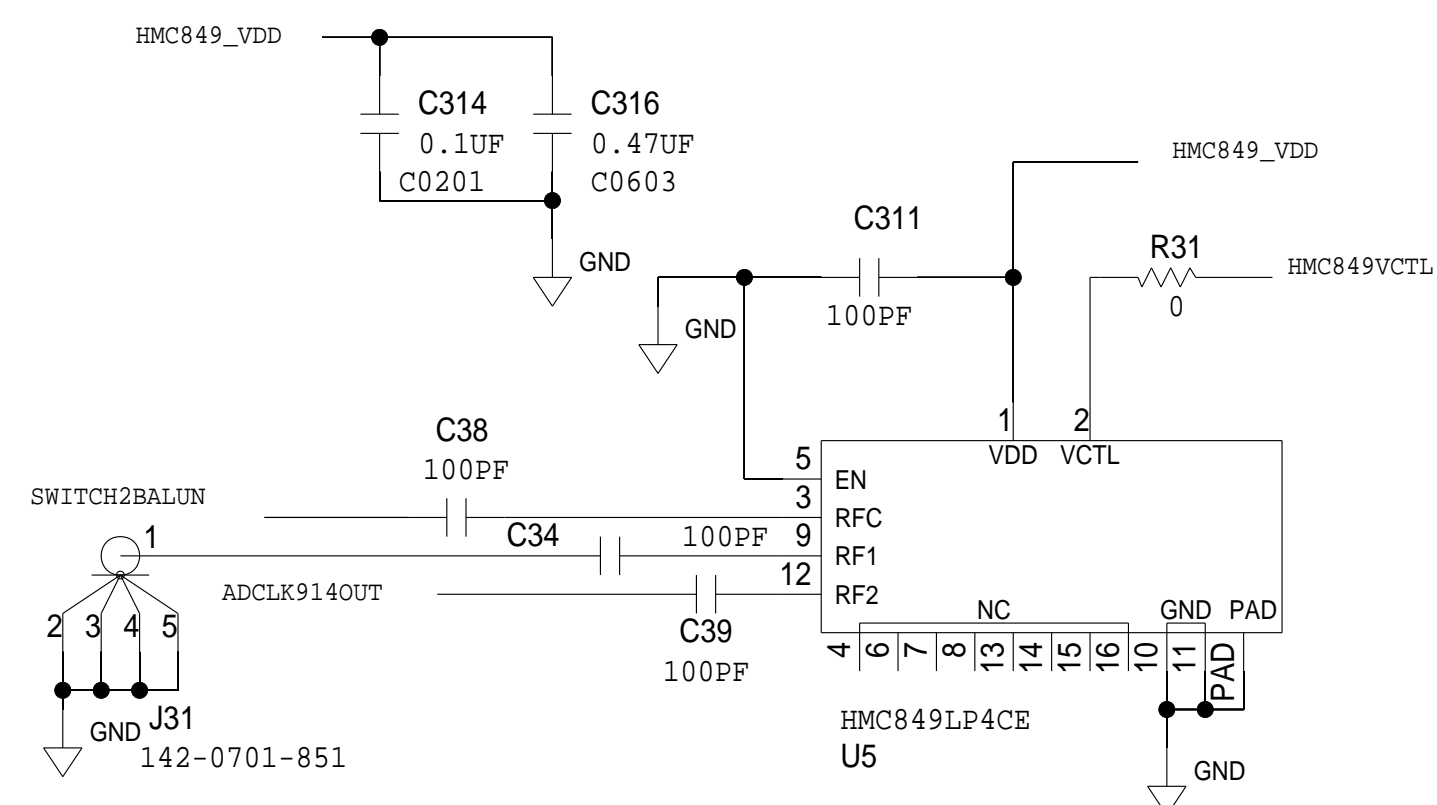
SIZE **D**
SCALE 1:1

REV
C

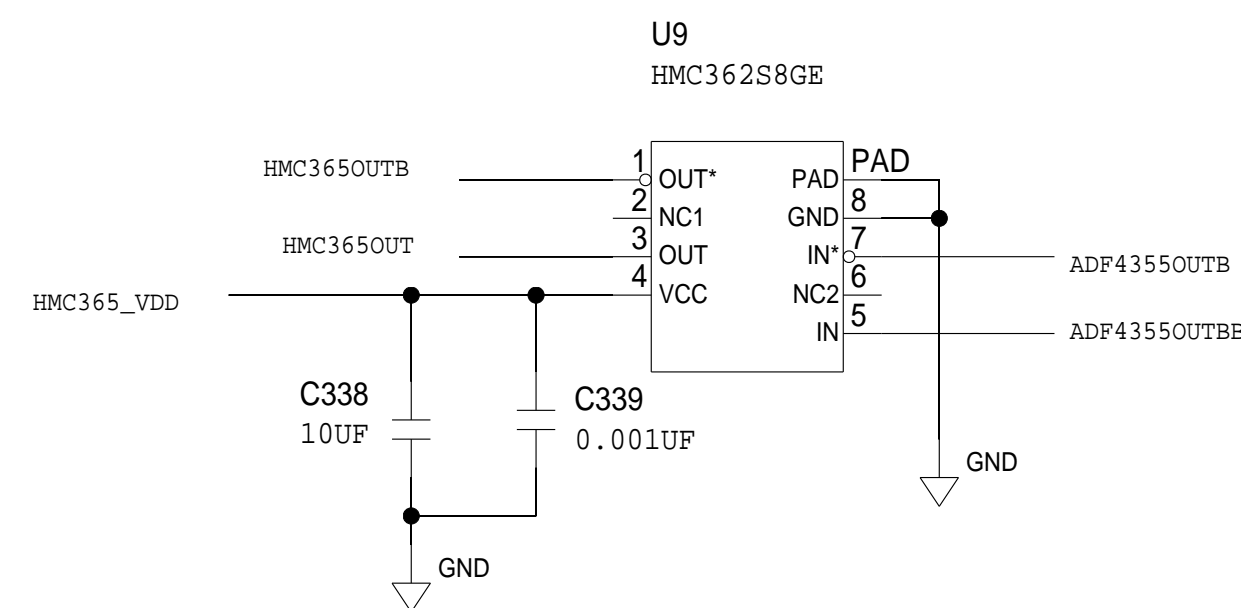
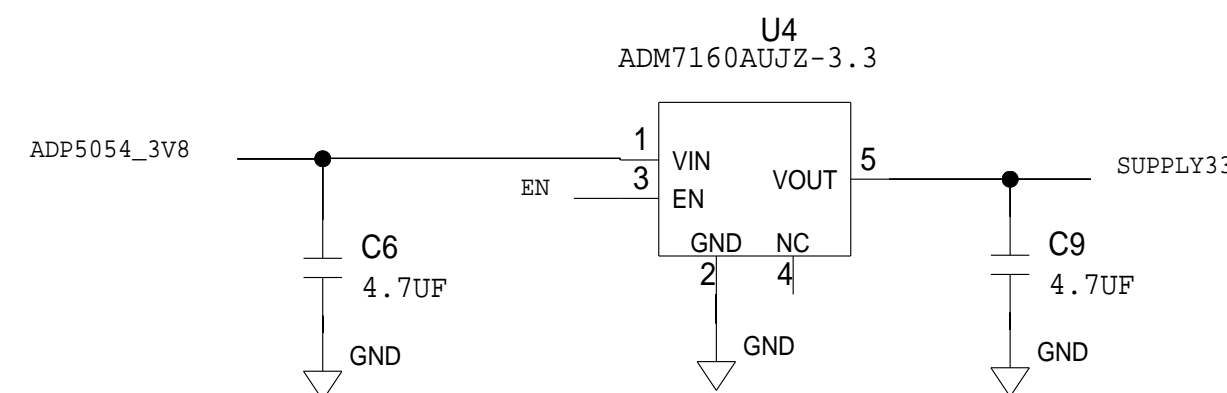
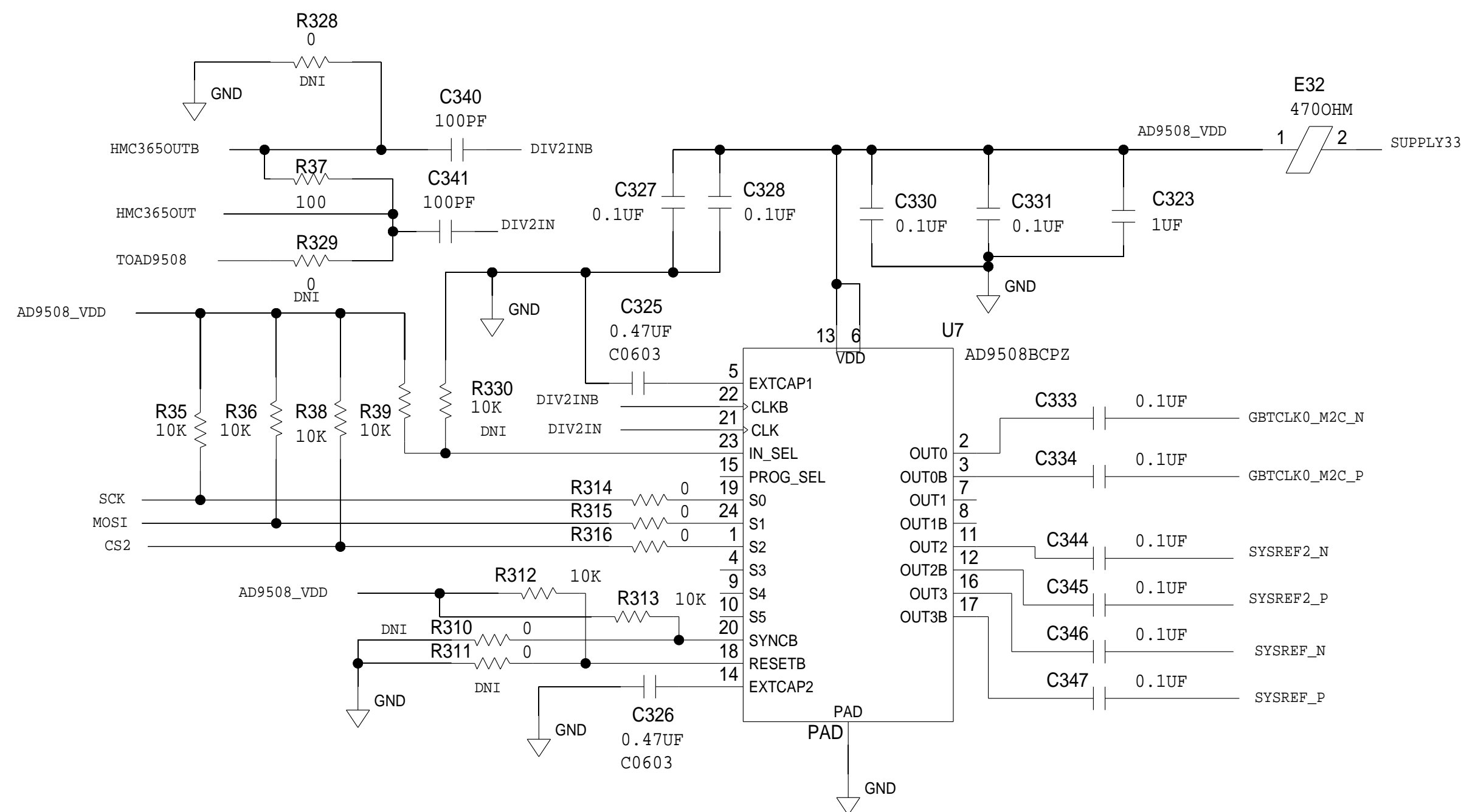
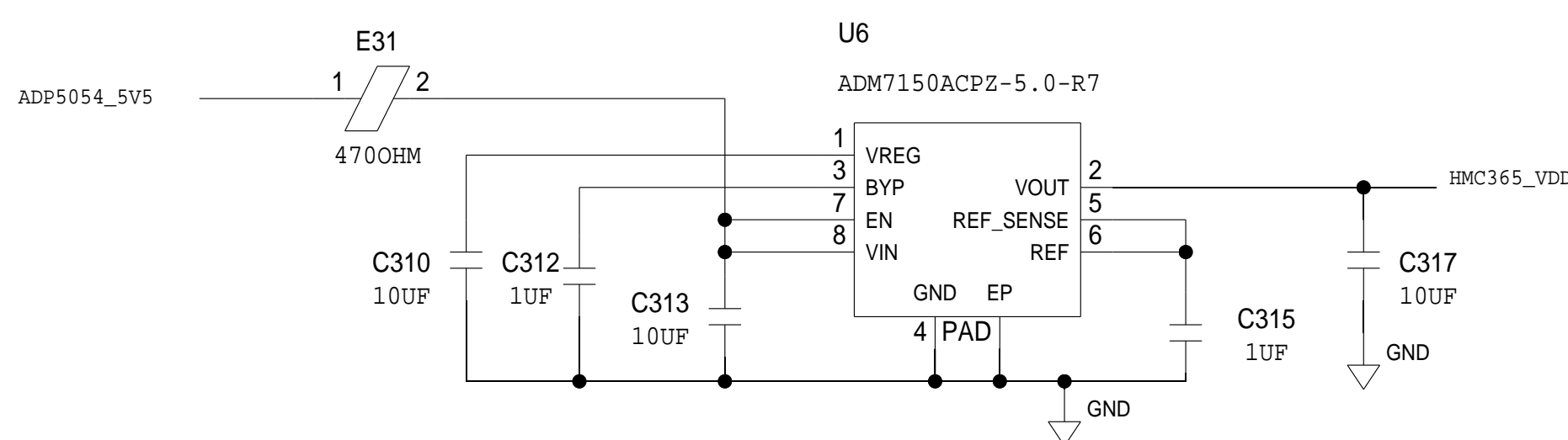
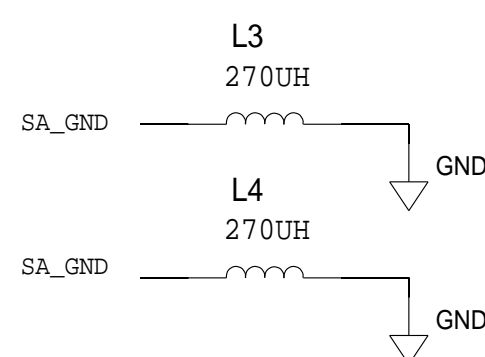
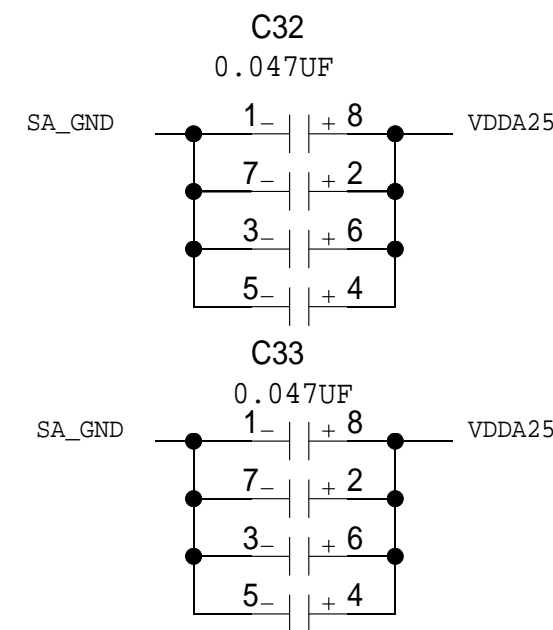
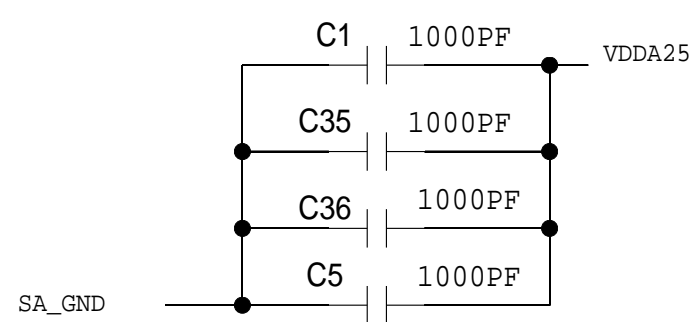
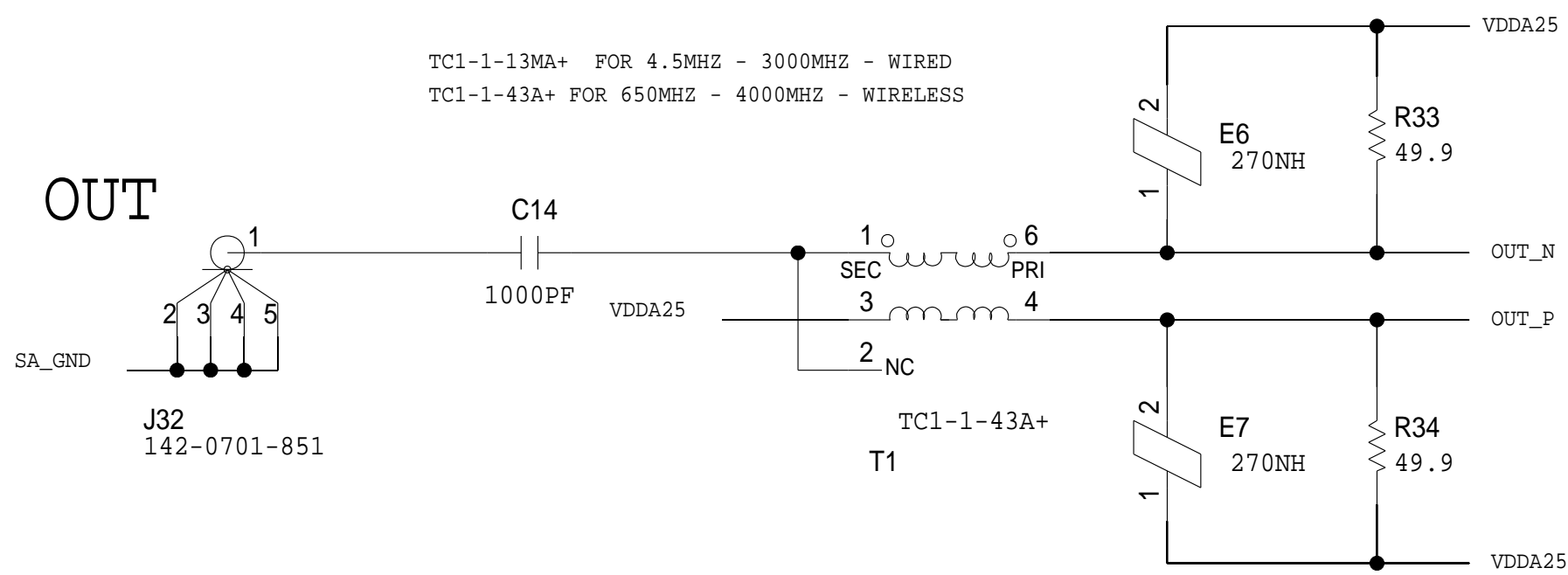
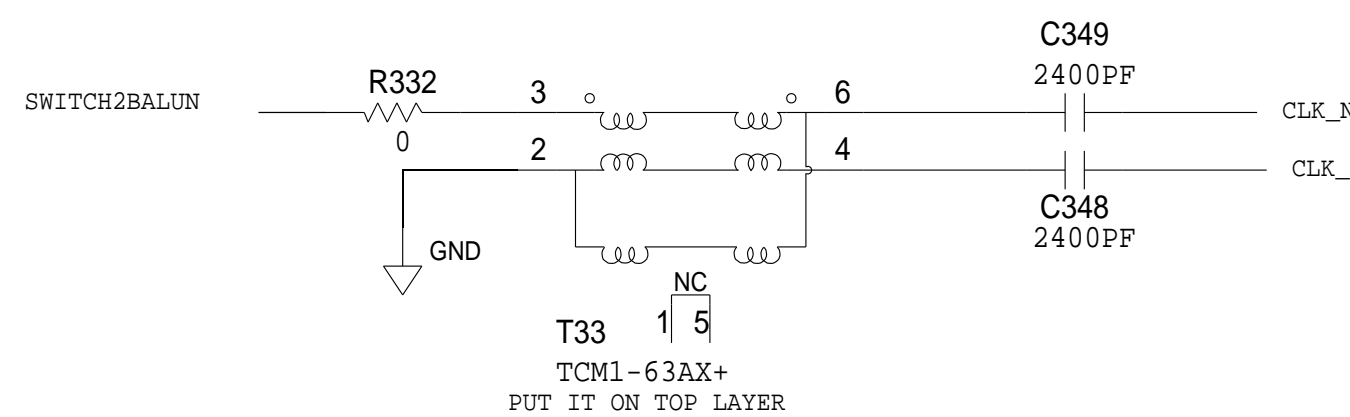
SHEET 2 OF 9

DAC OUTPUT & CLK & POWER SEQUENCE

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



EXT CLK

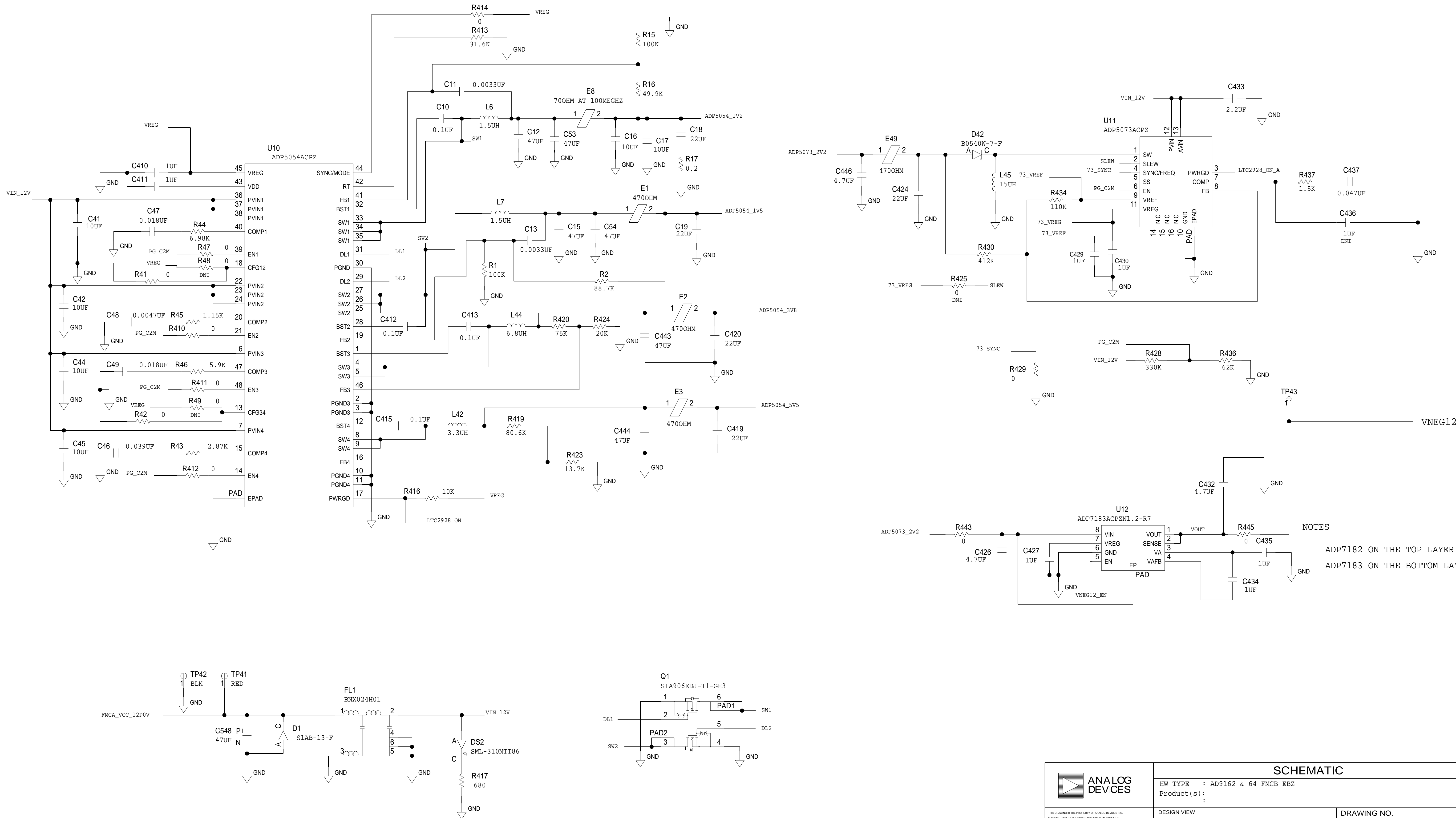


SCHEMATIC			
HW TYPE : AD9162 & 64-FMCMC FBZ			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_046215	REV C	
PTD ENGINEER A. ALVARAN	SIZE D	SCALE 1:1	SHEET 3 OF 9

POWER - REGULATOR & NEG LDO

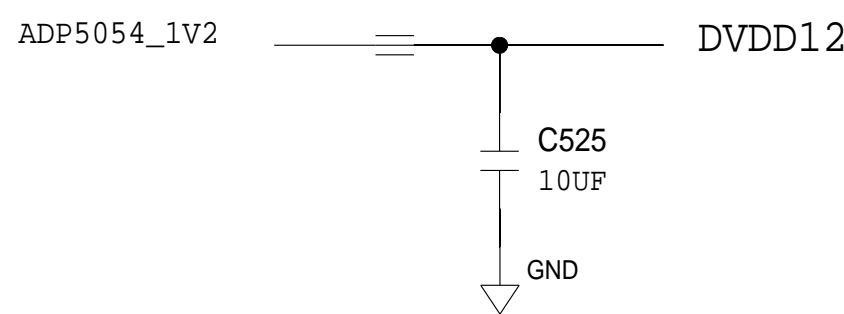
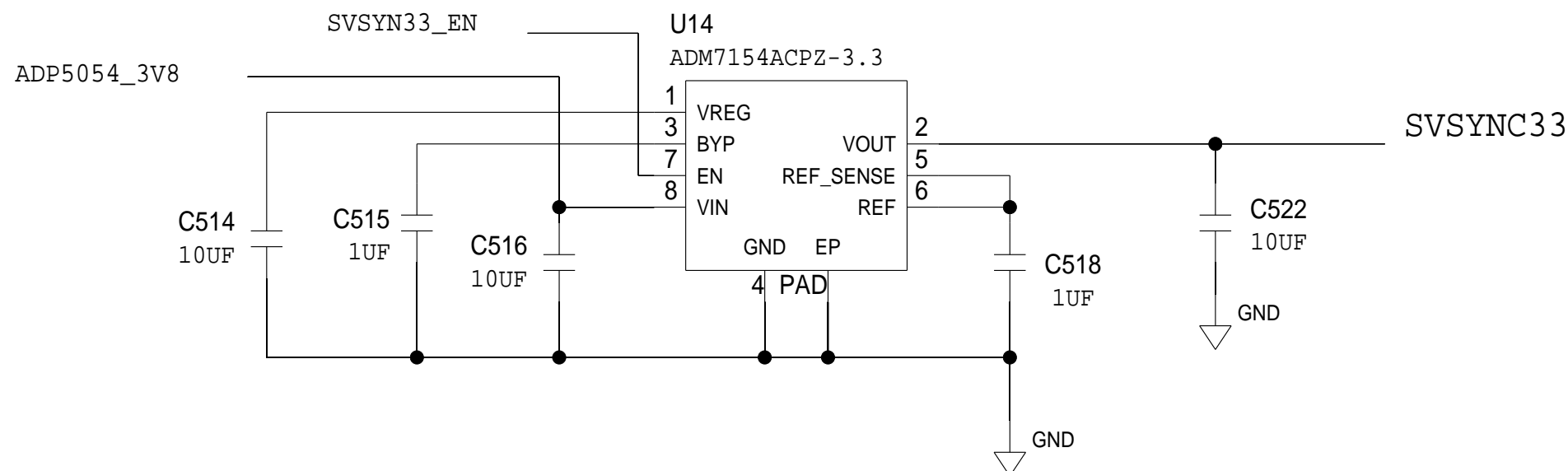
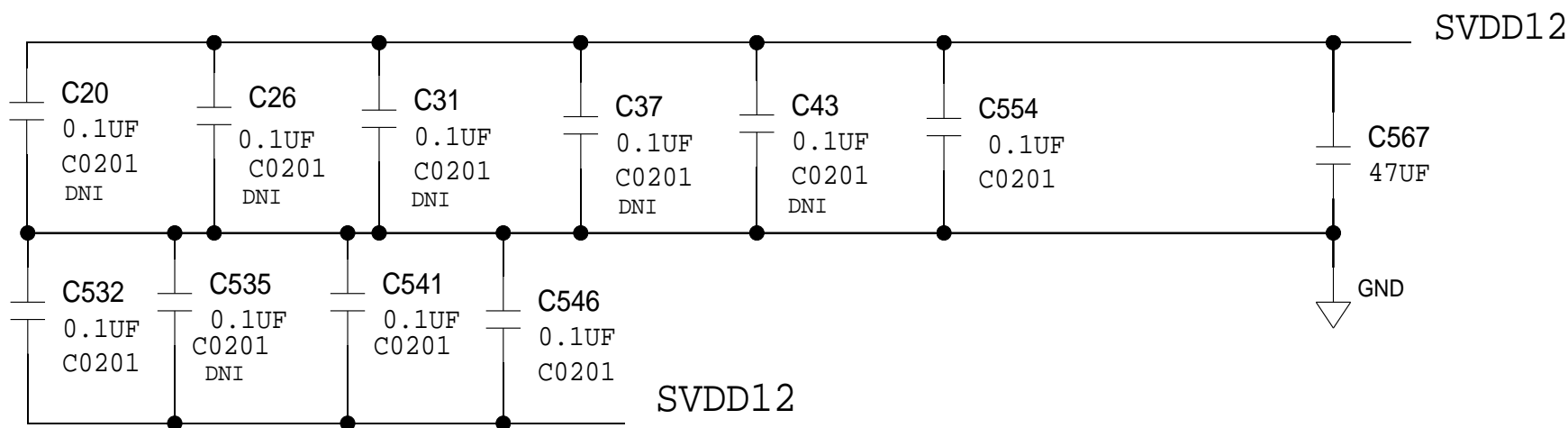
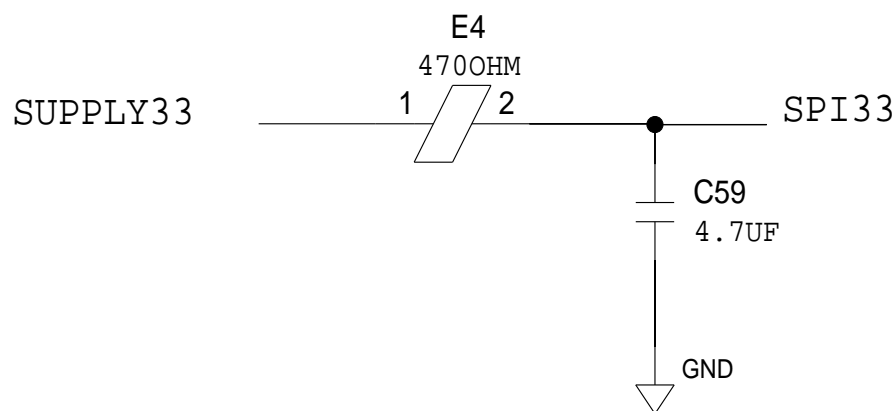
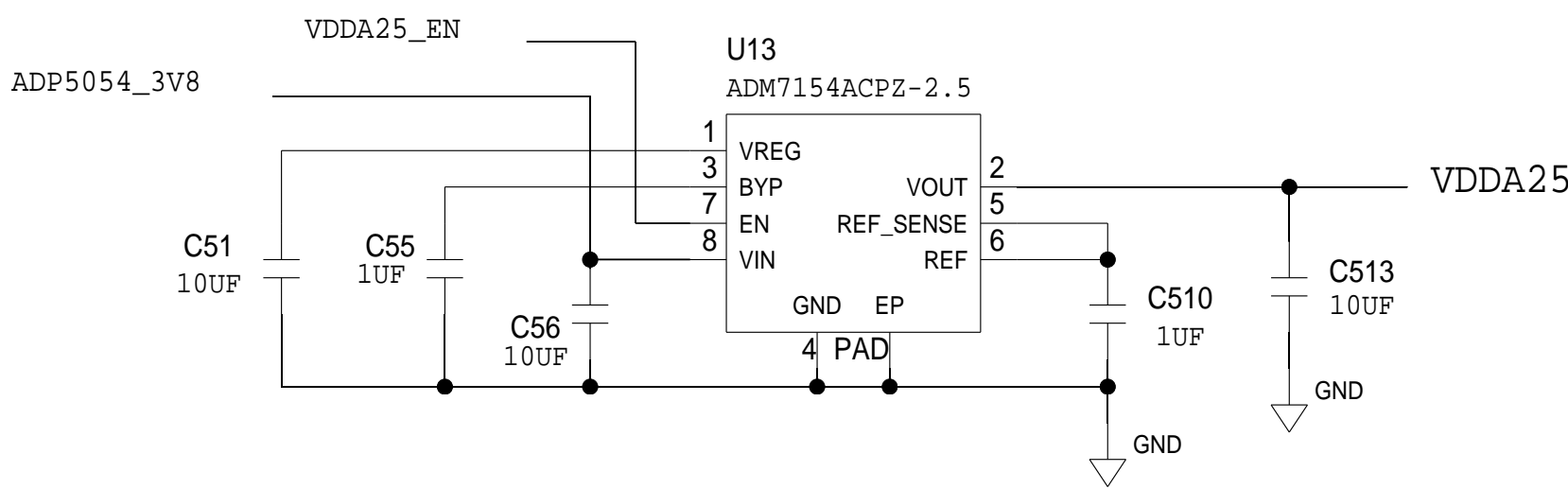
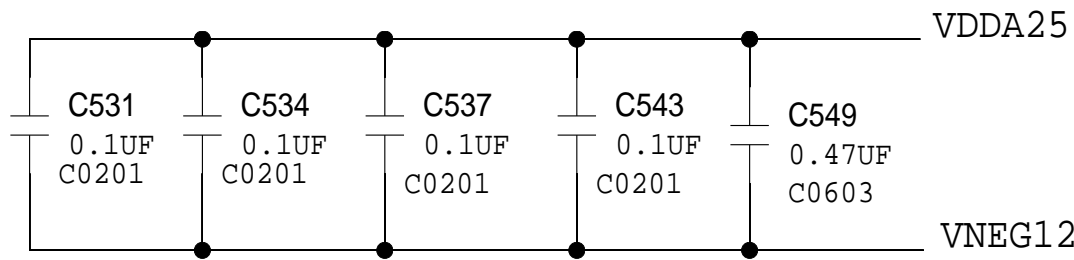
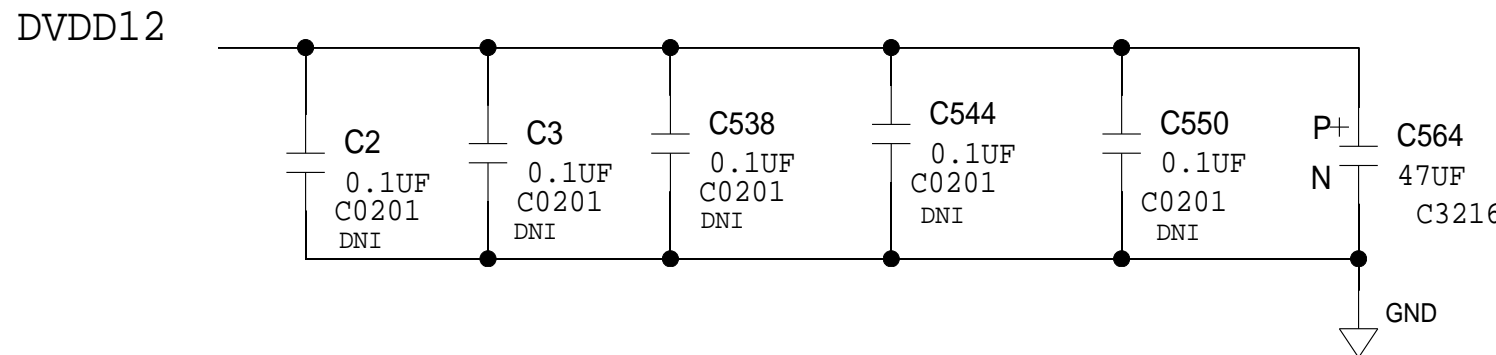
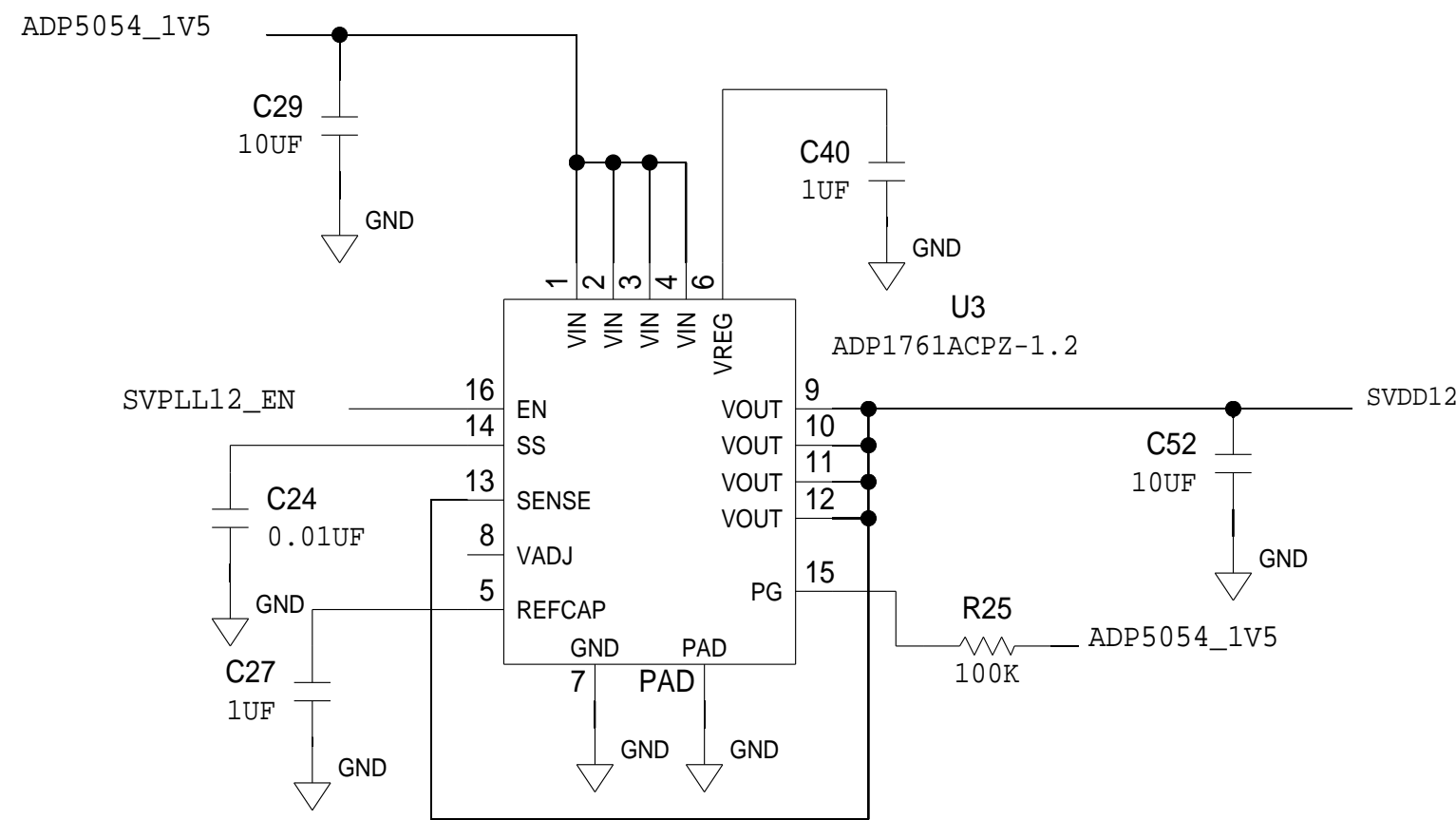
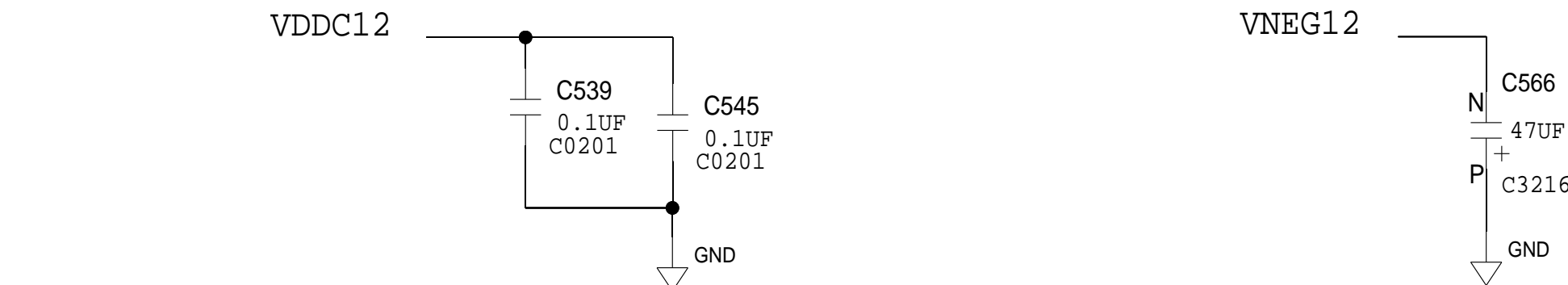
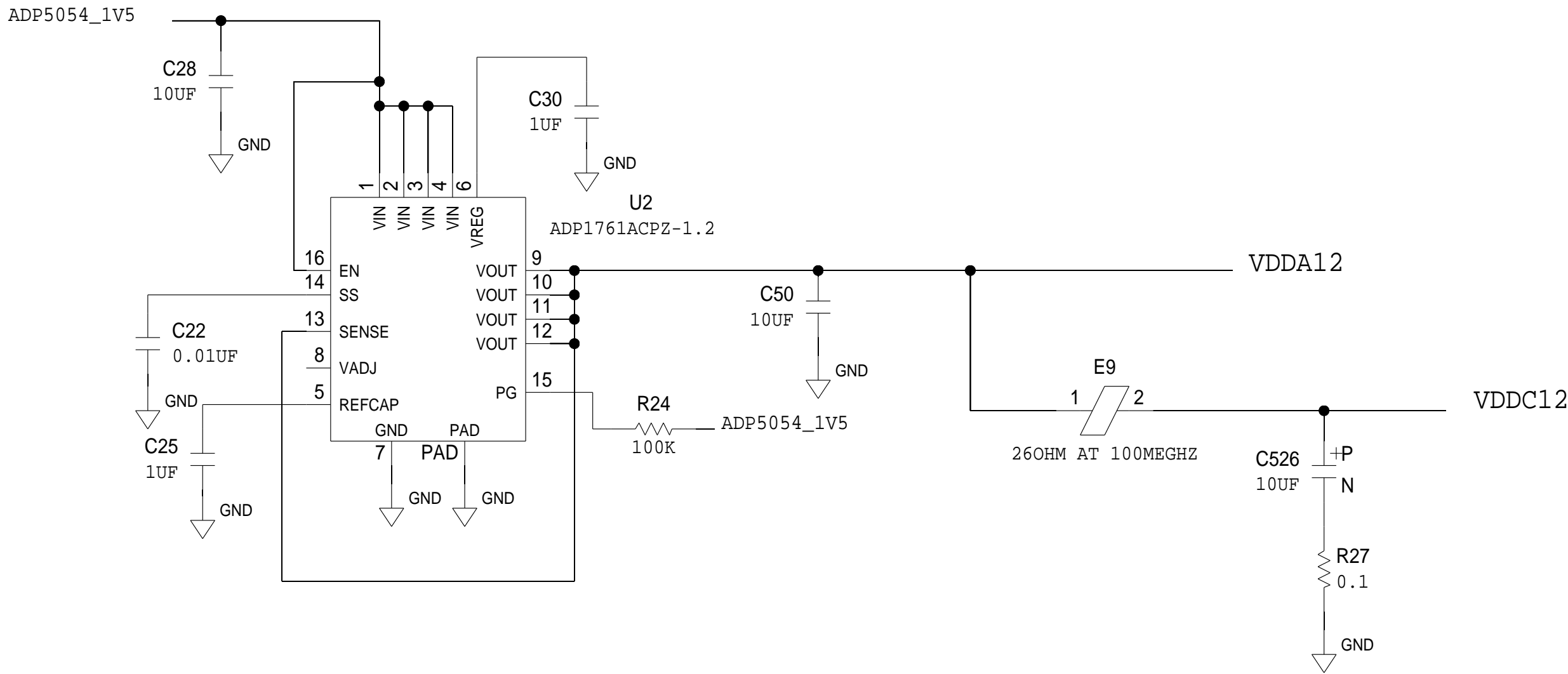
REVISIONS

REV	DESCRIPTION	DATE	APPROVED



POWER - LDO

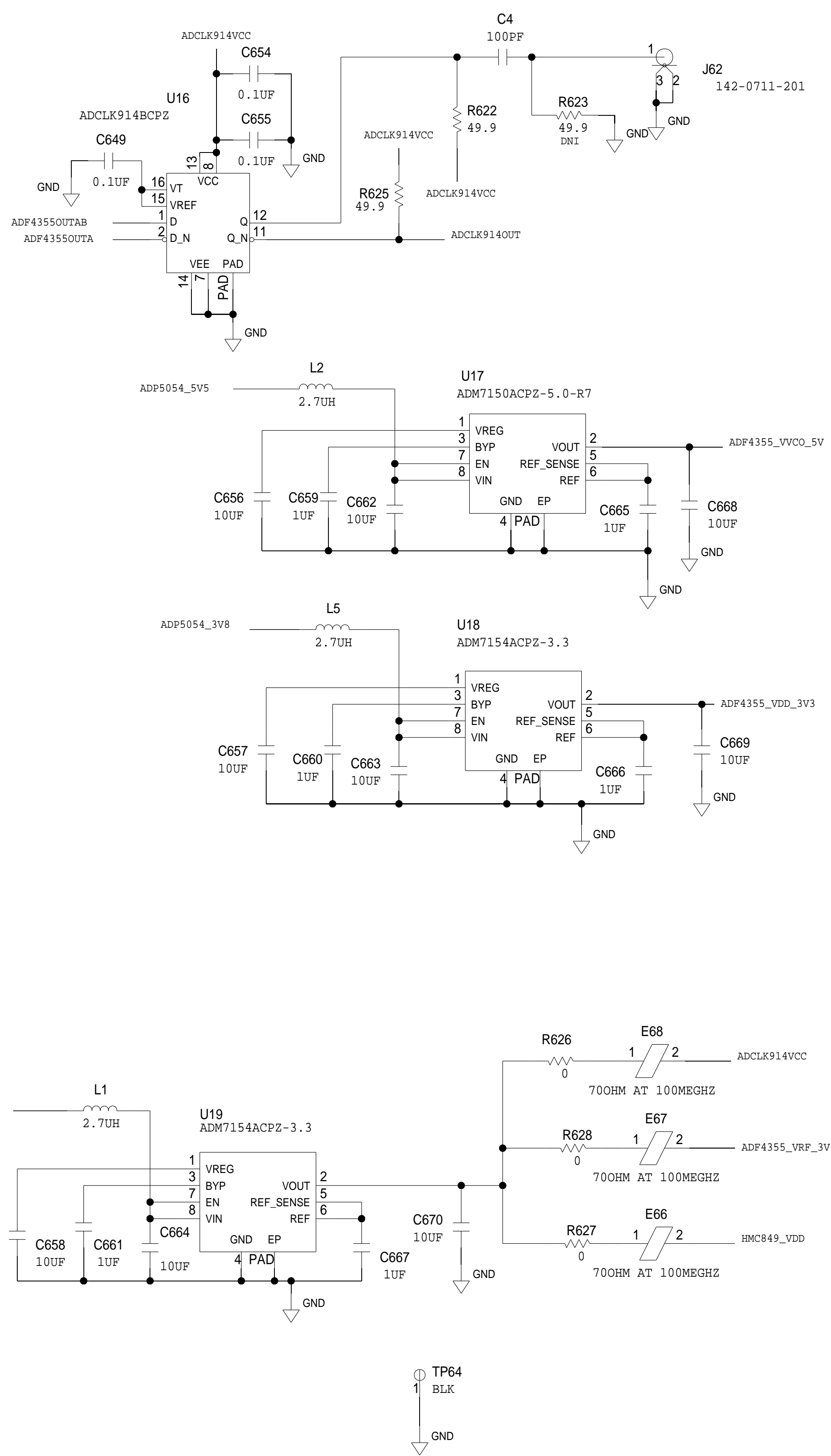
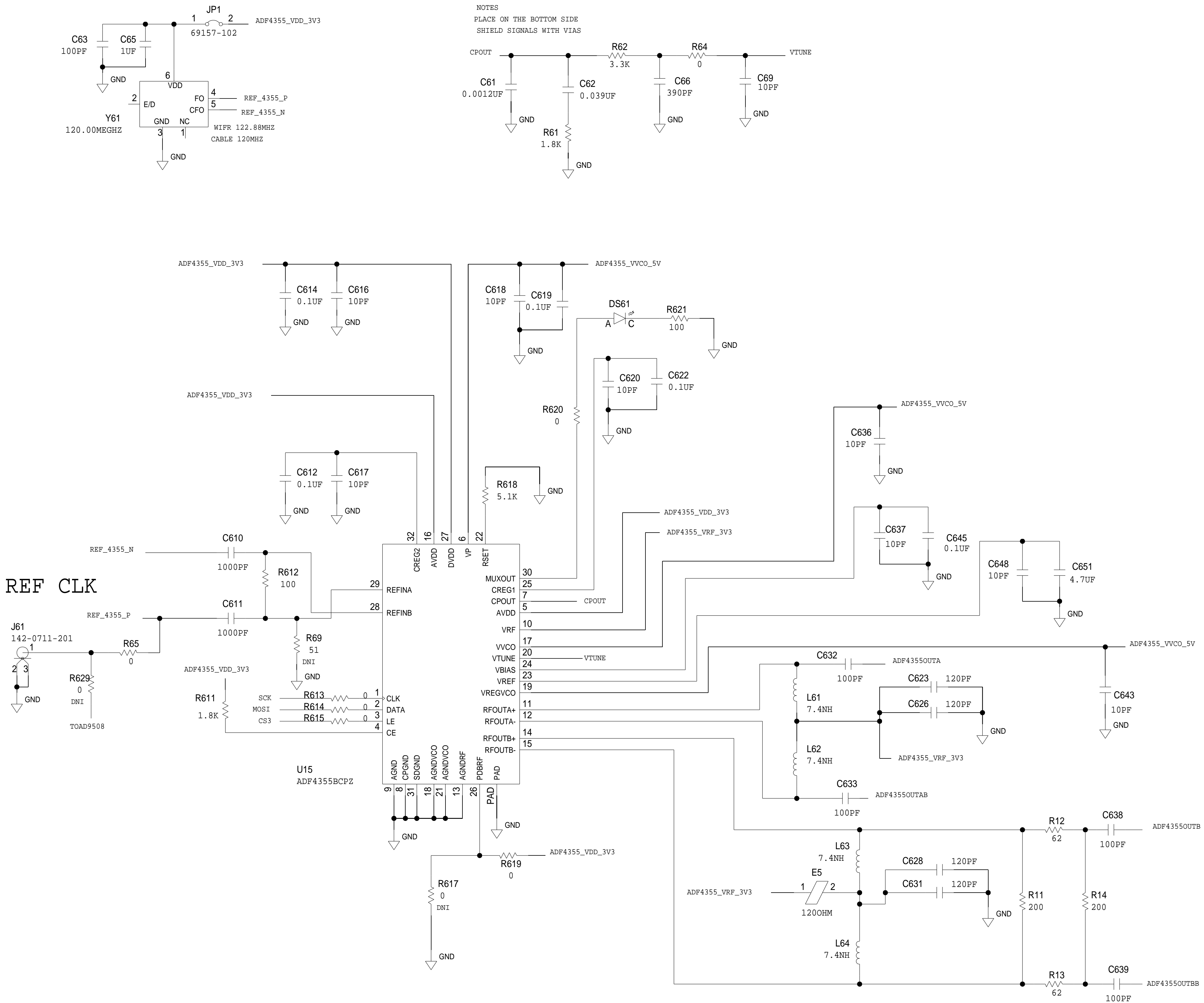
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



SCHEMATIC			
HW TYPE : AD9162 & 64-FMCB EBZ		DRAWING NO. 02_046215	
Product(s) :		REV C	
DESIGN VIEW <DESIGN_VIEW>		SIZE D	
PTD ENGINEER A. ALVARAN		SCALE 1:1	
		SHEET 5 OF 9	

ONBOARD CLK - ADF4355-2 & CLK BUFFER

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



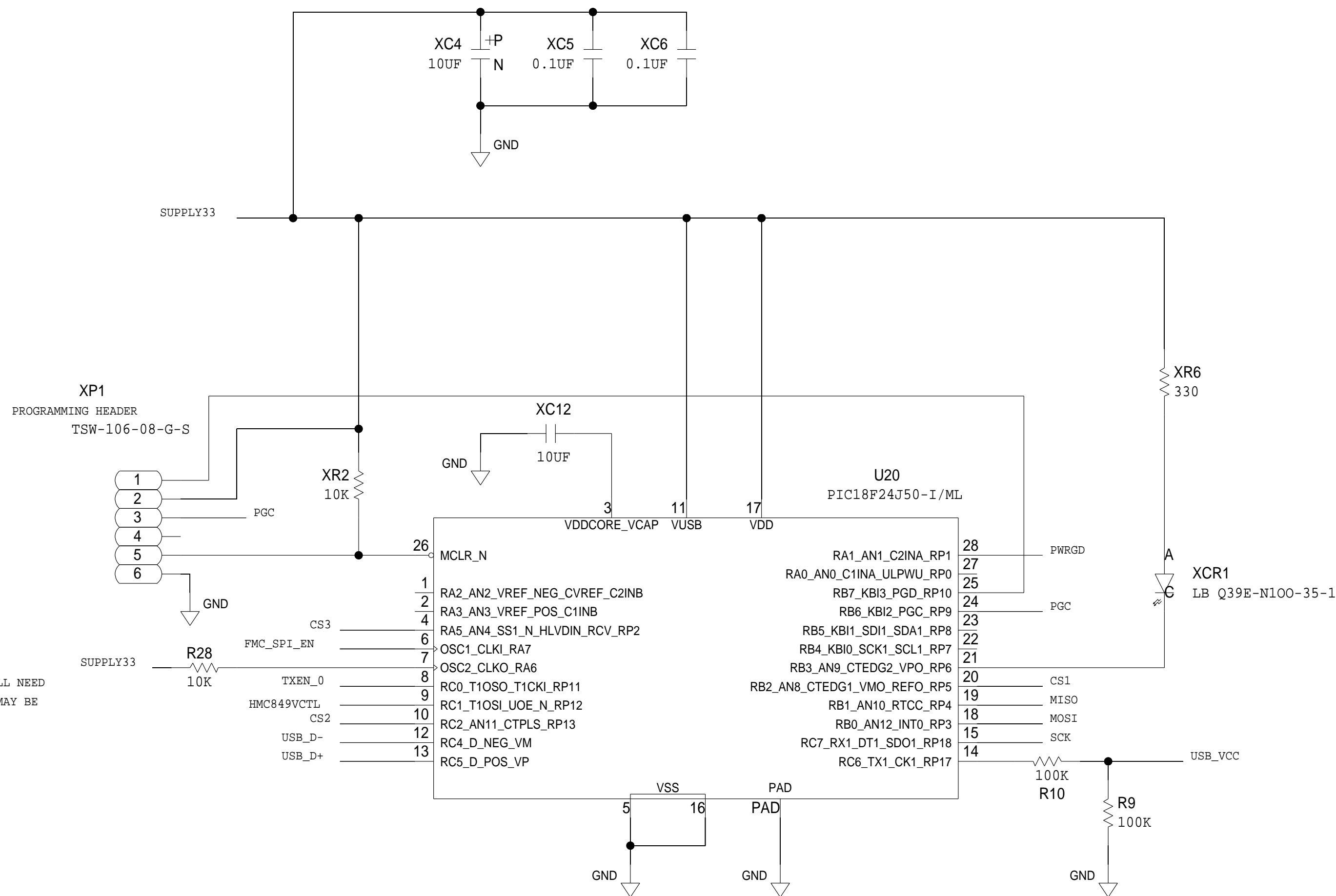
SCHEMATIC

HW TYPE : AD9162 & 64-FMCB EBZ Product(s) :			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_046215	REV C	
PTD ENGINEER A. ALVARAN	SIZE D	SCALE 1:1	SHEET 6 OF 9

GPIO & MISC

REVISIONS

REV	DESCRIPTION	DATE	APPROVED

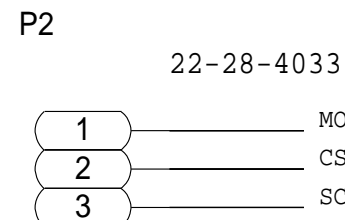
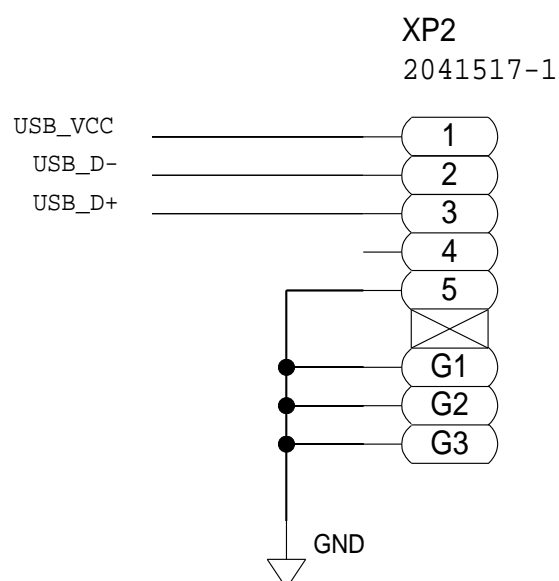


IMPLEMENTATION NOTE: FMC_SPI_EN WILL NEED TO BE SAMPLED AS ANALOG, SINCE IT MAY BE AS LOW AS 1.2V LOGIC HIGH.

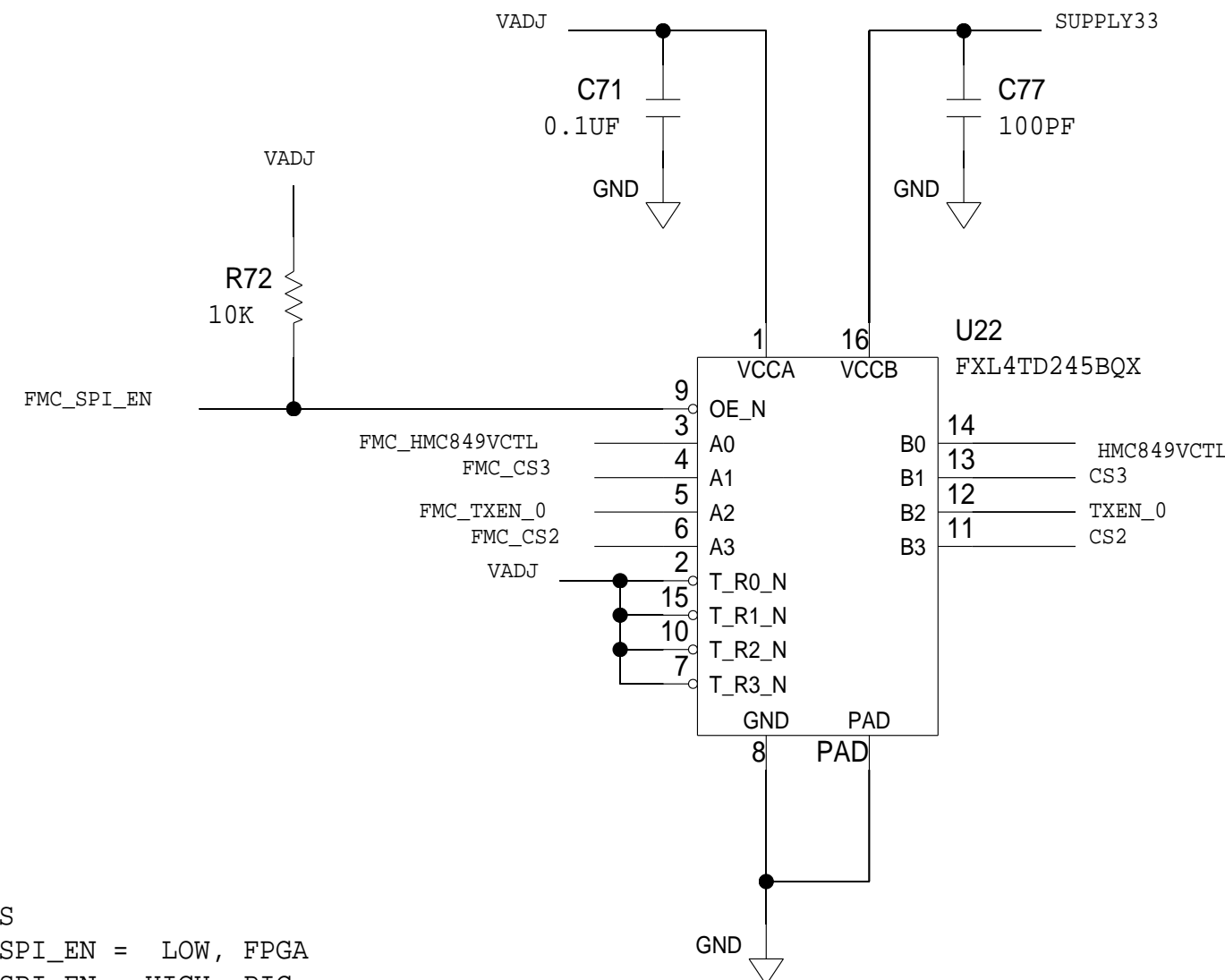
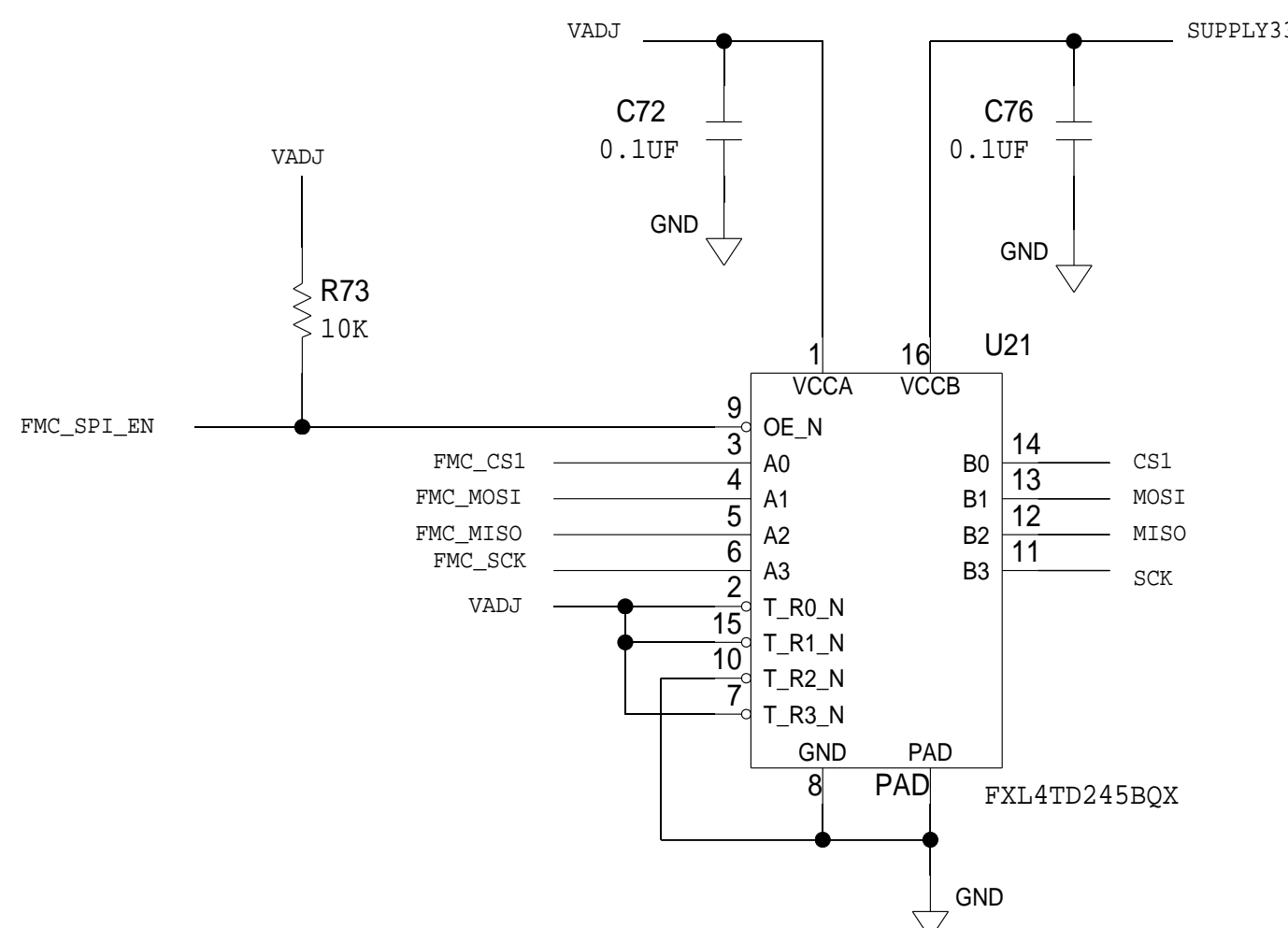
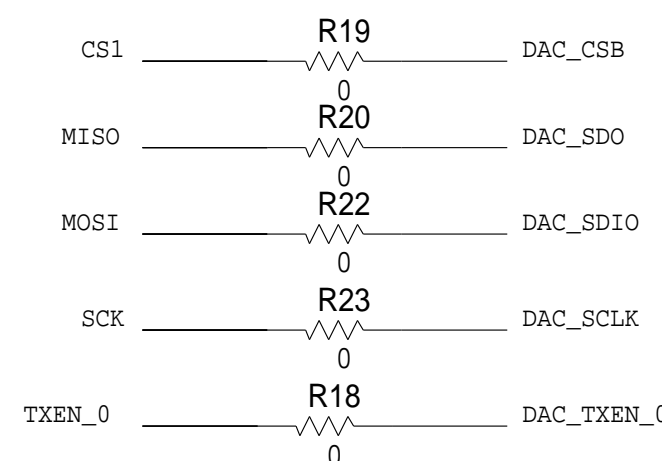
NOTE:

IF USB CABLE IS PLUGGED IN PIC IS THE SOURCE OF SPI BUS AND TXEN SIGNALS

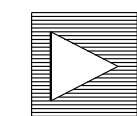
IF USB IS UNPLUGGED FMC INTERFACE IS THE SOURCE OF SPI BUS AND TXEN SIGNAL.



CONNECTIONS WILL PASS RIGHT THROUGH THE CONNECTOR



NOTES
FMC_SPI_EN = LOW, FPGA
FMC_SPI_EN = HIGH, PIC



ANALOG
DEVICES

SCHEMATIC

HW TYPE : AD9162 & 64-FMCB EBZ
Product(s) :

DESIGN VIEW
<DESIGN_VIEW>

PTD ENGINEER
A. ALVARAN

DRAWING NO.
02_046215

SIZE
D

SCALE
1:1

SHEET 8 OF 9

REV
C

