

87654321

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RELAY CONTROL CHART

CONTROL	CODE	DEVICE	FUNCTION	CONNECTOR

JUMPER TABLE

JP#	ON	OFF
1		
2		
3		
4		
5		

* SEE ASSEMBLY INSTRUCTIONS

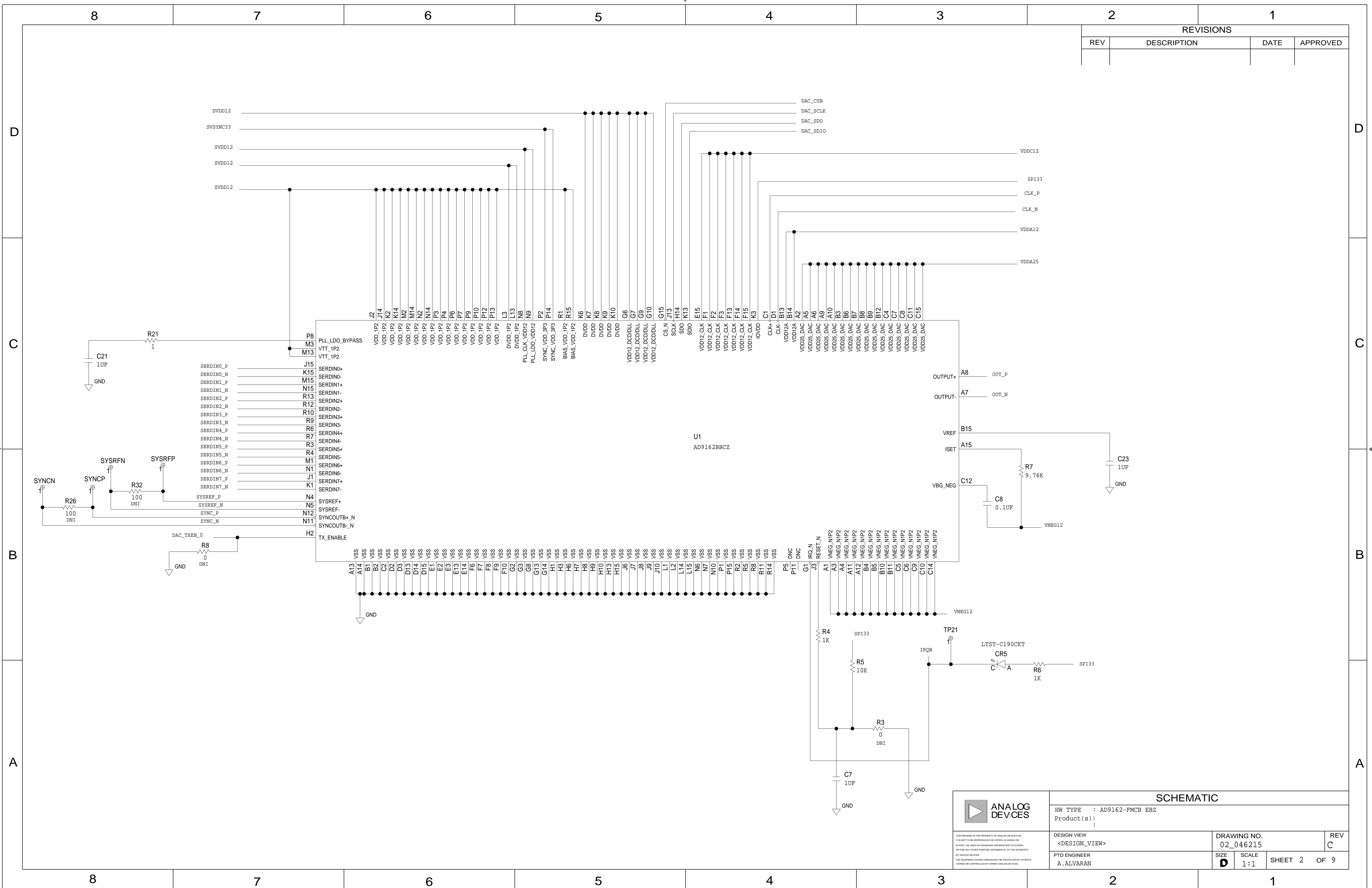
REVISIONS

REV	DESCRIPTION	DATE	APPROVED
C	REMOVE T32 R44 C318	2/1/2018	S.B.

SCHEMATIC

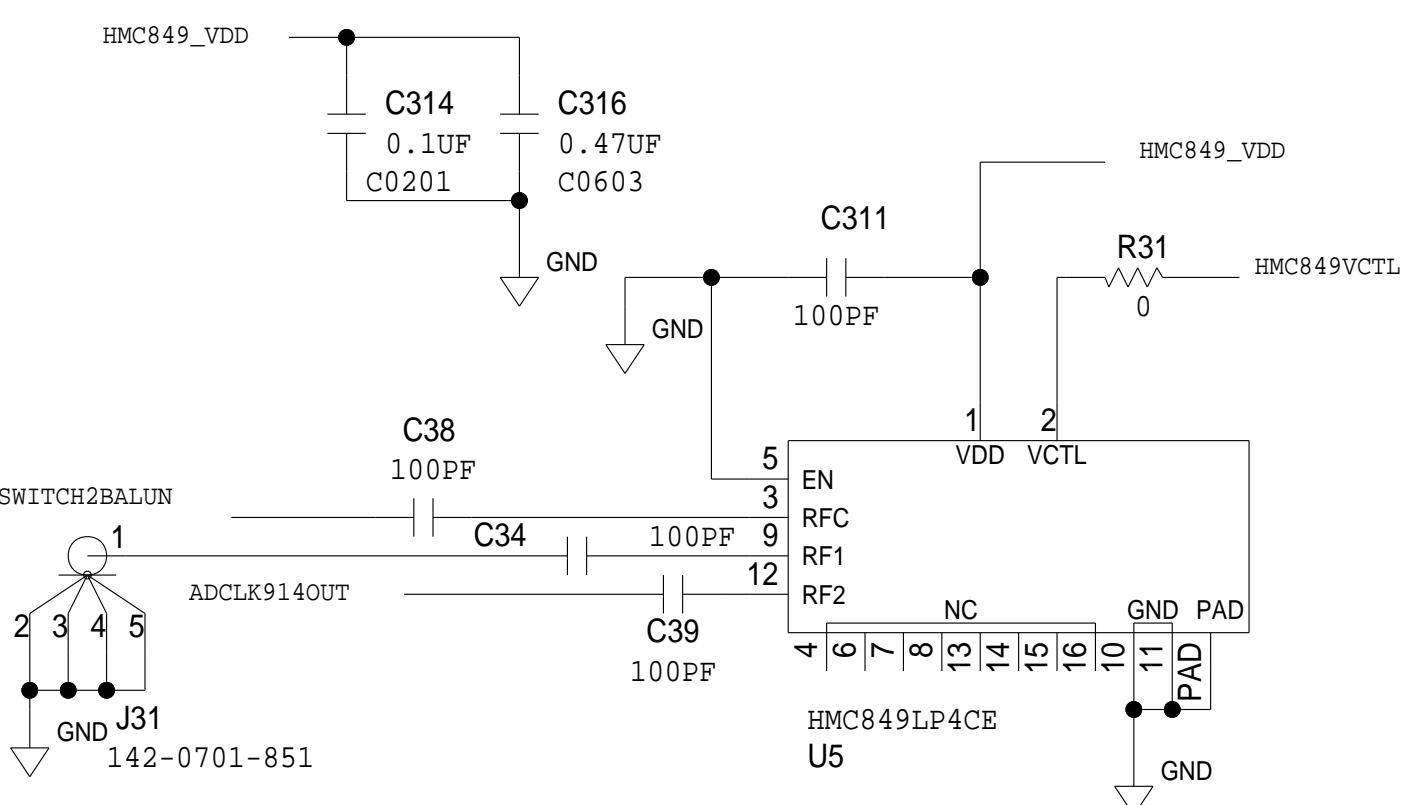
TEMPLATE ENGINEER B.PHILLIPS	DATE	HW TYPE : AD9162-FMCB EBZ Product(s) : : PACKAGE : N/A : N/A TESTER : N/A CONTACTOR : N/A HANDLER : N/A		<div><div></div><div>ANALOG DEVICES</div></div>
HARDWARE SERVICES R.MACDONALD				
HARDWARE SYSTEMS D.WILLIAMS				
TEST ENGINEER N/A				
COMPONENT ENGINEER N/A				
TEST PROCESS R.DELGADO		DRAWING NO. 02_046215		REV. C
HARDWARE RELEASE N/A		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES DECIMALS X.XX +0.010 X.XXX +0.005 FRACTIONS +1/32 ANGLES +2		SIZE D SCALE 1:1 CODE ID NO. CodeID
DESIGNER P. ALONGI		MASTER PROJECT TEMPLATE TBD		TESTER TEMPLATE 040127_b
PTD ENGINEER A.ALVARAN		SHEET 1		OF 9
CHECKER -				

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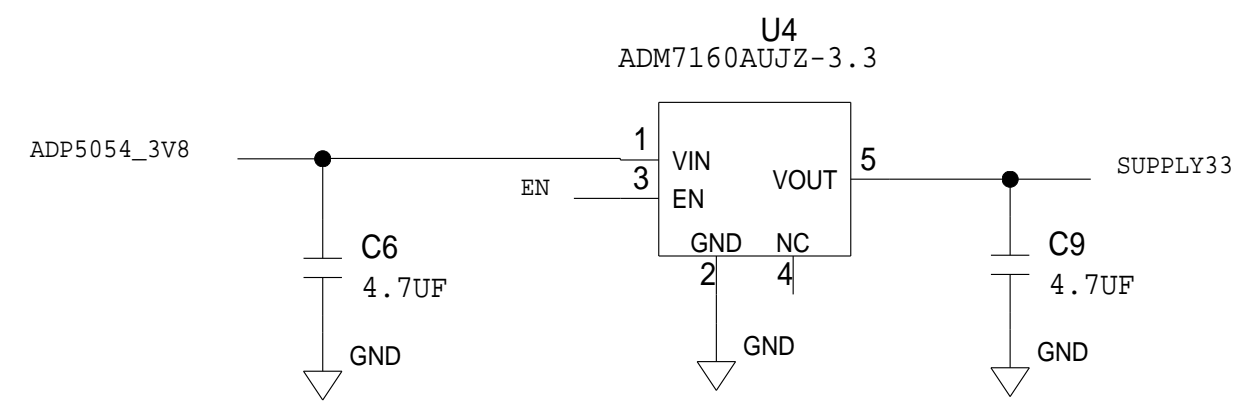
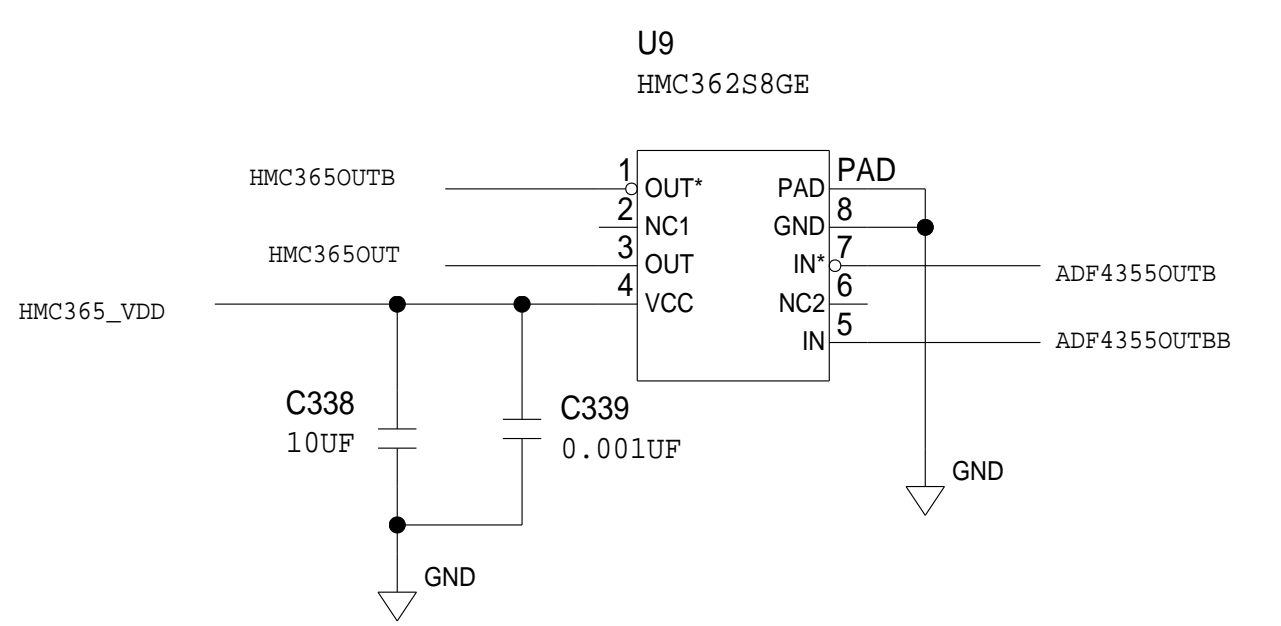
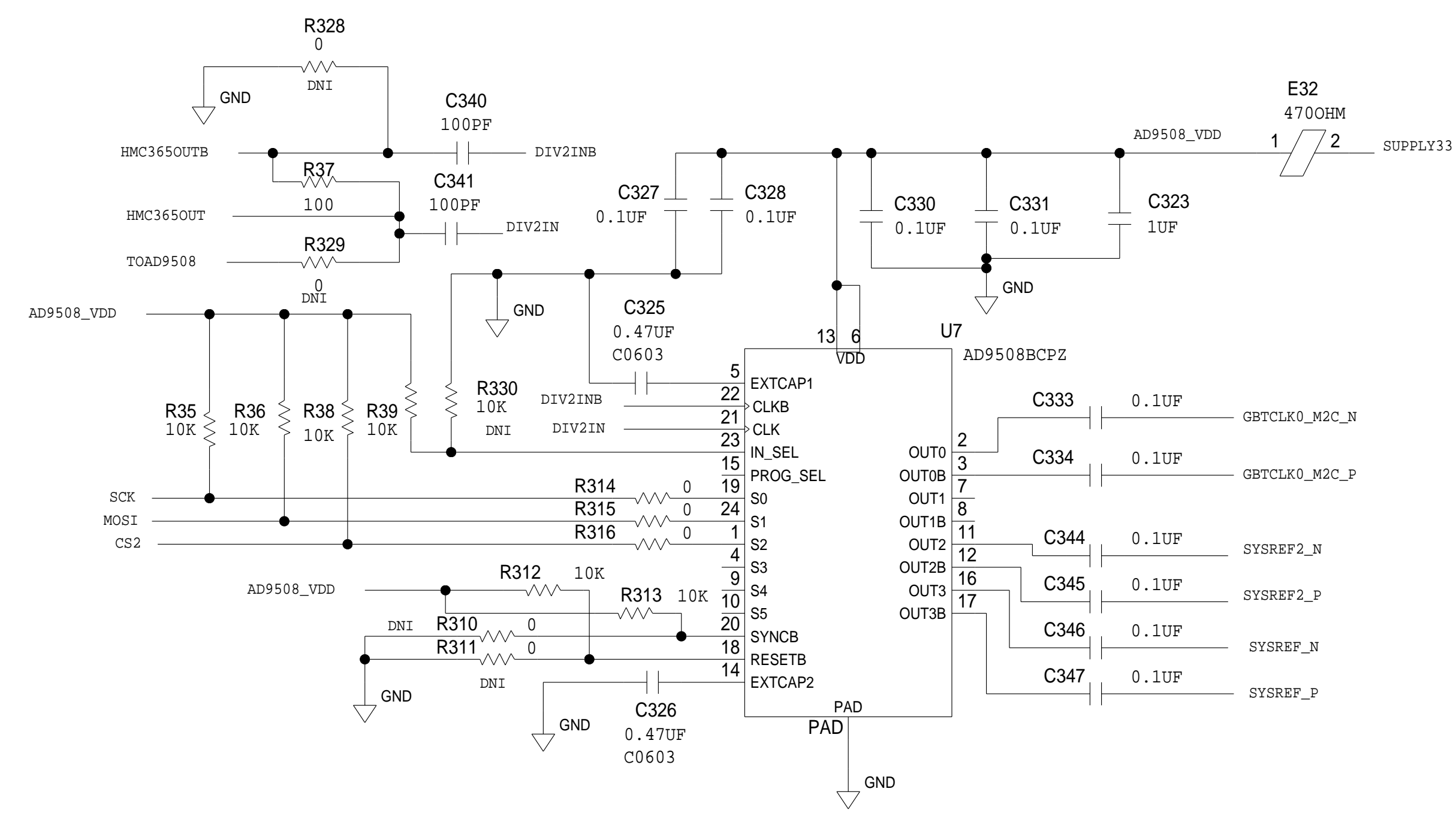
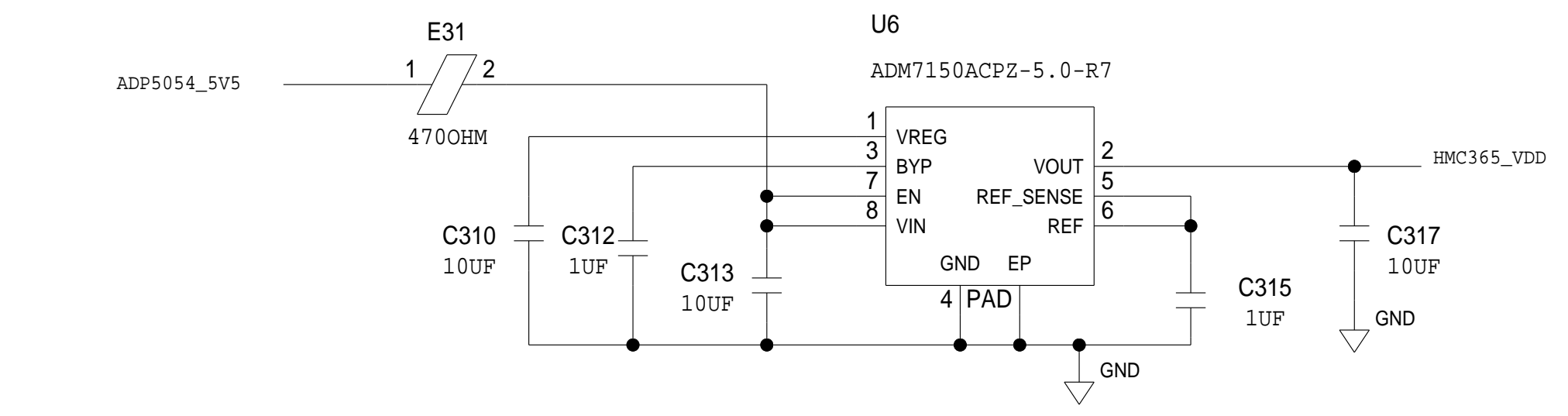
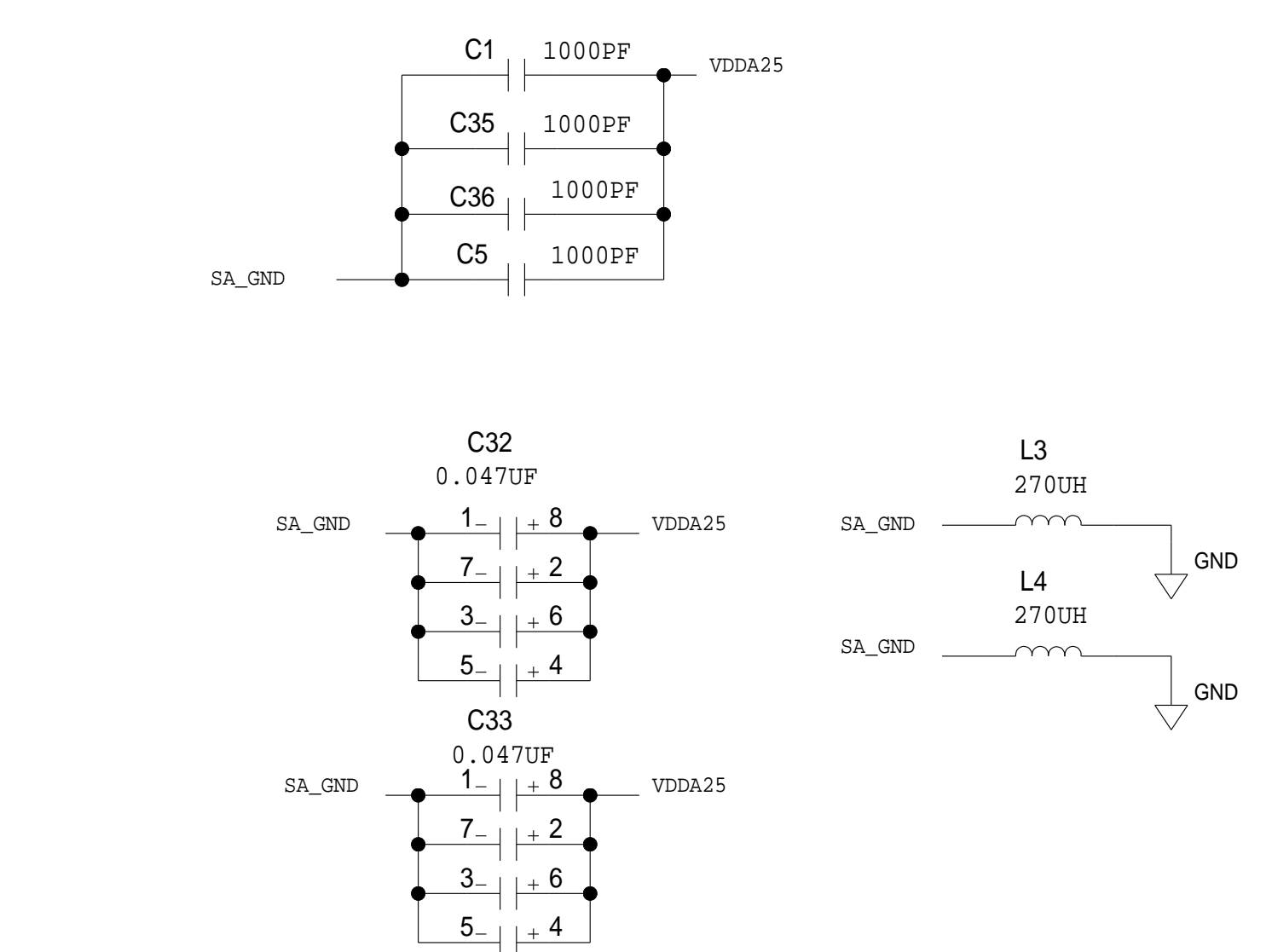
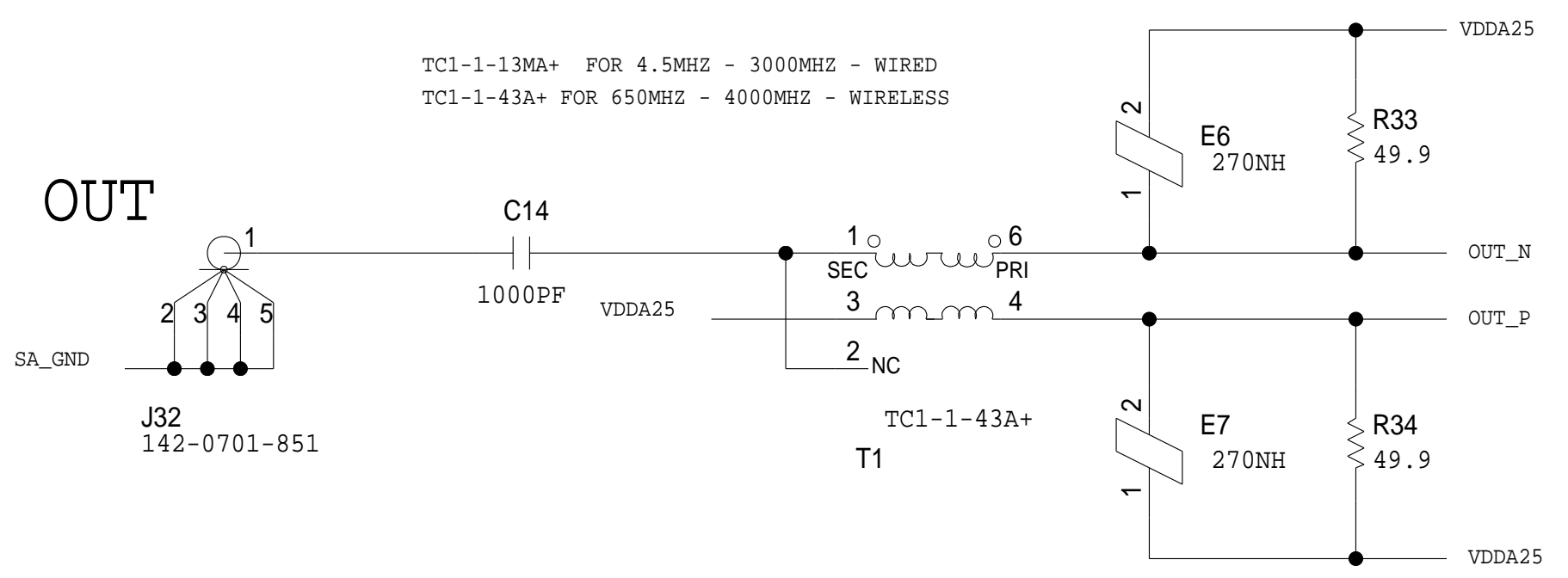
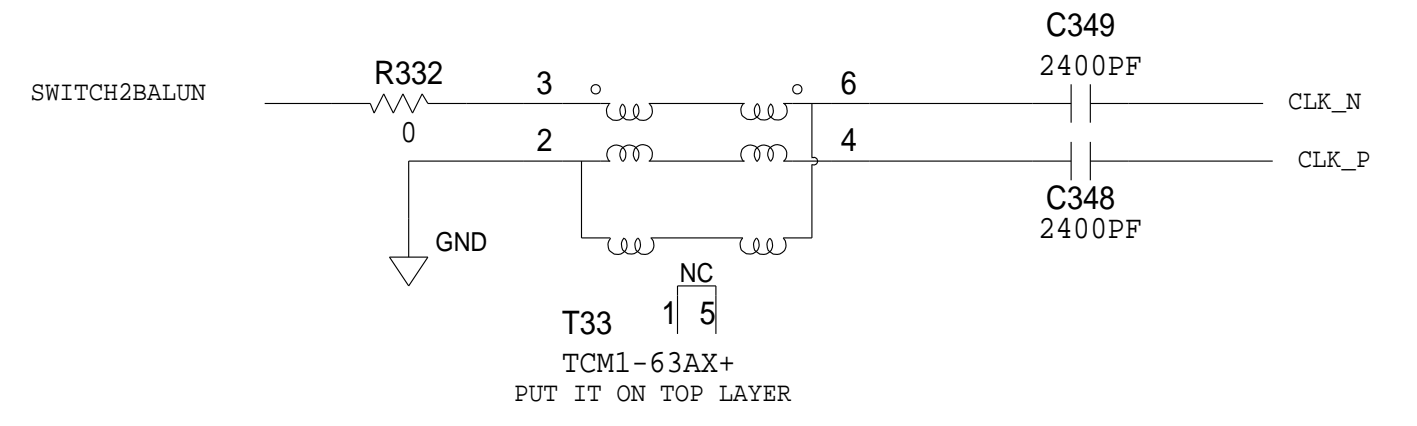


DAC OUTPUT & CLK & POWER SEQUENCE

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



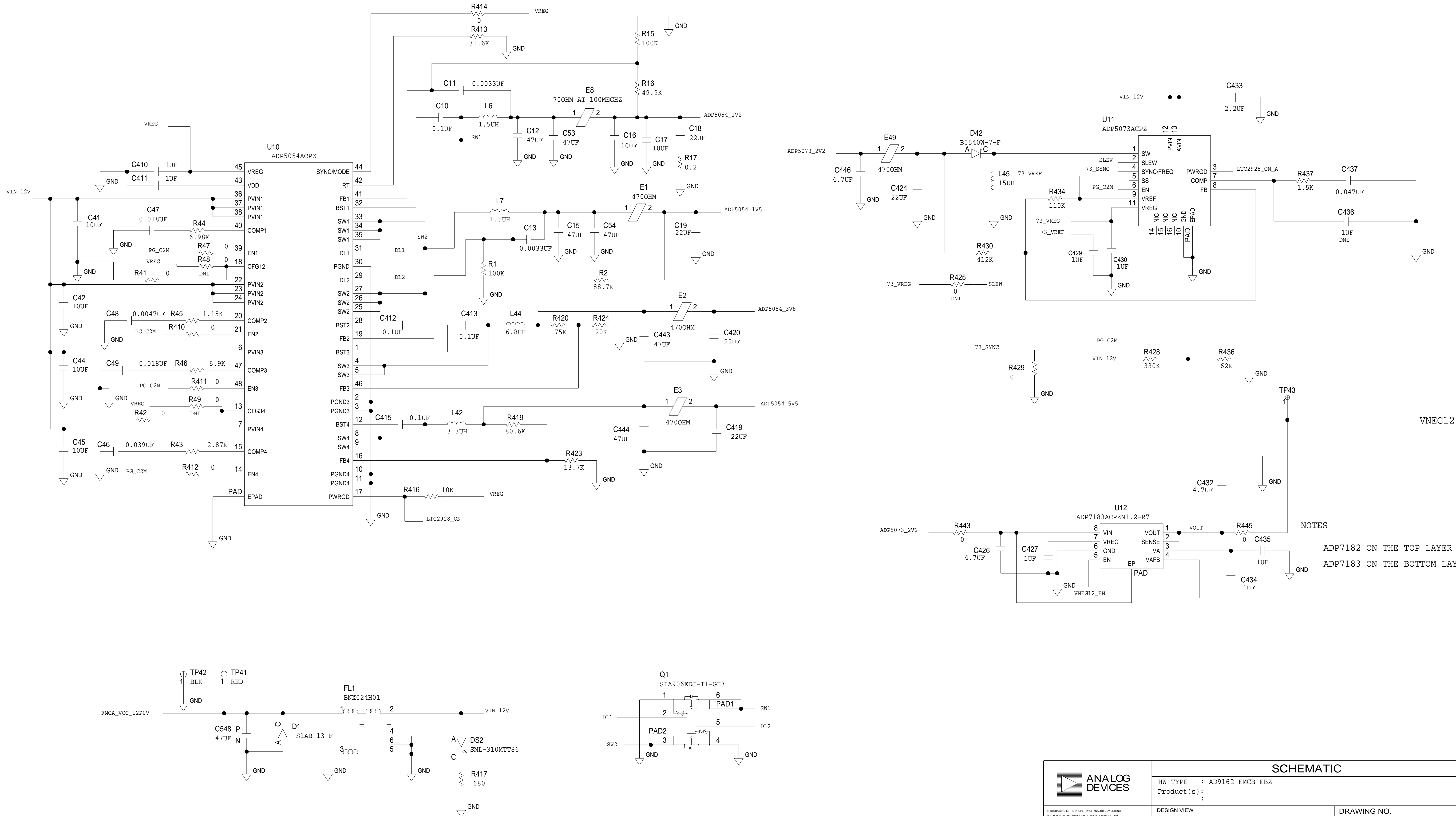
EXT CLK



SCHEMATIC			
HW TYPE : AD9162-FMCPB EBZ			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_046215	REV C	
PTD ENGINEER A. ALVARAN	SIZE D	SCALE 1:1	SHEET 3 OF 9

POWER - REGULATOR & NEG LDO

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

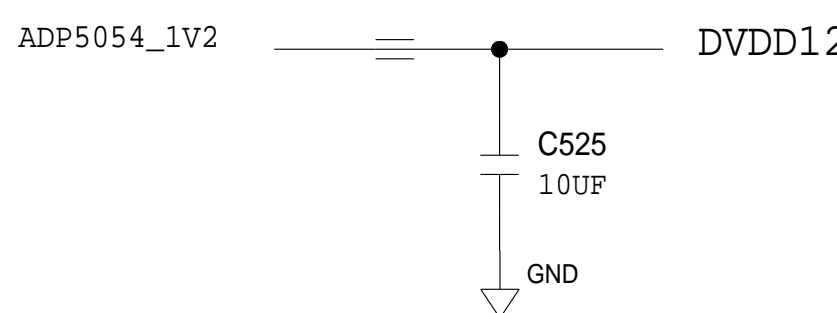
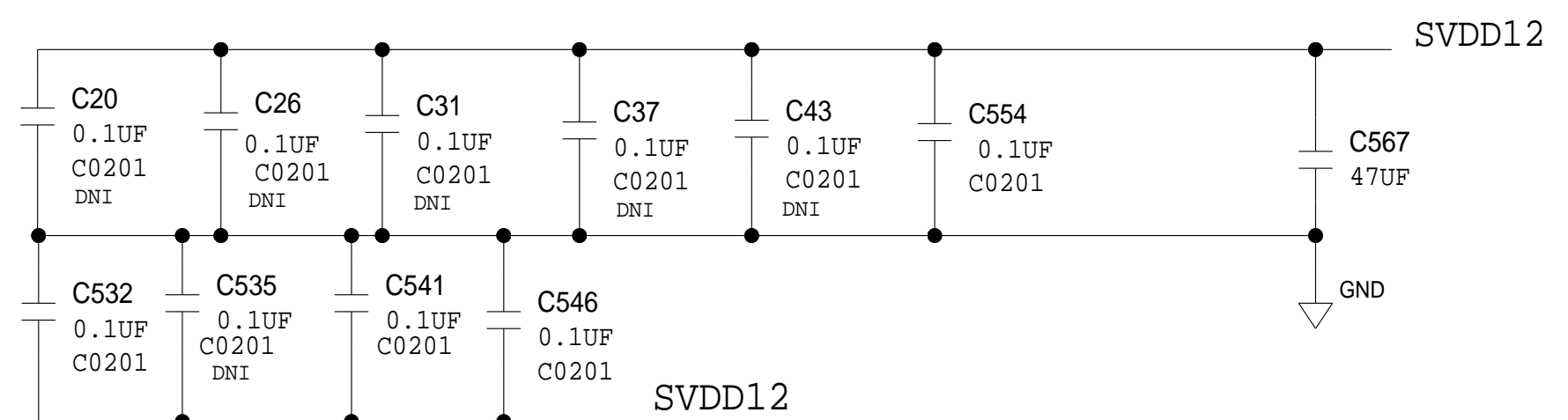
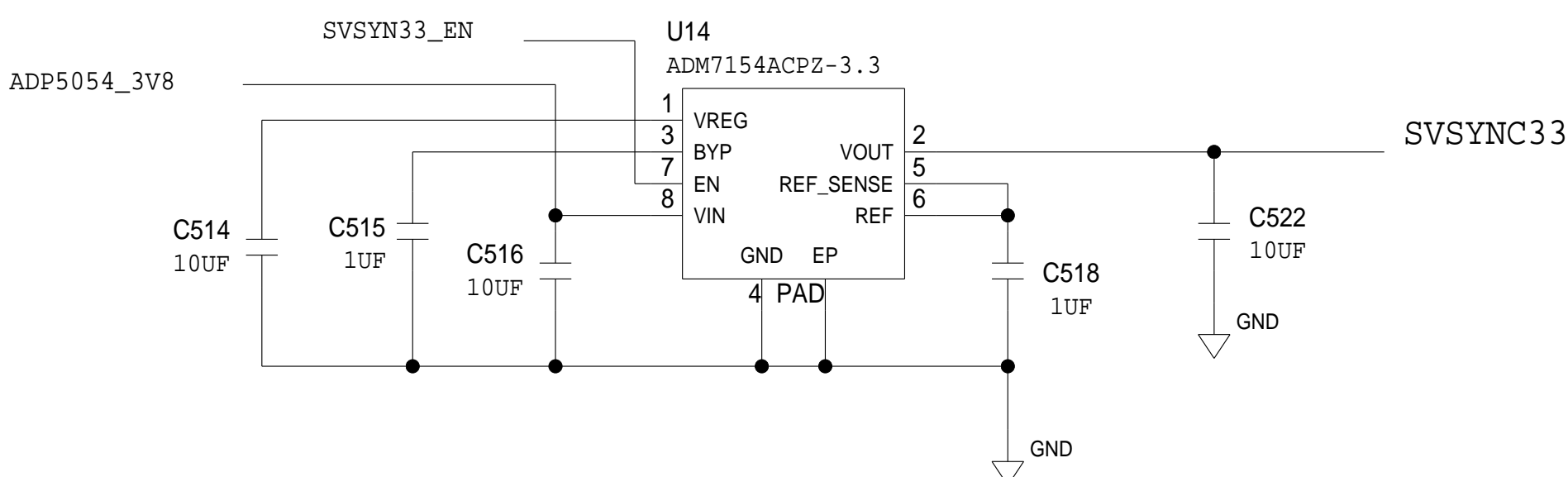
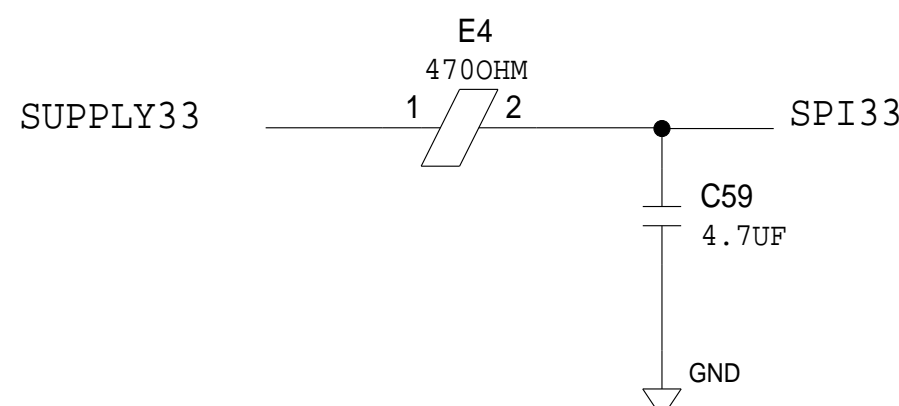
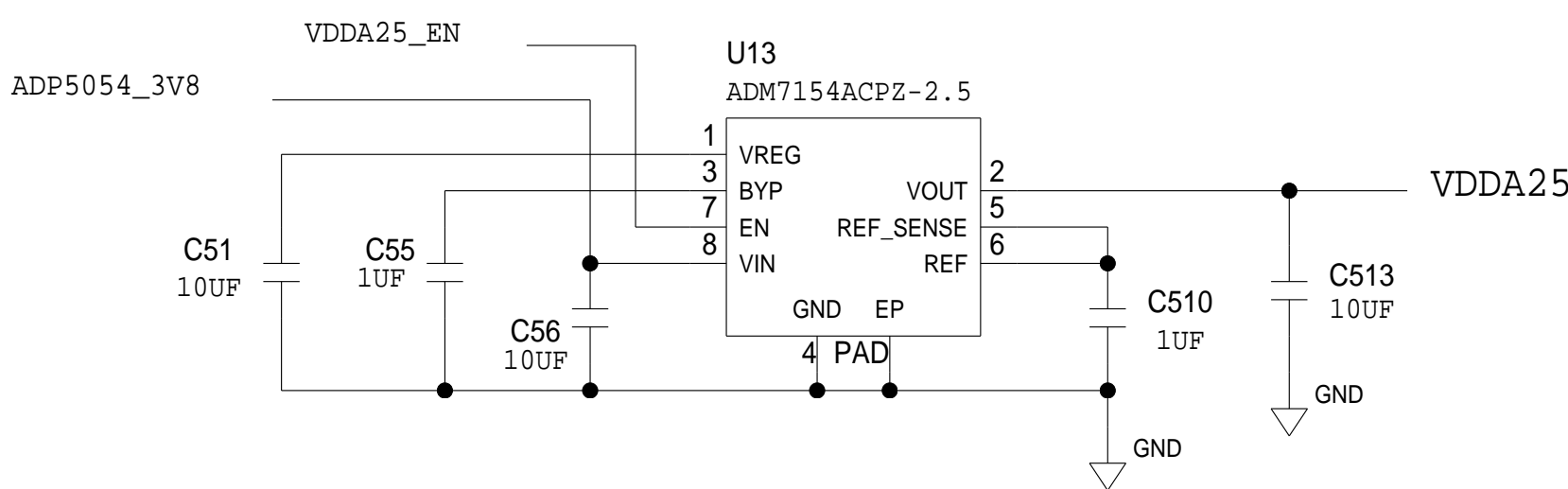
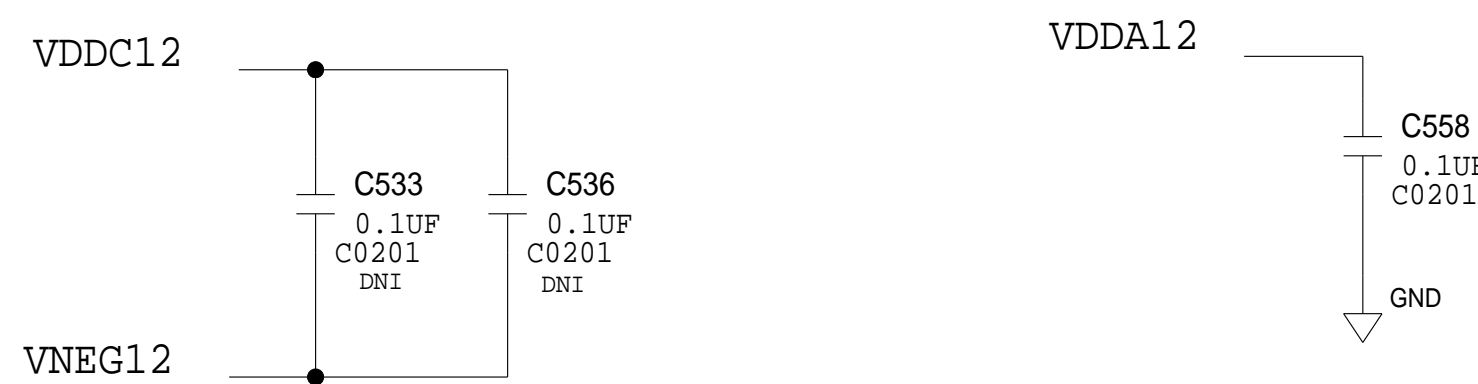
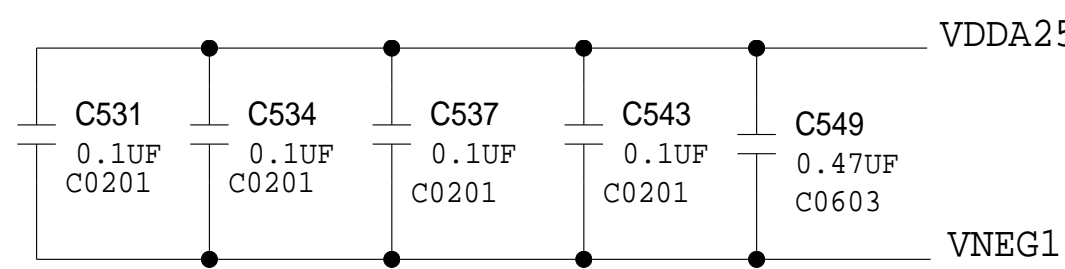
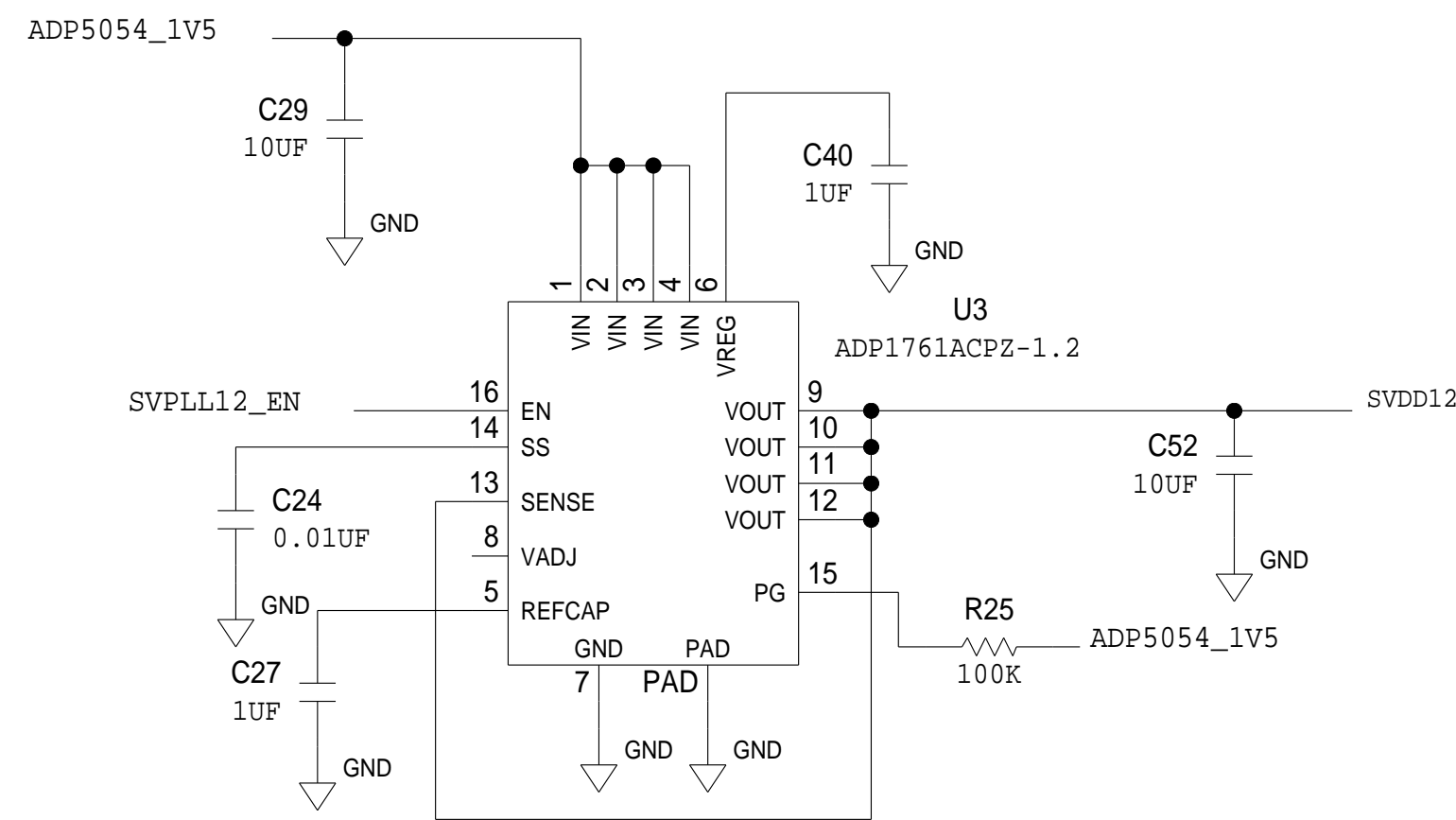
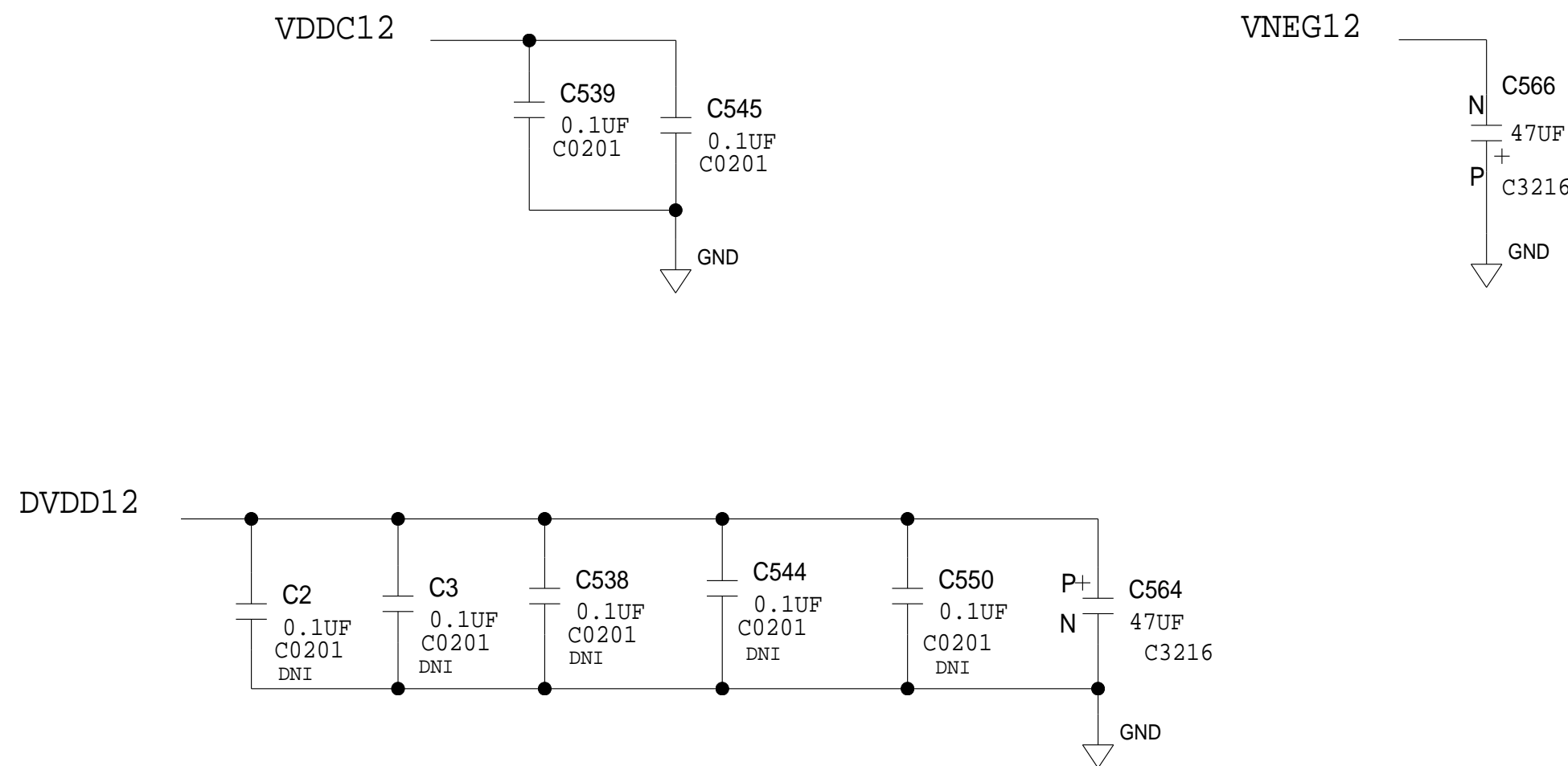
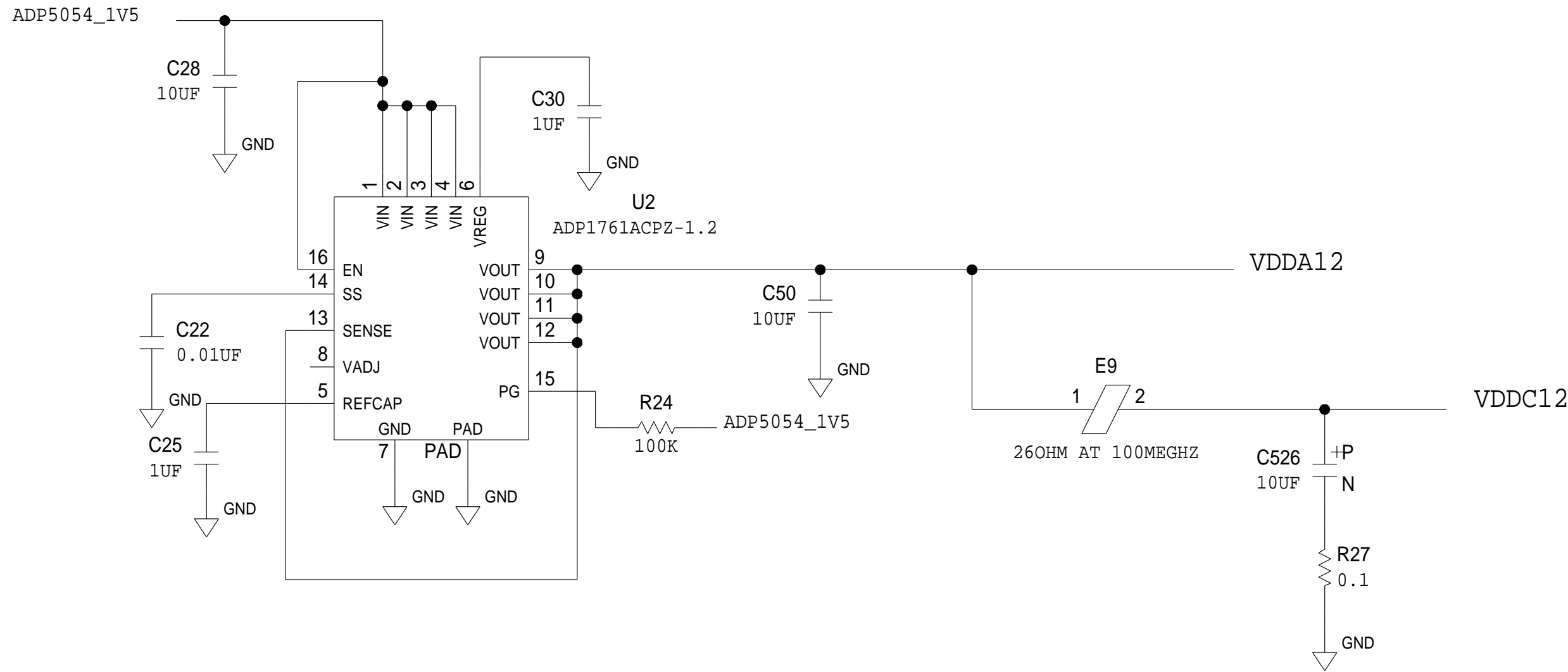


NOTES
ADP7182 ON THE TOP LAYER
ADP7183 ON THE BOTTOM LAYER

SCHEMATIC			
HW TYPE : AD9162-FWCB EBZ		DRAWING NO. 02_046215	
Product (s) :		REV C	
DESIGN VIEW <DESIGN_VIEW>		SIZE D	
PTD ENGINEER A. ALVARAN		SCALE 1:1	
		SHEET 4 OF 9	

POWER - LDO

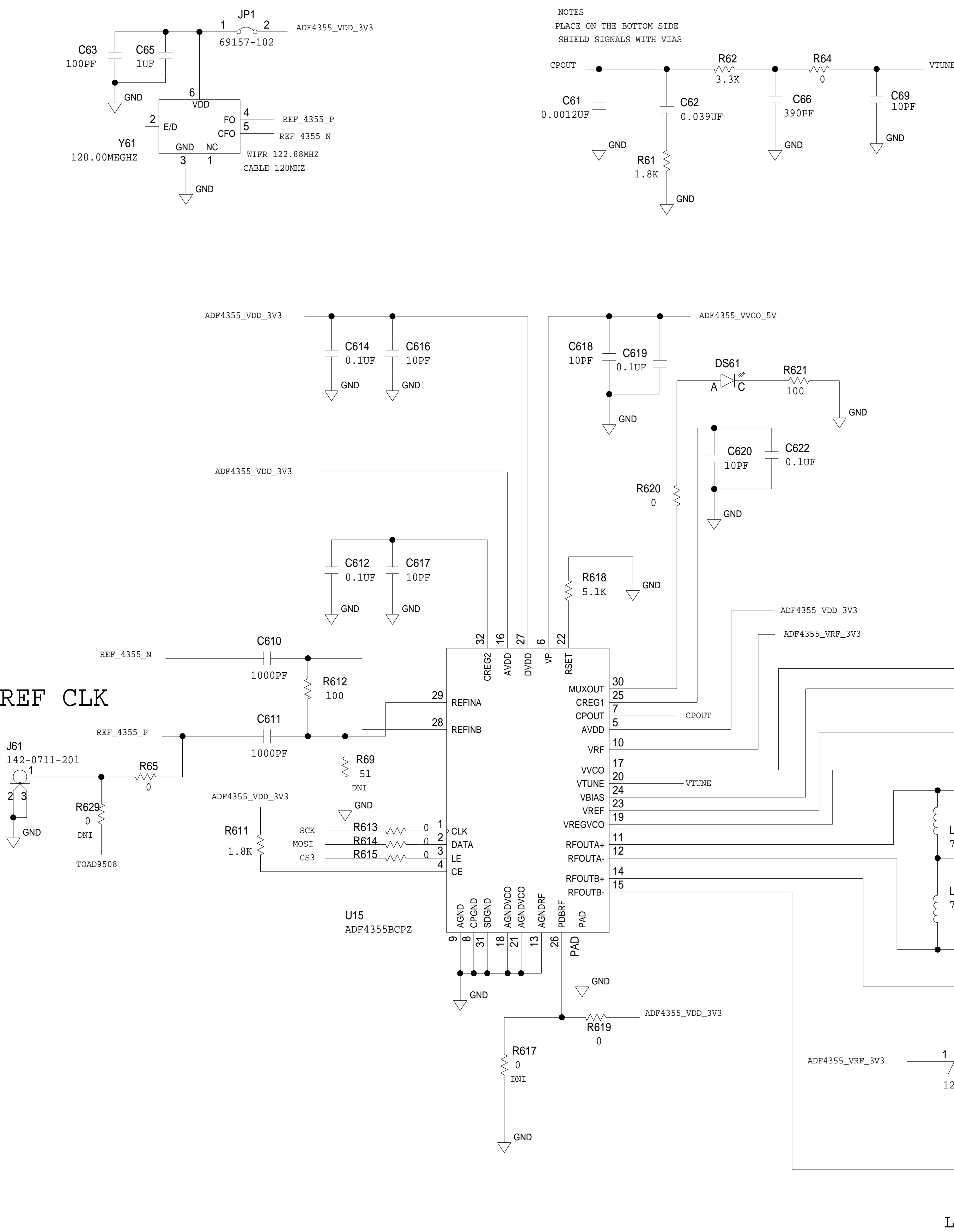
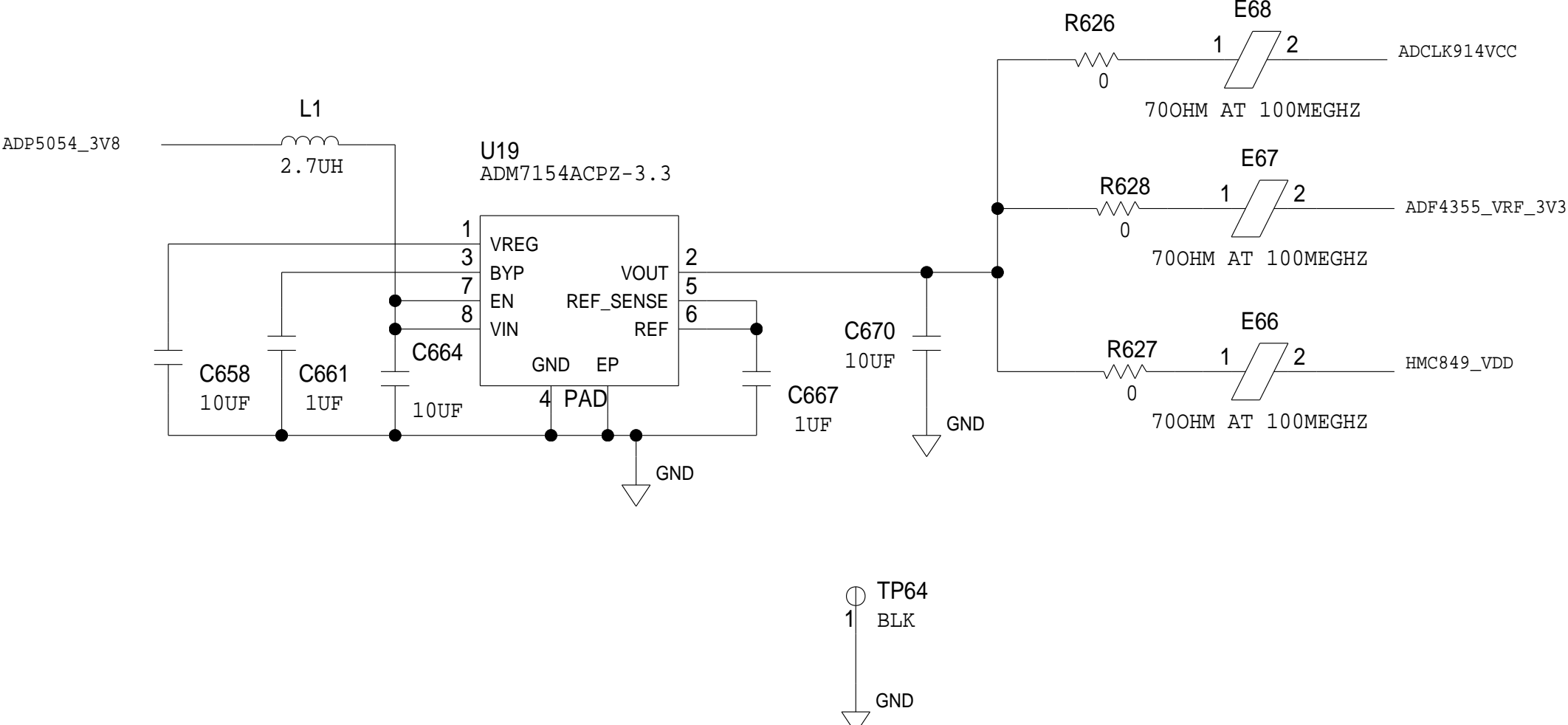
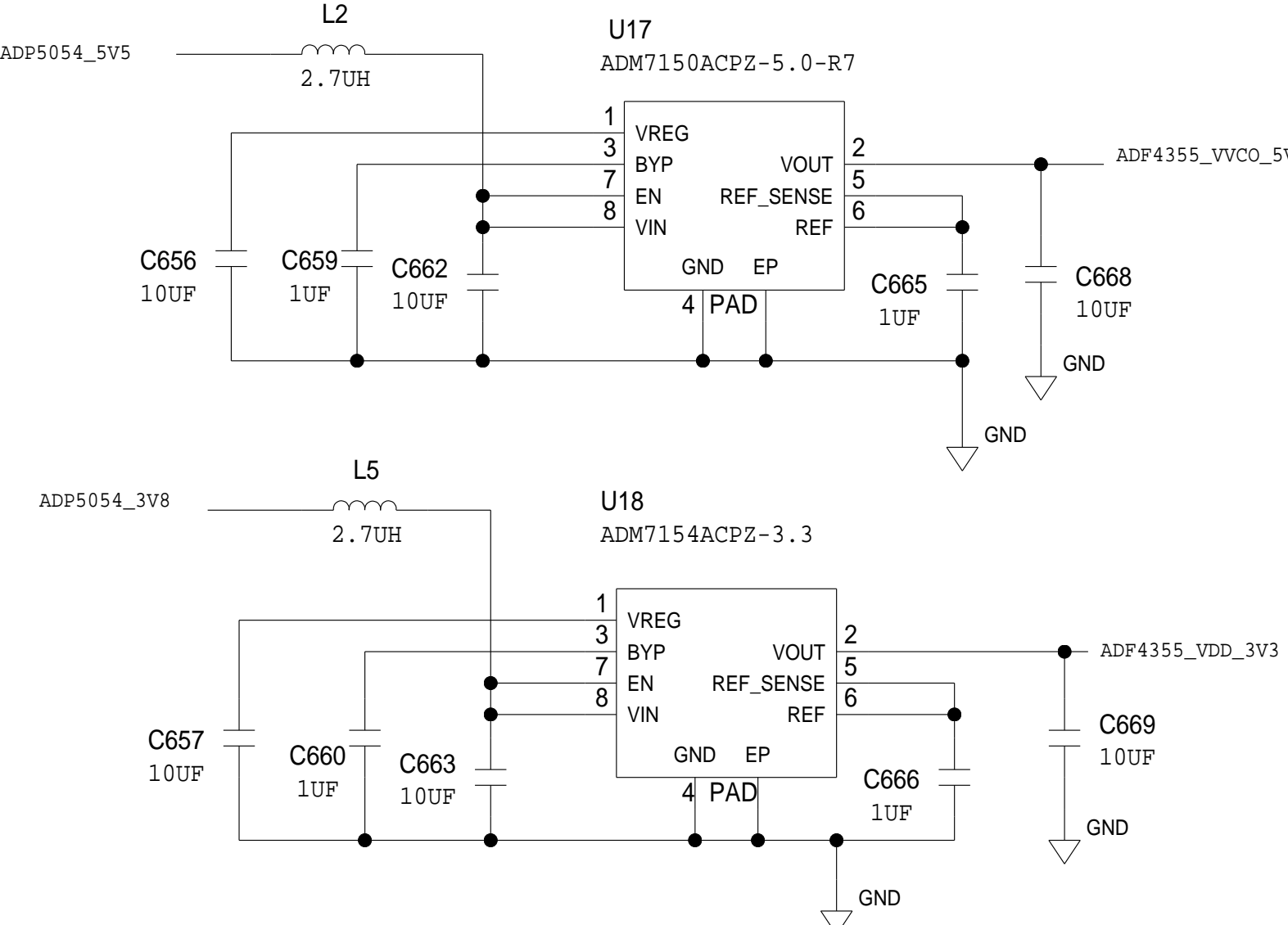
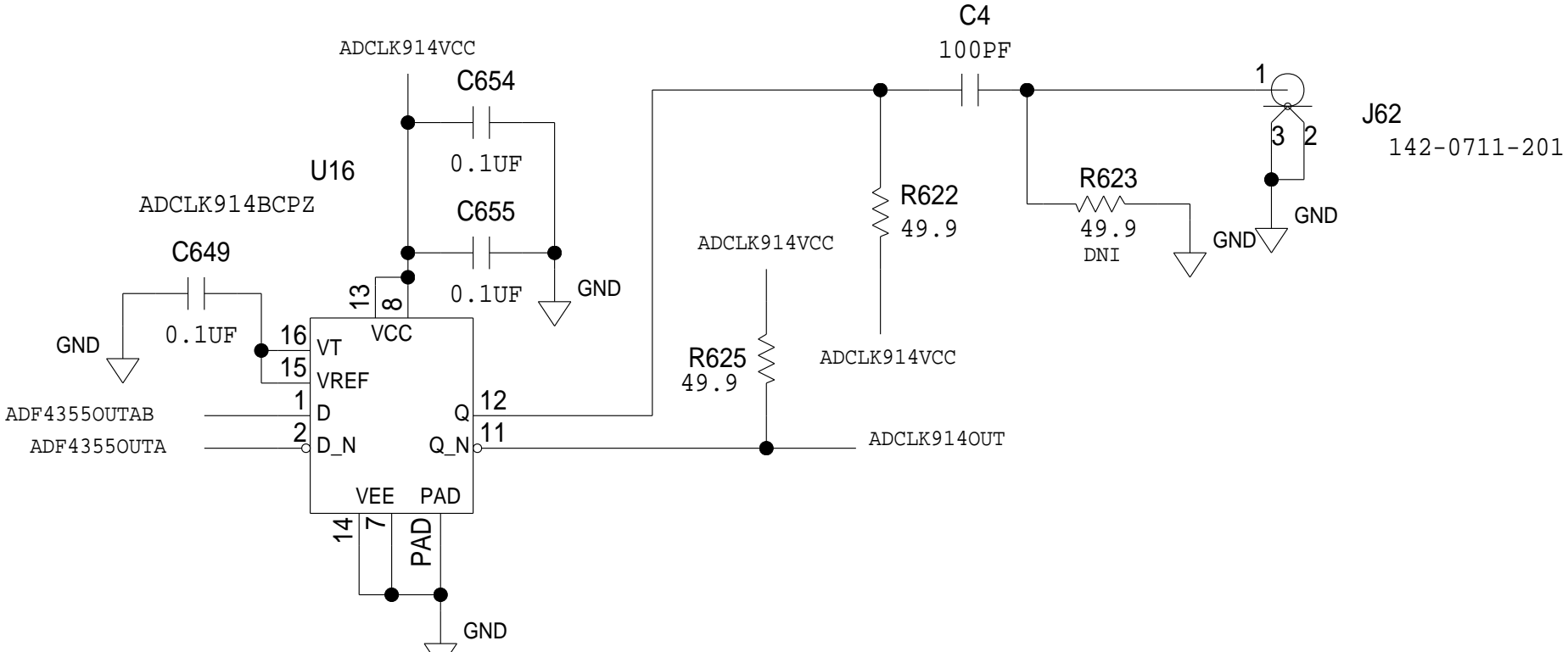
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED





ANALOG DEVICES	SCHEMATIC				
	HW TYPE : AD9162-FMBC EBZ Product(s) : :				
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	PTD ENGINEER A. ALVARAN		SIZE D	SCALE 1:1	SHEET 5 OF 9

ONBOARD CLK - ADF4355-2 & CLK BUFFER

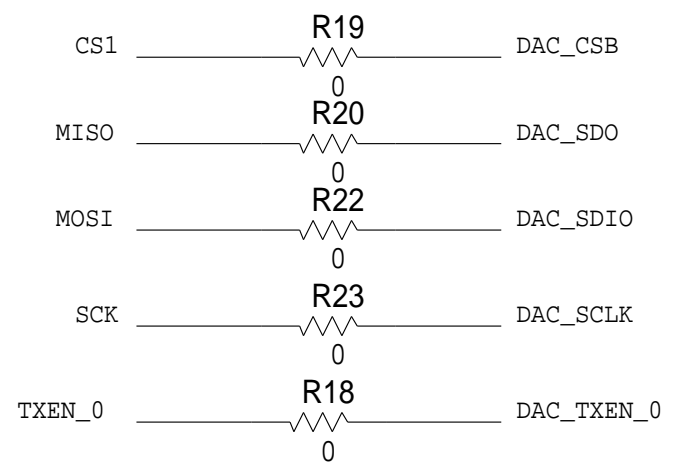
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



LO (ADF4355)

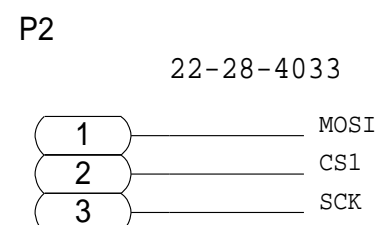
 ANALOG DEVICES	SCHEMATIC				
	HW TYPE : AD9162-FMBC EBZ Product(s) : :				
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	PTD ENGINEER A. ALVARAN	SIZE D	SCALE 1:1	SHEET 6	OF 9

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

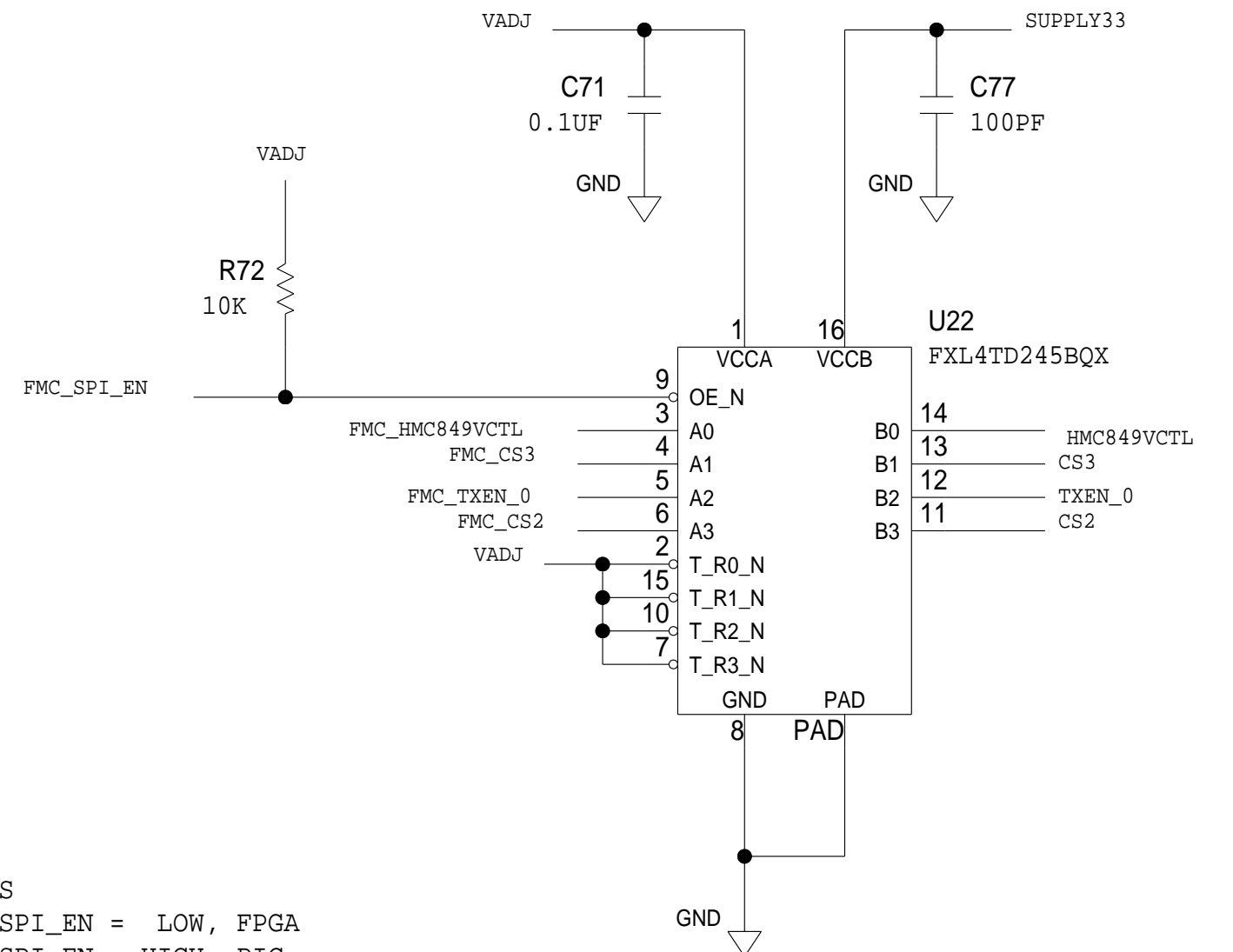


NOTE:

IF USB IS UNPLUGGED FMC INTERFACE IS THE SOURCE OF SPI BUS
AND TXEN SIGNAL.



```
S
SPI_EN = LOW, FPGA
SPI_EN = HIGH, PIC
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SCHEMATIC

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HW TYPE      : AD9162-FMCB EBZ
Product(s):
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DESIGN VIEW
<DESIGN_VIEW>

PTD ENGINEER
A. ALVARAN

DRAWING NO.
02 046215

SIZE D	SCALE 1:1	
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REV
C

SHEET 8 OF 9

