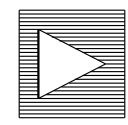
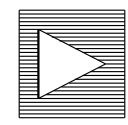
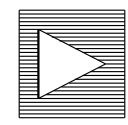
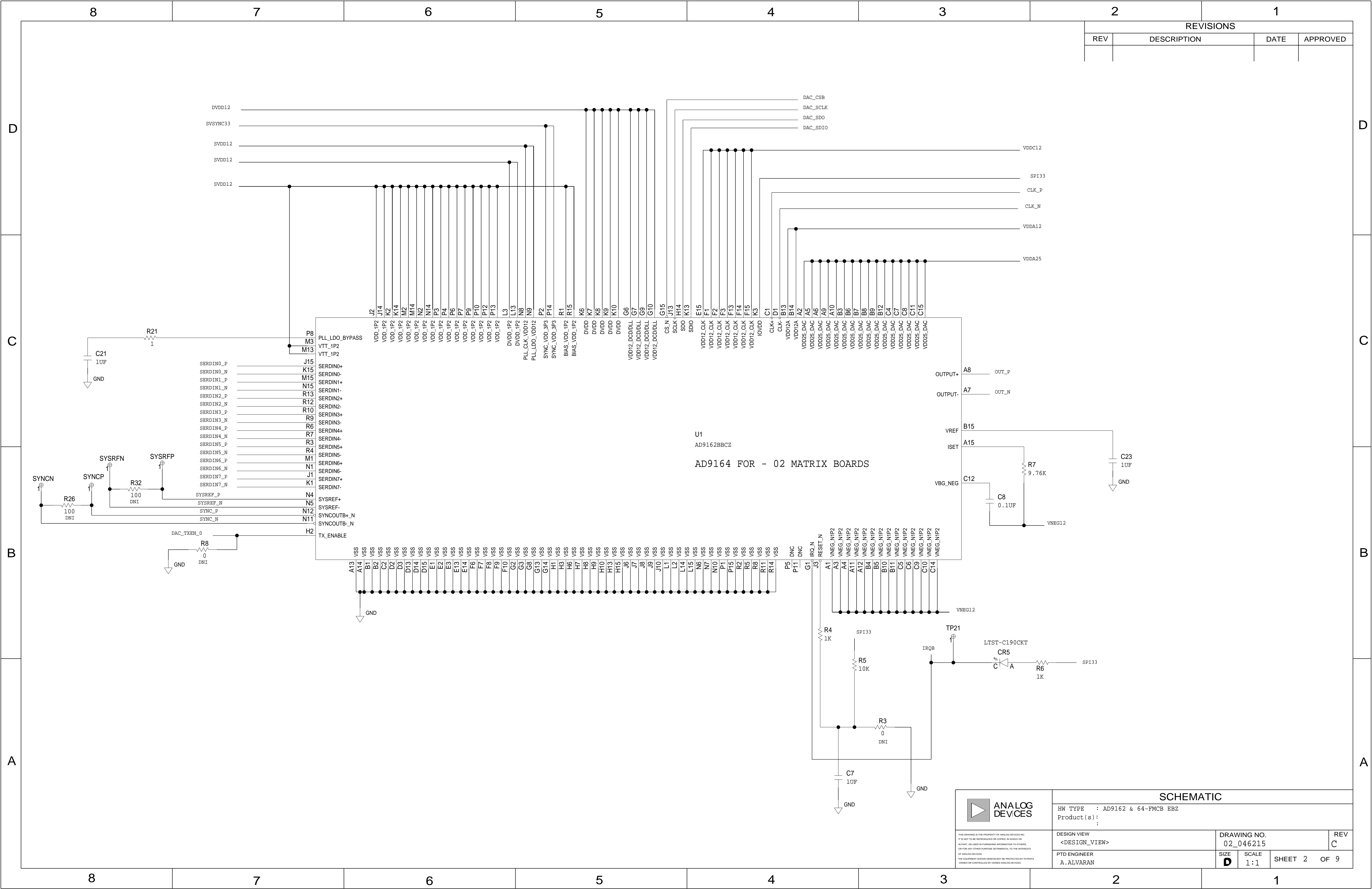
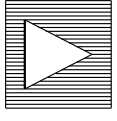


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D	THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED, IN WHOLE OR IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS, OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS OF ANALOG DEVICES. THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY ANALOG DEVICES.					<table><tr><th colspan="3">JUMPER TABLE</th></tr><tr><th>JP#</th><th>ON</th><th>OFF</th></tr><tr><td>1</td><td></td><td></td></tr><tr><td>2</td><td></td><td></td></tr><tr><td>3</td><td></td><td></td></tr><tr><td>4</td><td></td><td></td></tr><tr><td>5</td><td></td><td></td></tr></table>			JUMPER TABLE			JP#	ON	OFF	1			2			3			4			5			<table><tr><th colspan="4">REVISIONS</th></tr><tr><th>REV</th><th>DESCRIPTION</th><th>DATE</th><th>APPROVED</th></tr><tr><td>C</td><td>REMOVE T32 R44 C318</td><td>2/1/2018</td><td>S B.</td></tr></table>										REVISIONS				REV	DESCRIPTION	DATE	APPROVED	C	REMOVE T32 R44 C318	2/1/2018	S B.																																																																																														
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REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



**ANALOG
DEVICES**

SCHEMATIC

HW TYPE : AD9162 & 64-FMCMC EBZ

Product(s) :

DESIGN VIEW
<DESIGN_VIEW>

PTD ENGINEER
A. ALVARAN

DRAWING NO.
02_046215

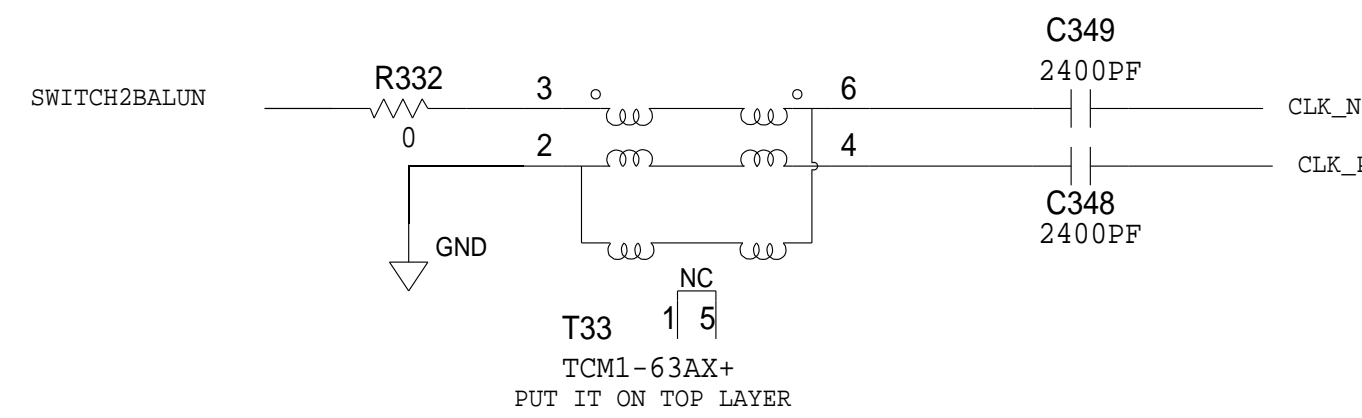
SIZE **D**

REV
C

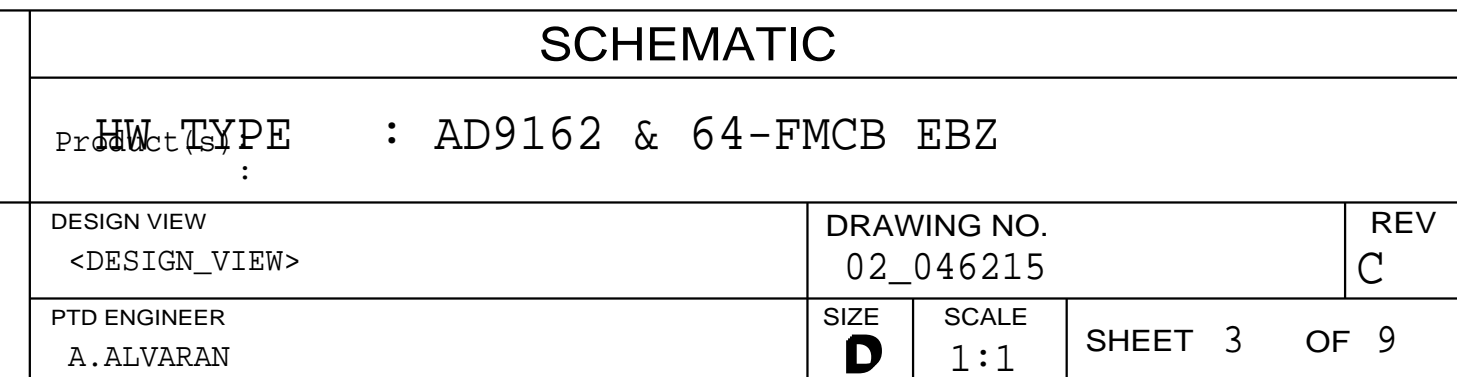
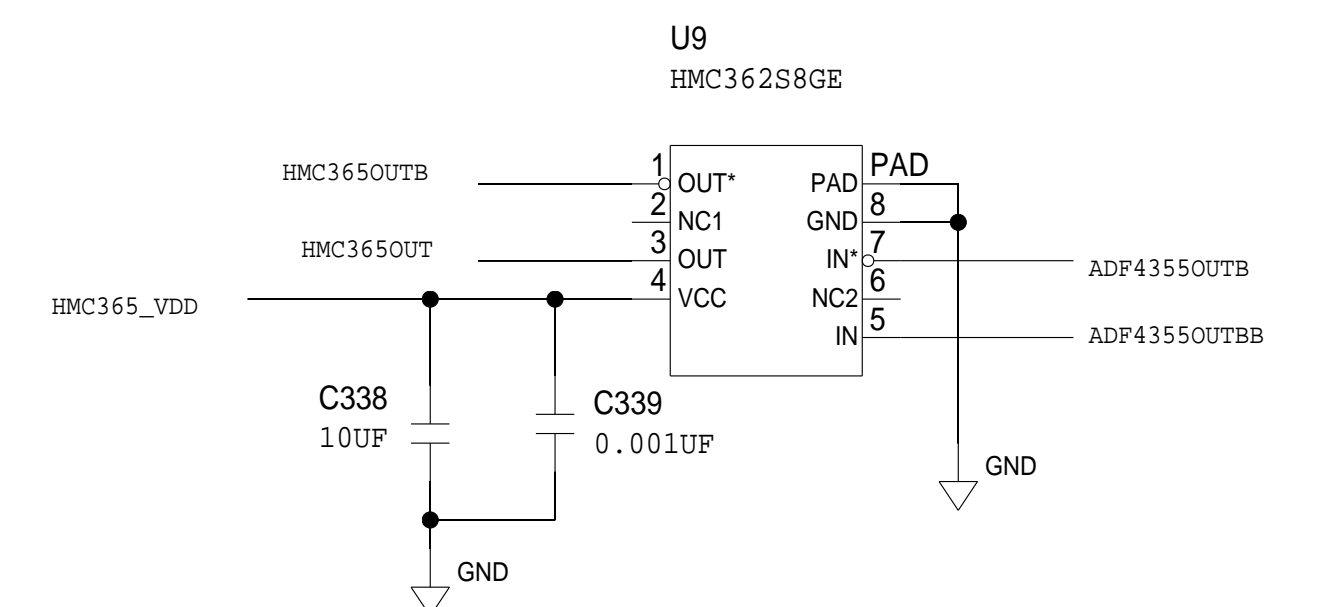
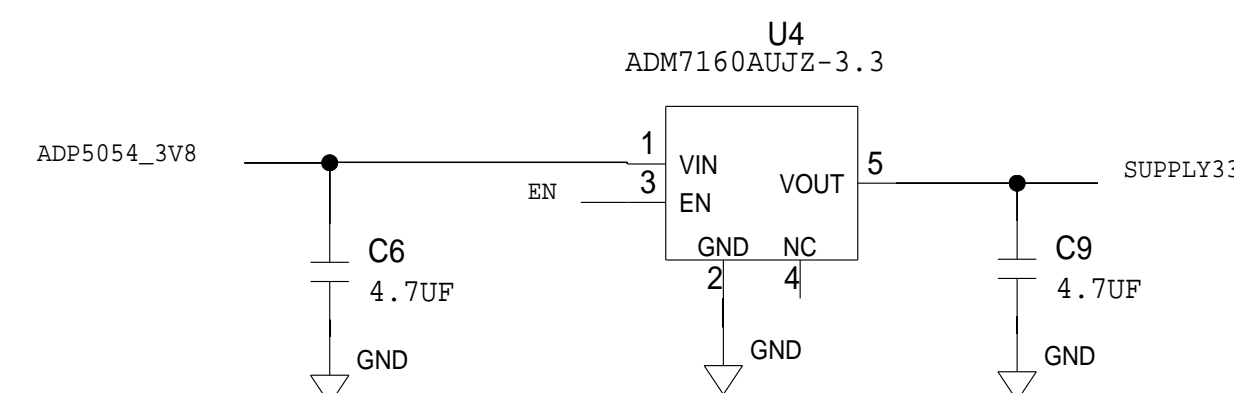
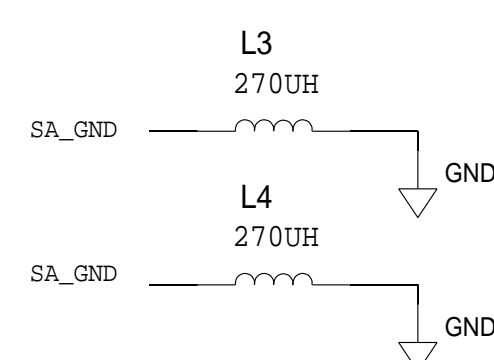
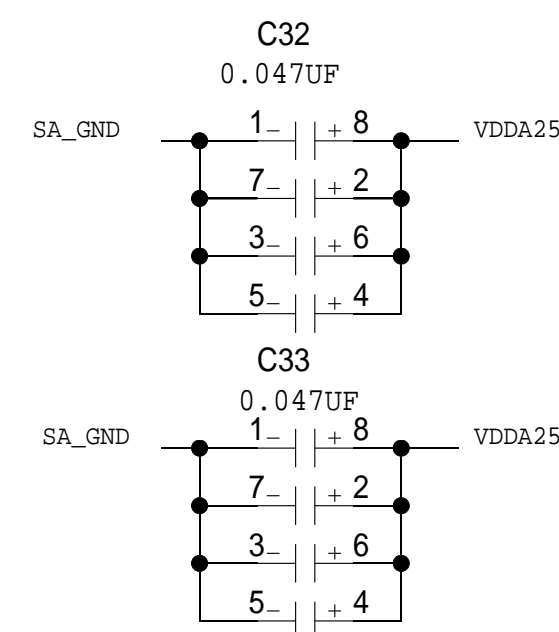
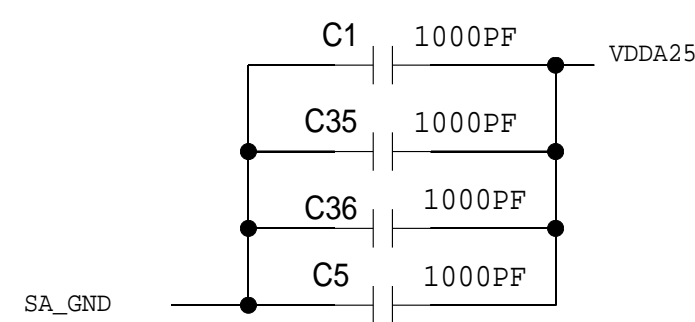
SCALE
1:1

SHEET 2 OF 9

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



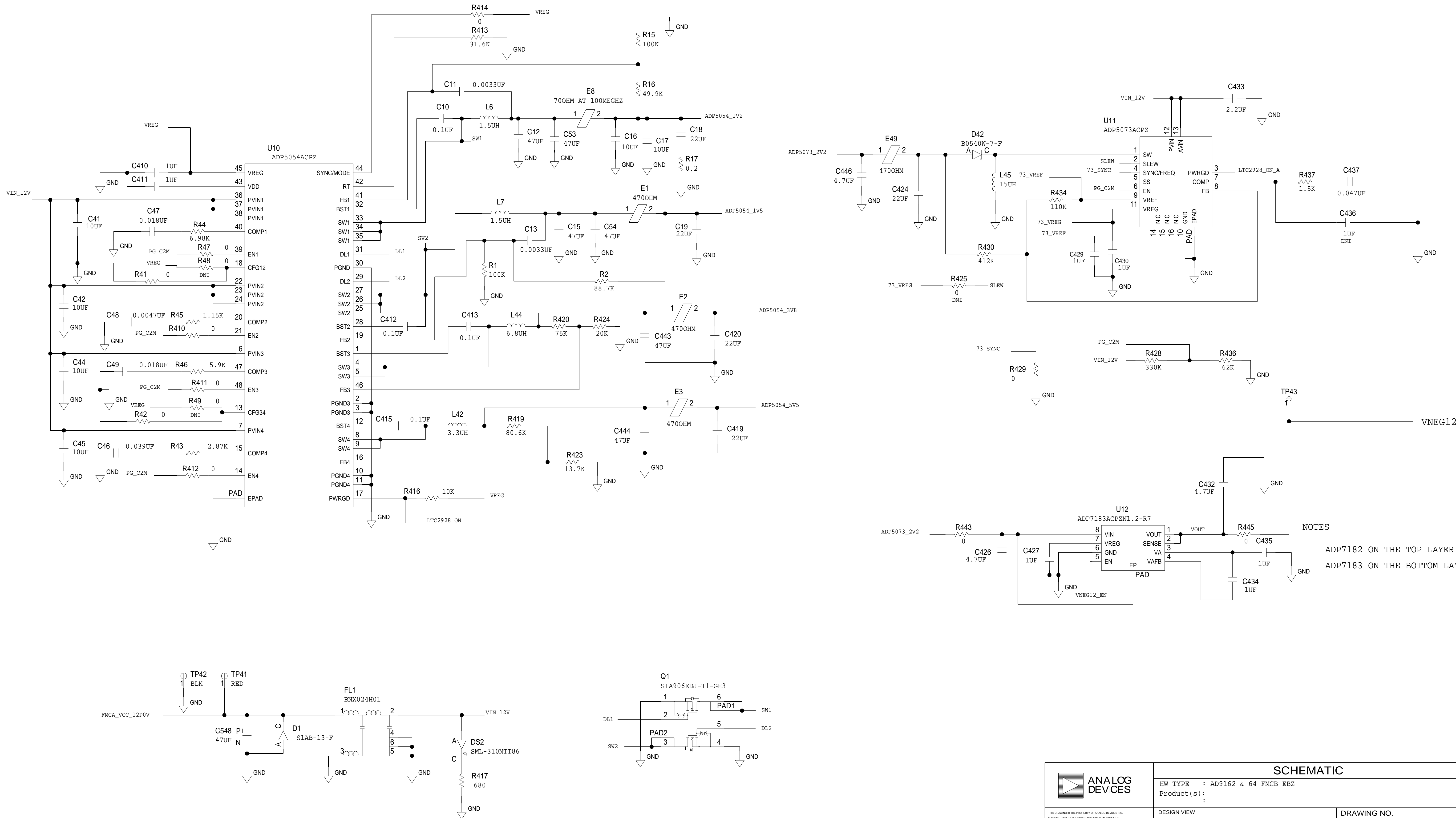
OUT



POWER - REGULATOR & NEG LDO

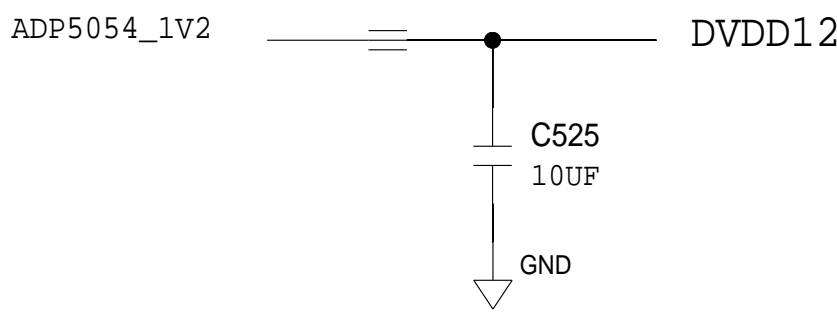
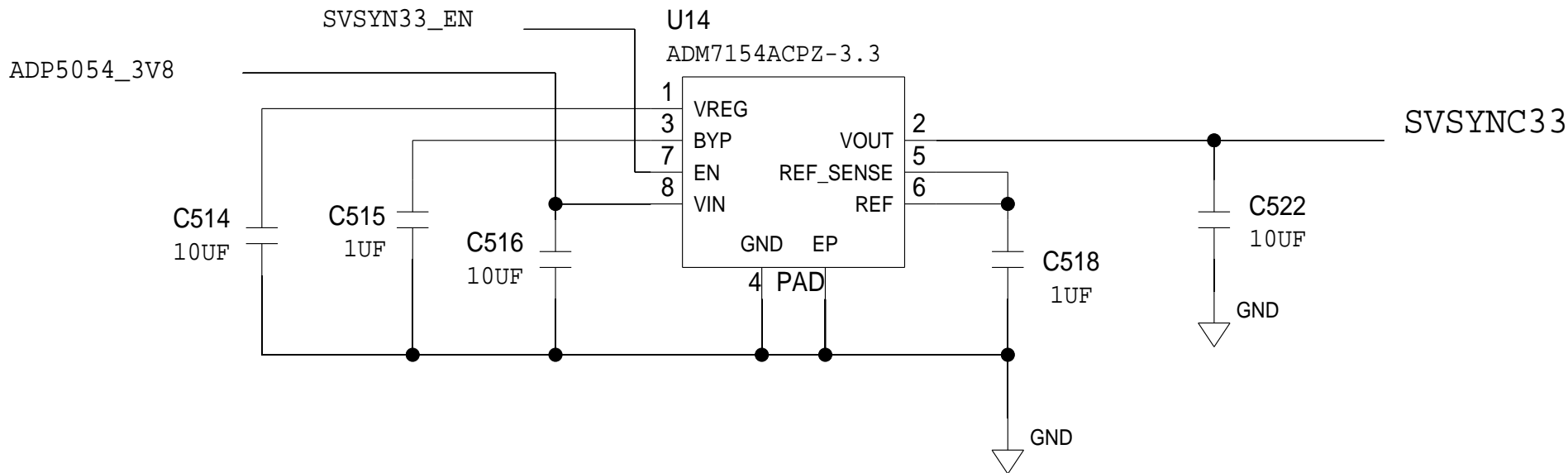
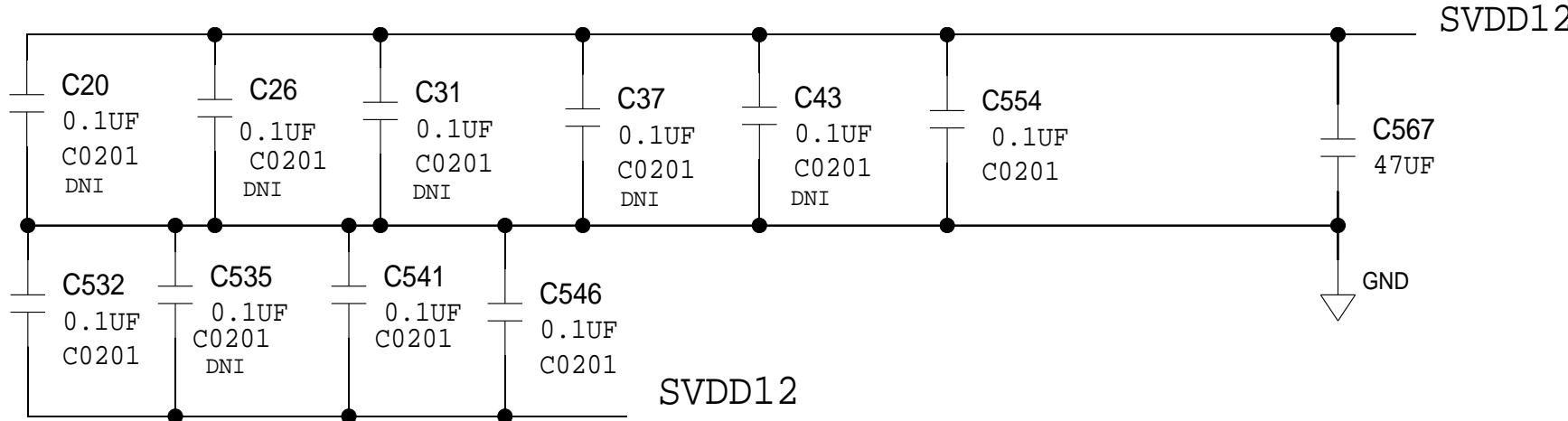
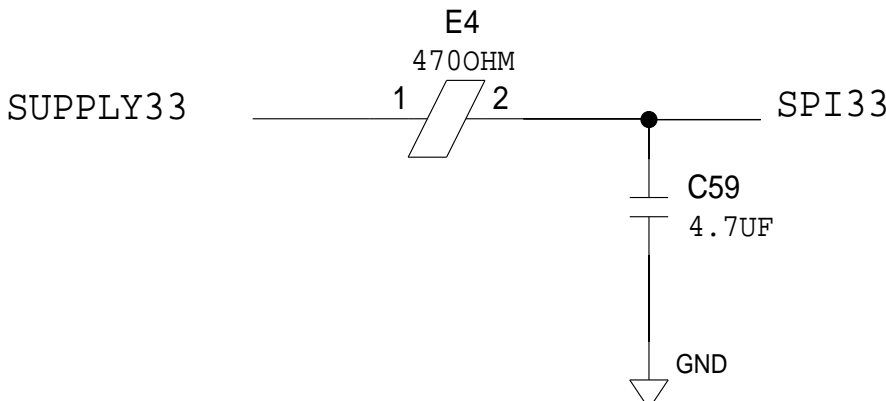
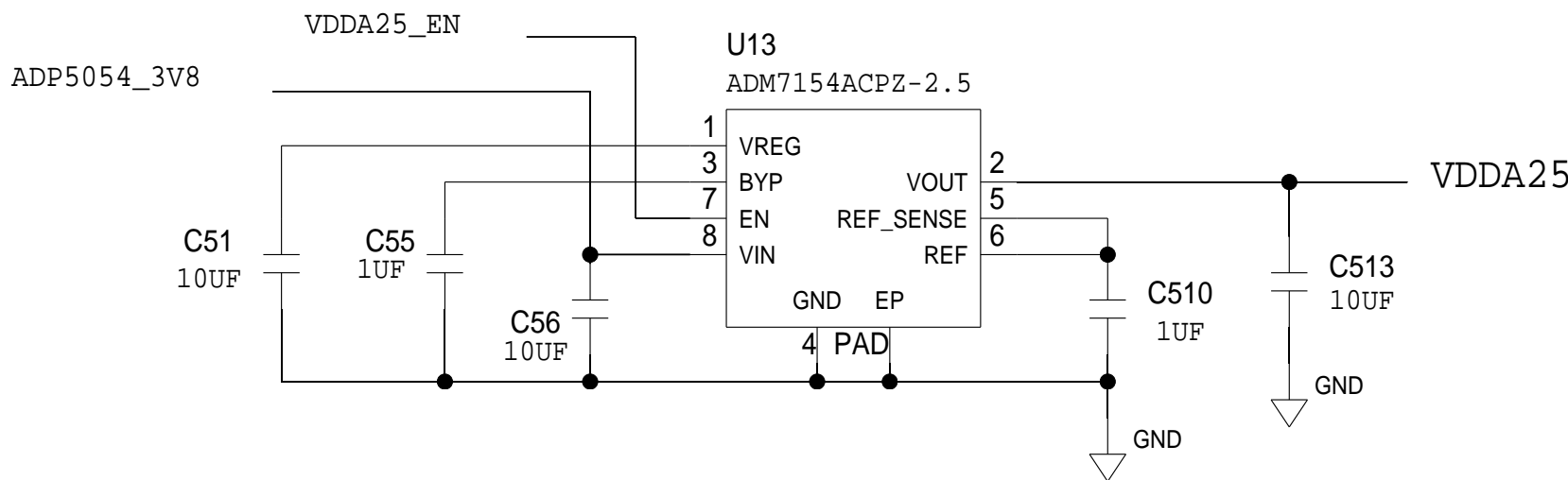
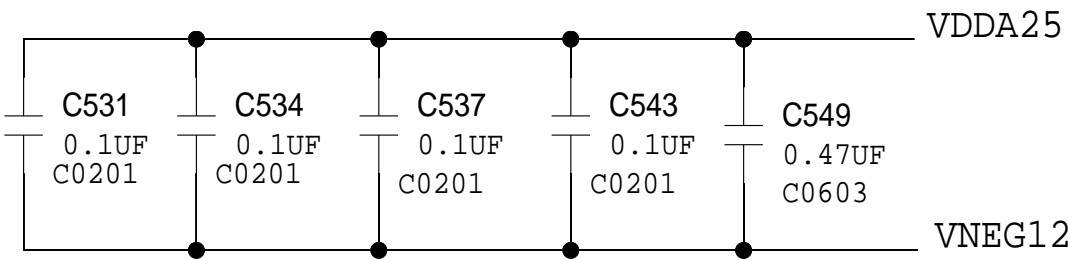
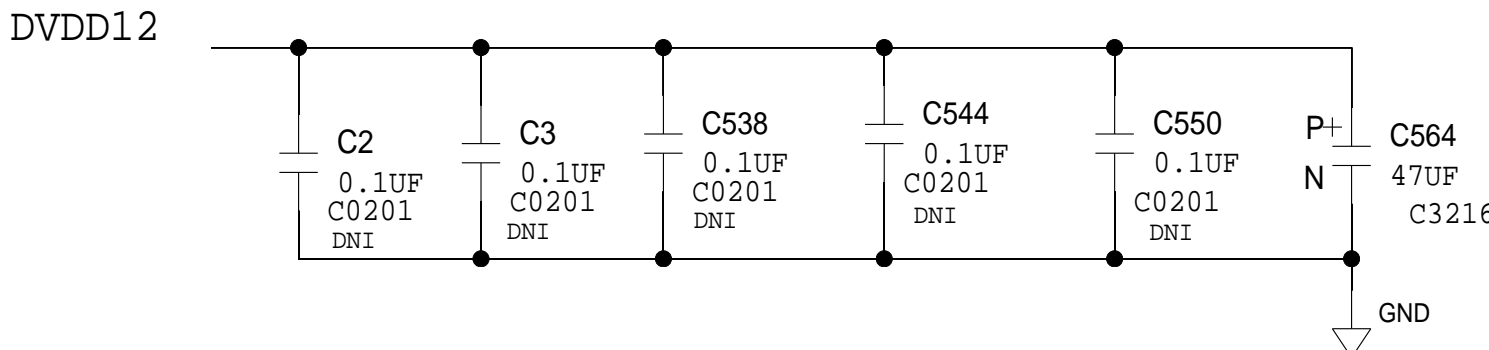
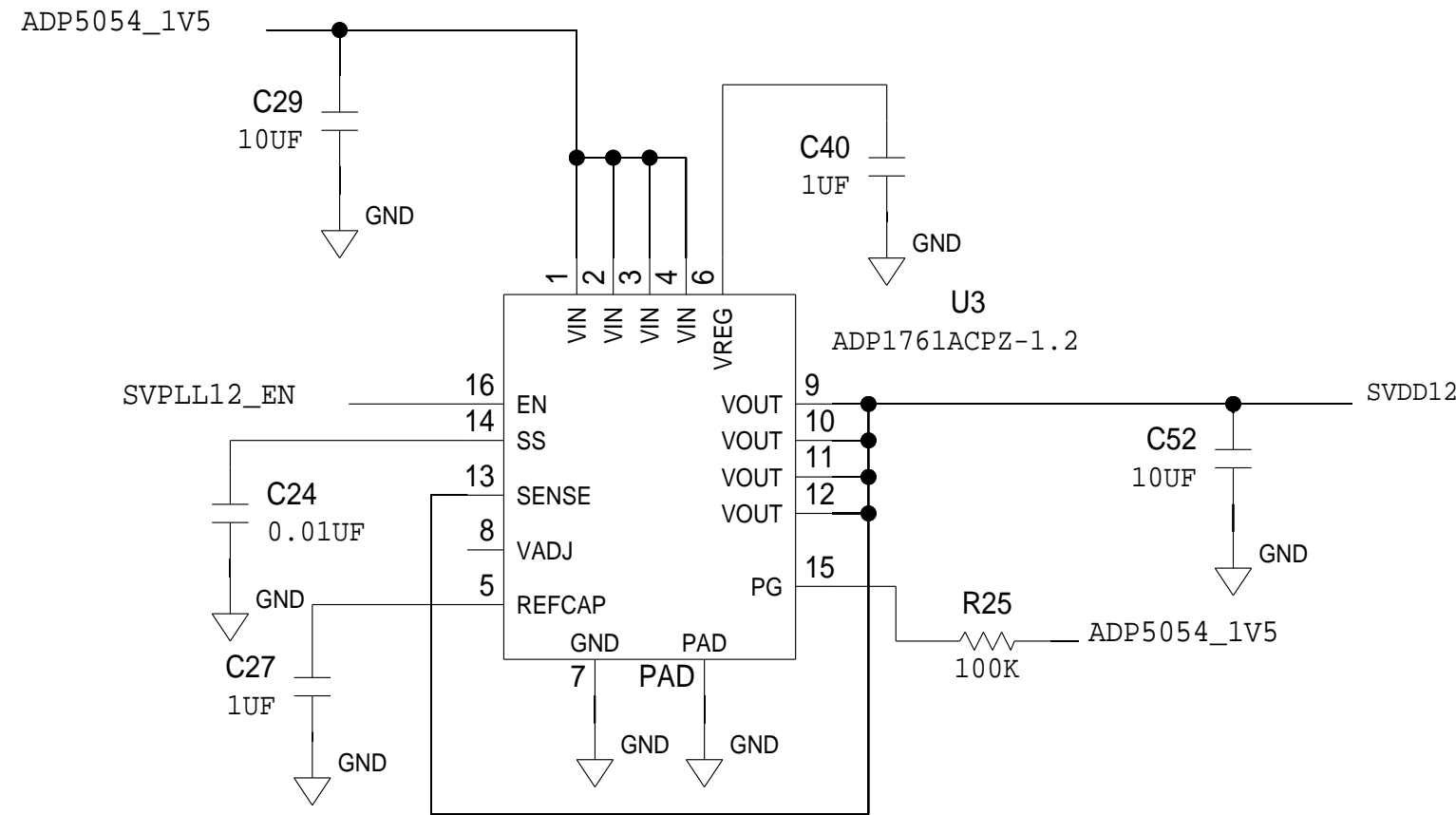
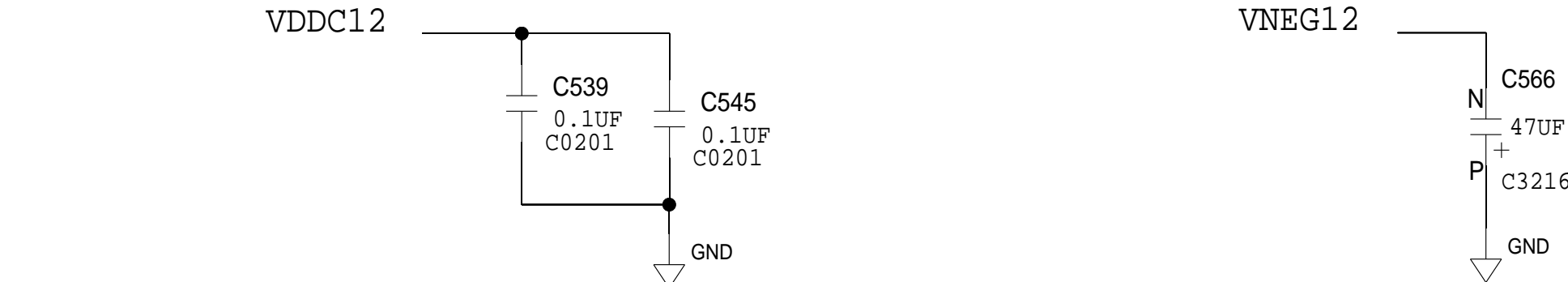
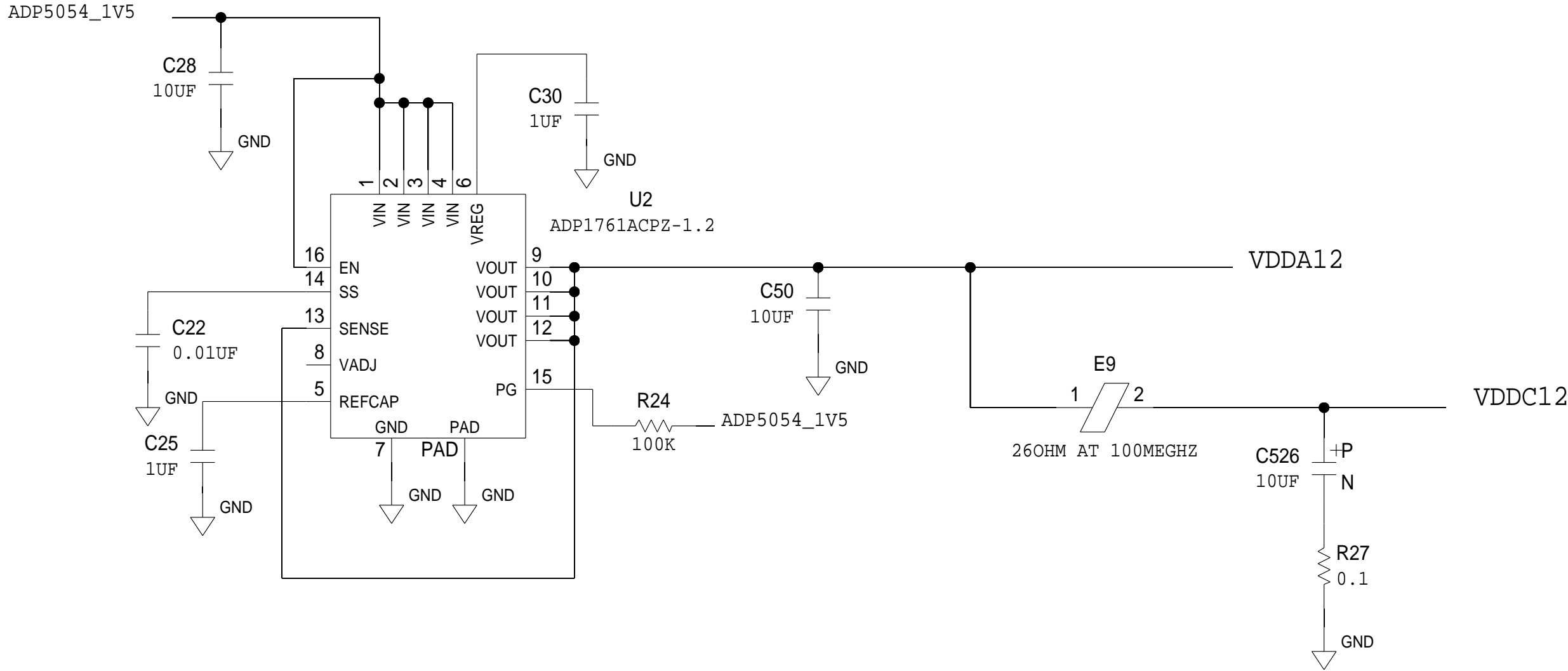
REVISIONS

REV	DESCRIPTION	DATE	APPROVED



POWER - LDO

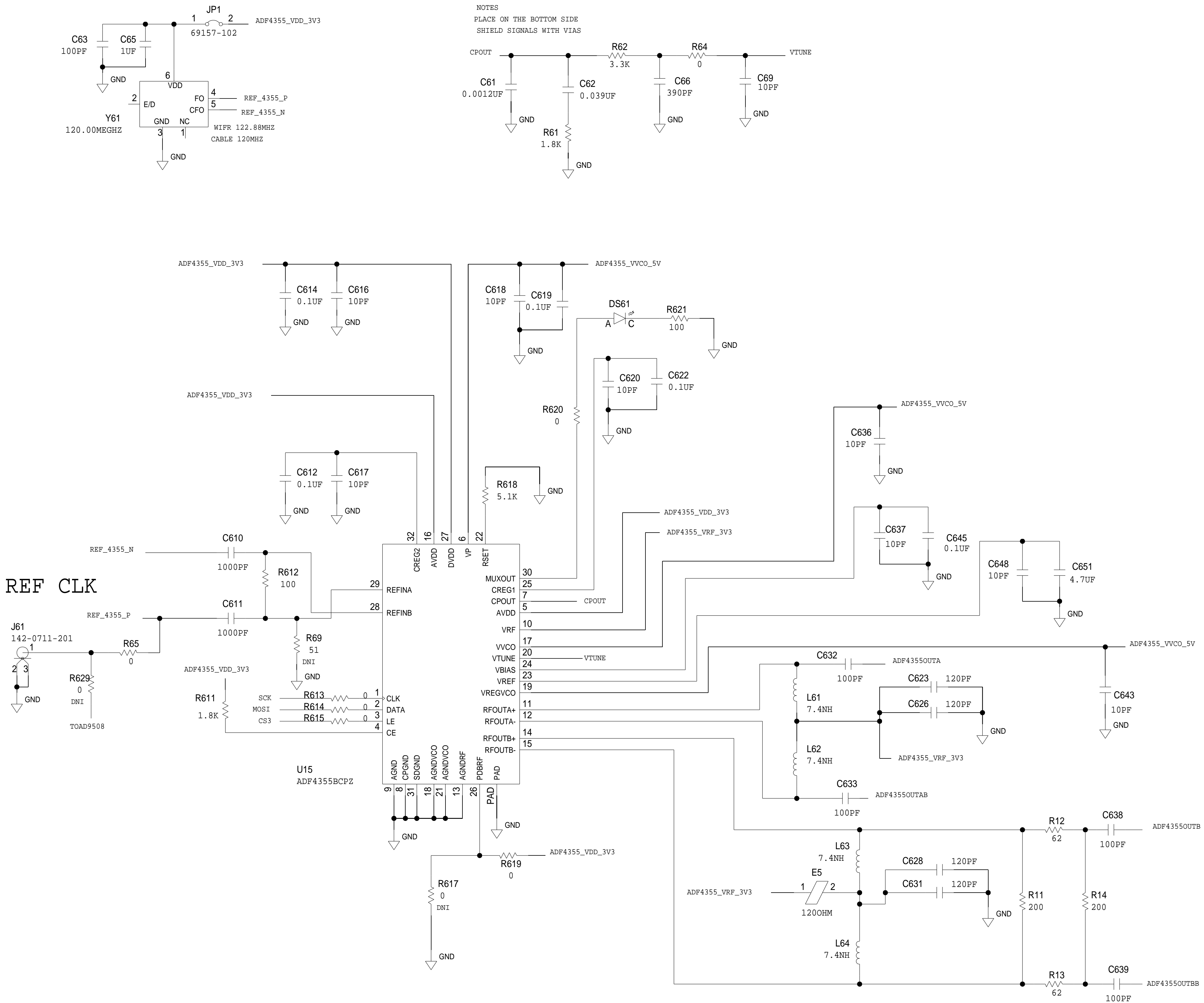
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

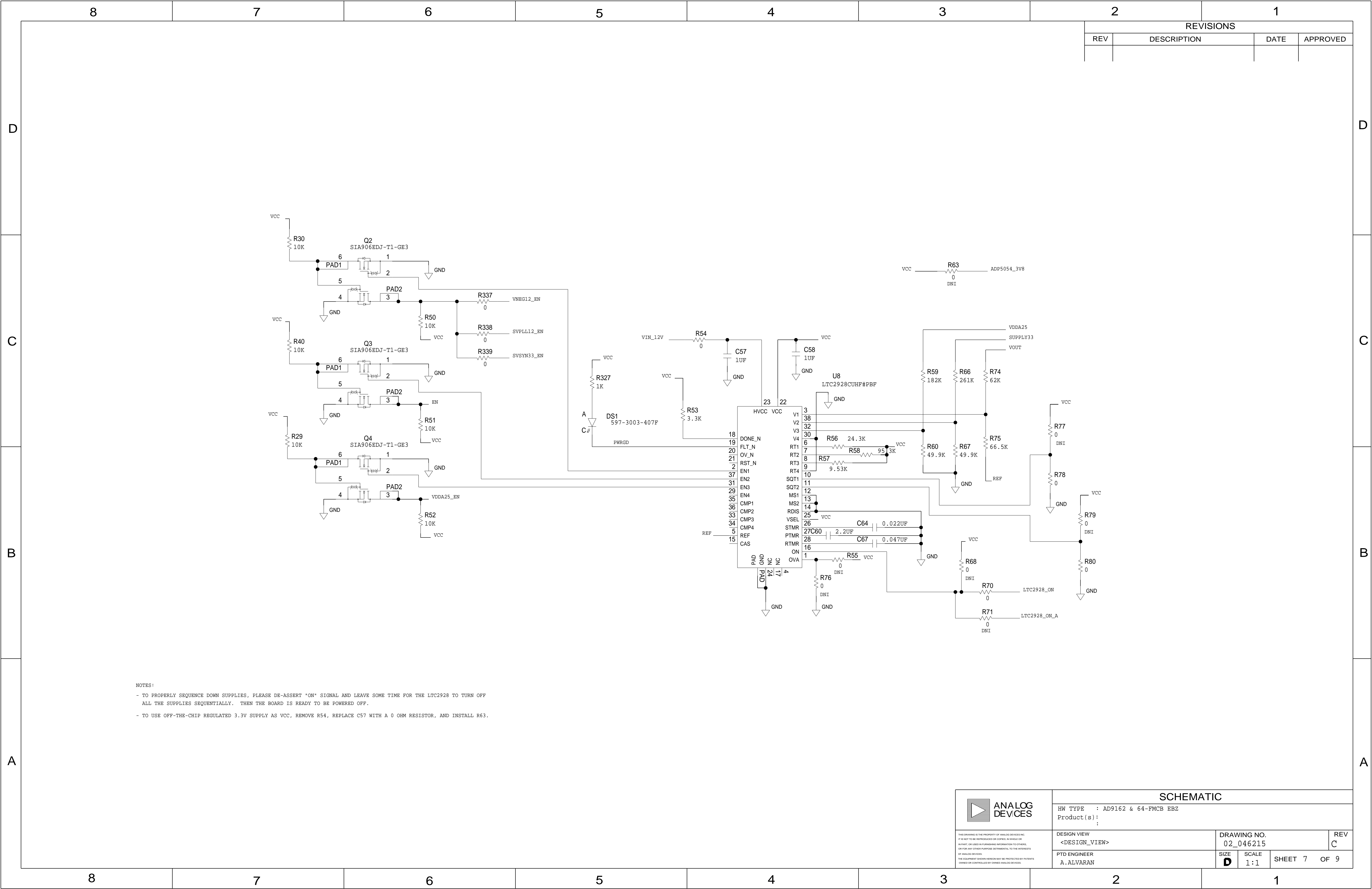


SCHEMATIC			
HW TYPE : AD9162 & 64-FMCB EBZ		DRAWING NO. 02_046215	
Product(s) :		REV C	
DESIGN VIEW <DESIGN_VIEW>		SIZE D	
PTD ENGINEER A. ALVARAN		SCALE 1:1	
		SHEET 5 OF 9	

ONBOARD CLK - ADF4355-2 & CLK BUFFER

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

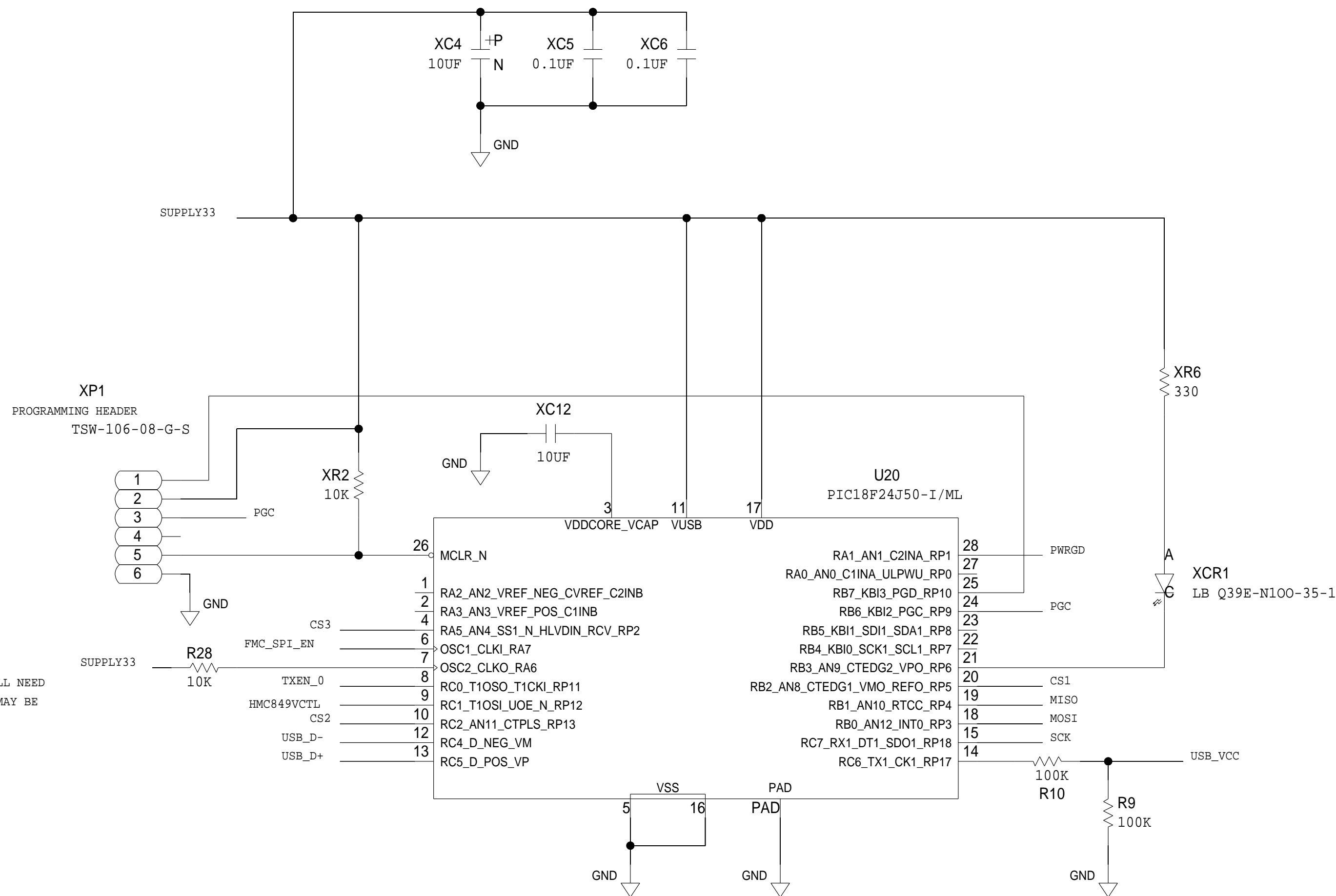




GPIO & MISC

REVISIONS

REV	DESCRIPTION	DATE	APPROVED

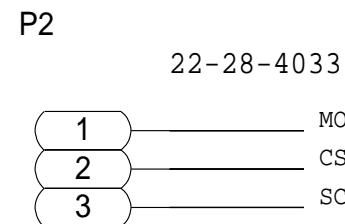
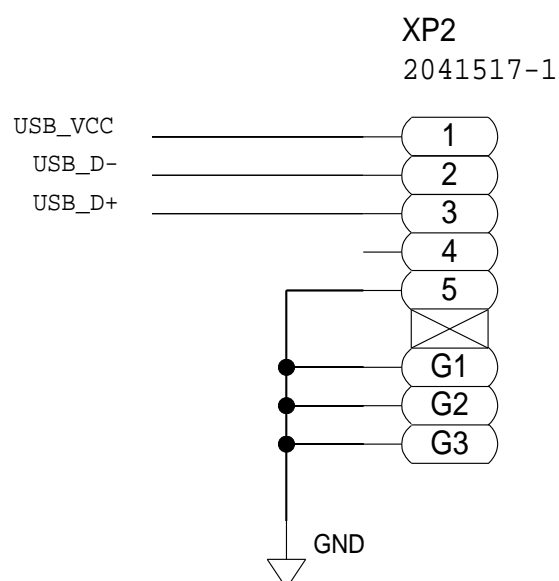


IMPLEMENTATION NOTE: FMC_SPI_EN WILL NEED TO BE SAMPLED AS ANALOG, SINCE IT MAY BE AS LOW AS 1.2V LOGIC HIGH.

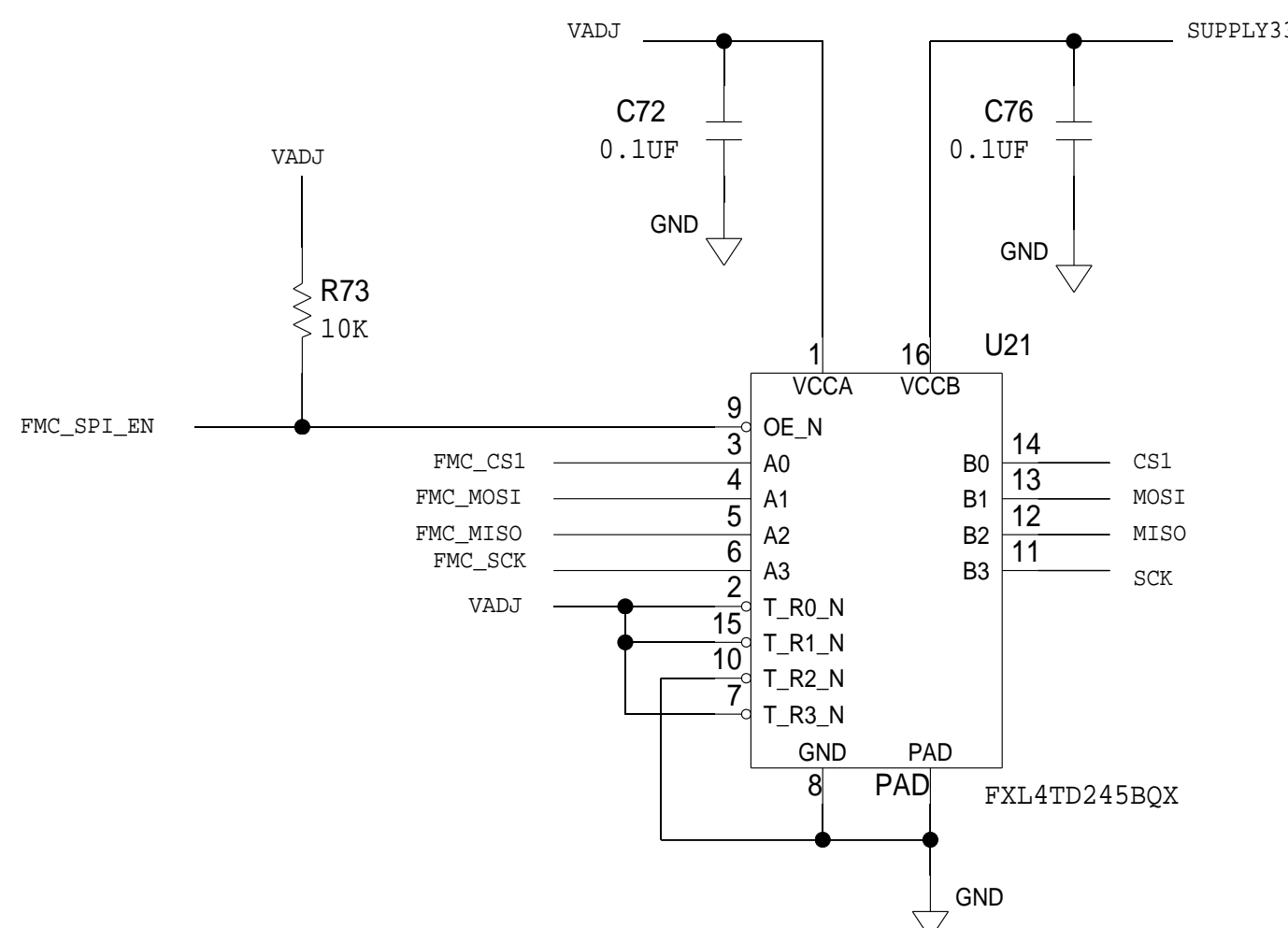
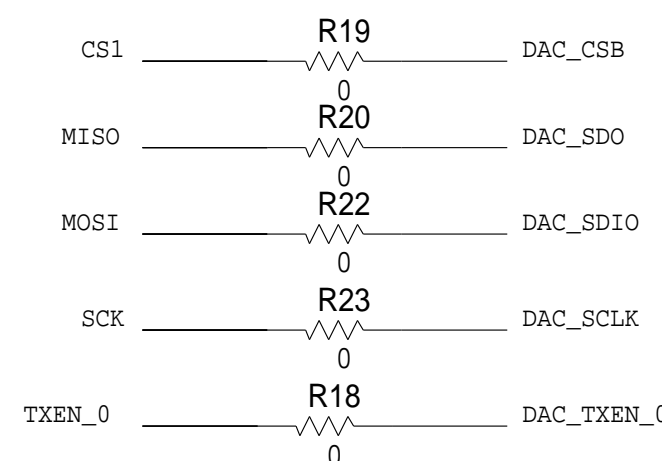
NOTE:

IF USB CABLE IS PLUGGED IN PIC IS THE SOURCE OF SPI BUS AND TXEN SIGNALS

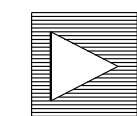
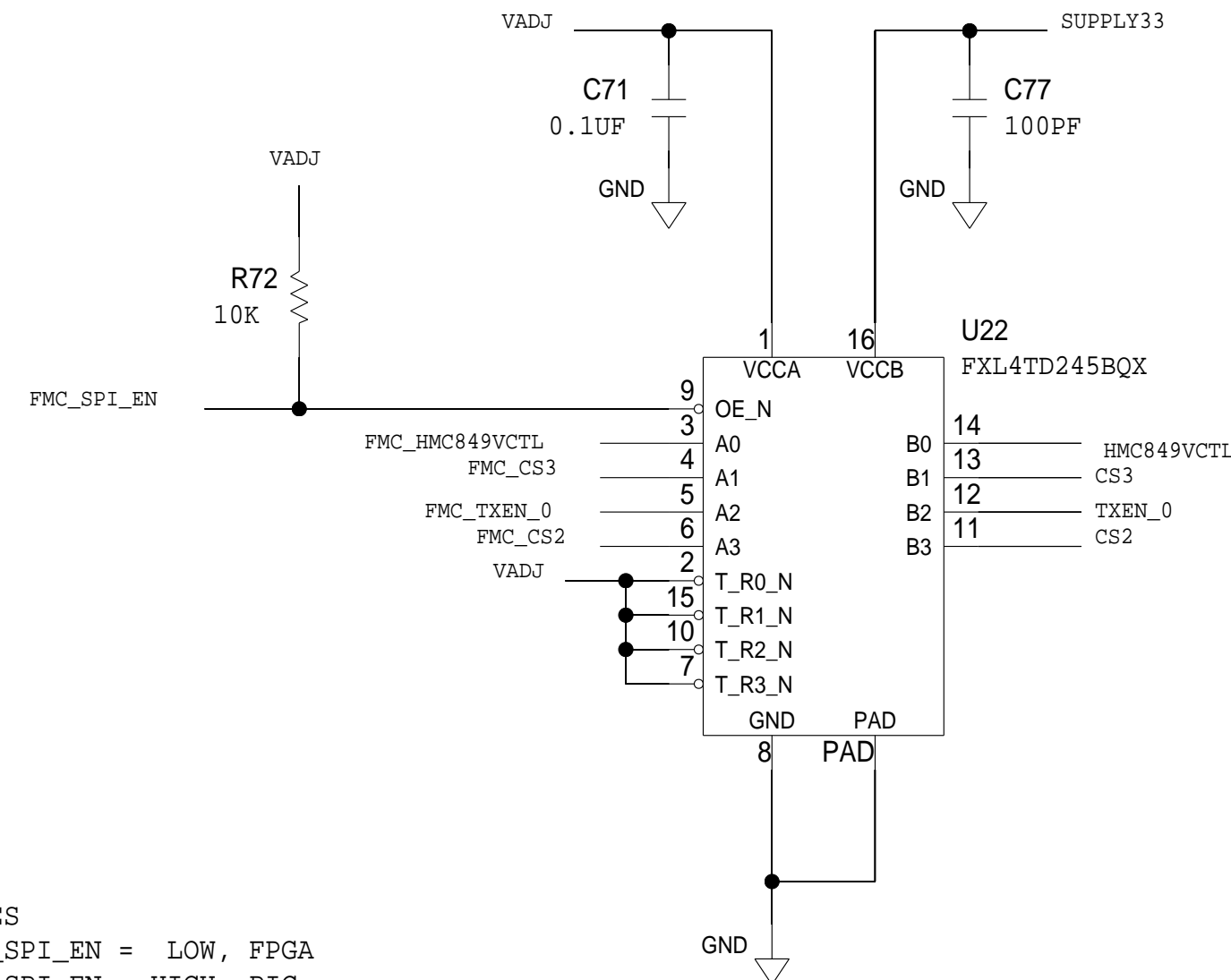
IF USB IS UNPLUGGED FMC INTERFACE IS THE SOURCE OF SPI BUS AND TXEN SIGNAL.



CONNECTIONS WILL PASS RIGHT THROUGH THE CONNECTOR



NOTES
FMC_SPI_EN = LOW, FPGA
FMC_SPI_EN = HIGH, PIC



ANALOG
DEVICES

SCHEMATIC

HW TYPE : AD9162 & 64-FMCB EBZ
Product(s) :

DESIGN VIEW
<DESIGN_VIEW>

PTD ENGINEER
A. ALVARAN

DRAWING NO.
02_046215

SIZE
D

SCALE
1:1

SHEET 8 OF 9

REV
C

