

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

DRAWN: Eric Cline		DATED: <Drawn Date>		COMPANY: A			
CHECKED: PCB PACKAGING Eric Cline		DATED: 1803.21		TITLE: EVAL-AD2428WB1BZ BLOCK_DIAGRAM			
QUALITY CONTROL: Martin Kessler		DATED: 2/19/2014		CODE: <Code>	SIZE: C	DRAWING NO: EVAL-AD2428WB1BZ	REV: 2.0
RELEASED: <Released By>		DATED: <Release Date>		SCALE: <Scale>			SHEET: 1 OF 4

6

5

4

3

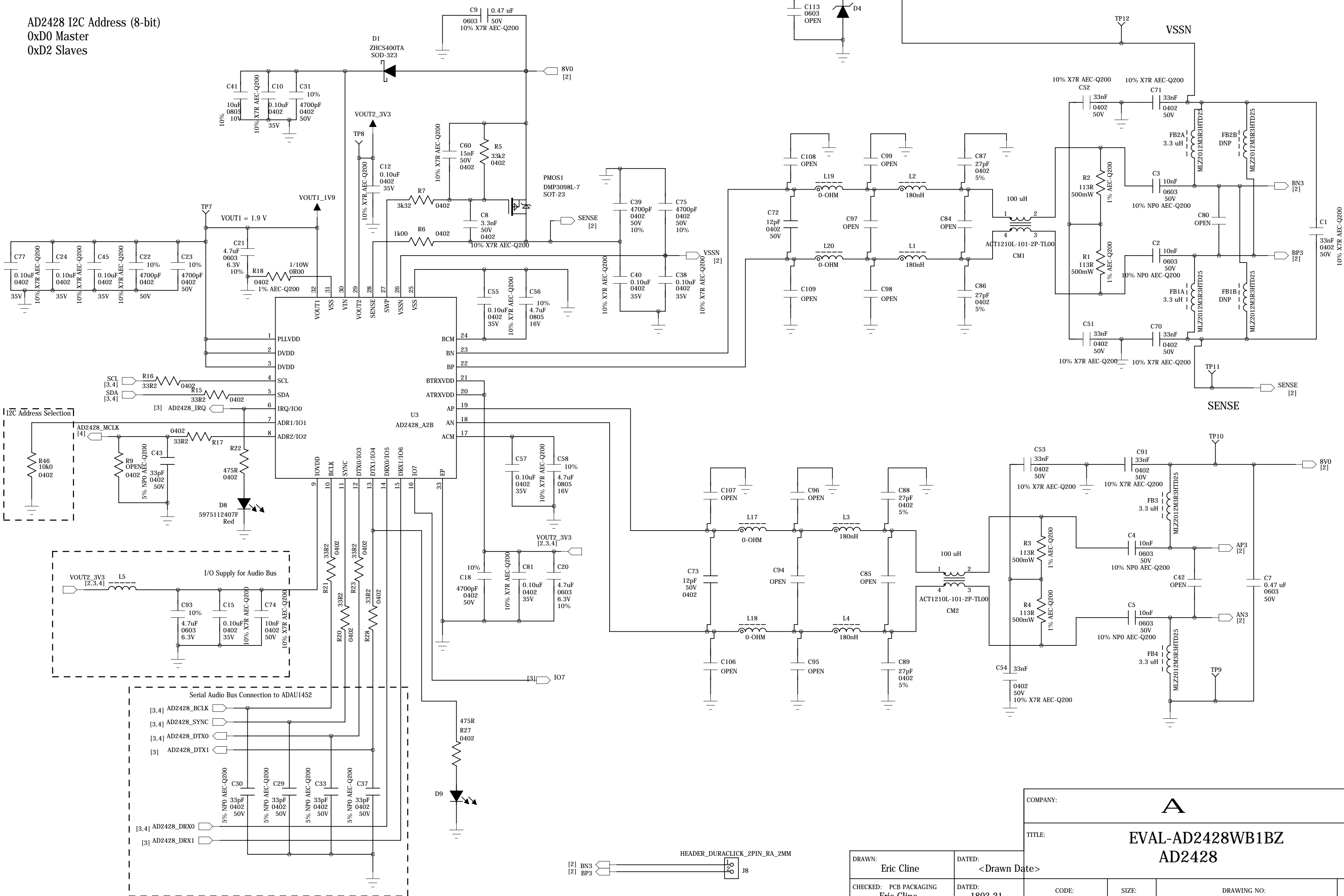
2

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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

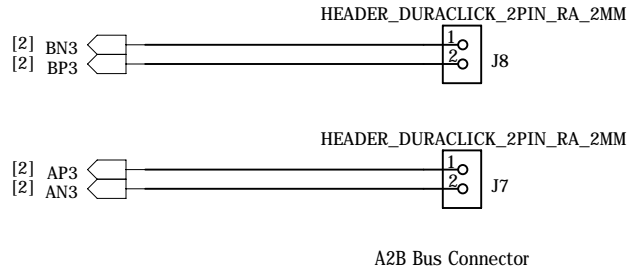
AD2428 I2C Address (7-bit)
0x68 Master
0x69 Slaves

AD2428 I2C Address (8-bit)
0xD0 Master
0xD2 Slaves

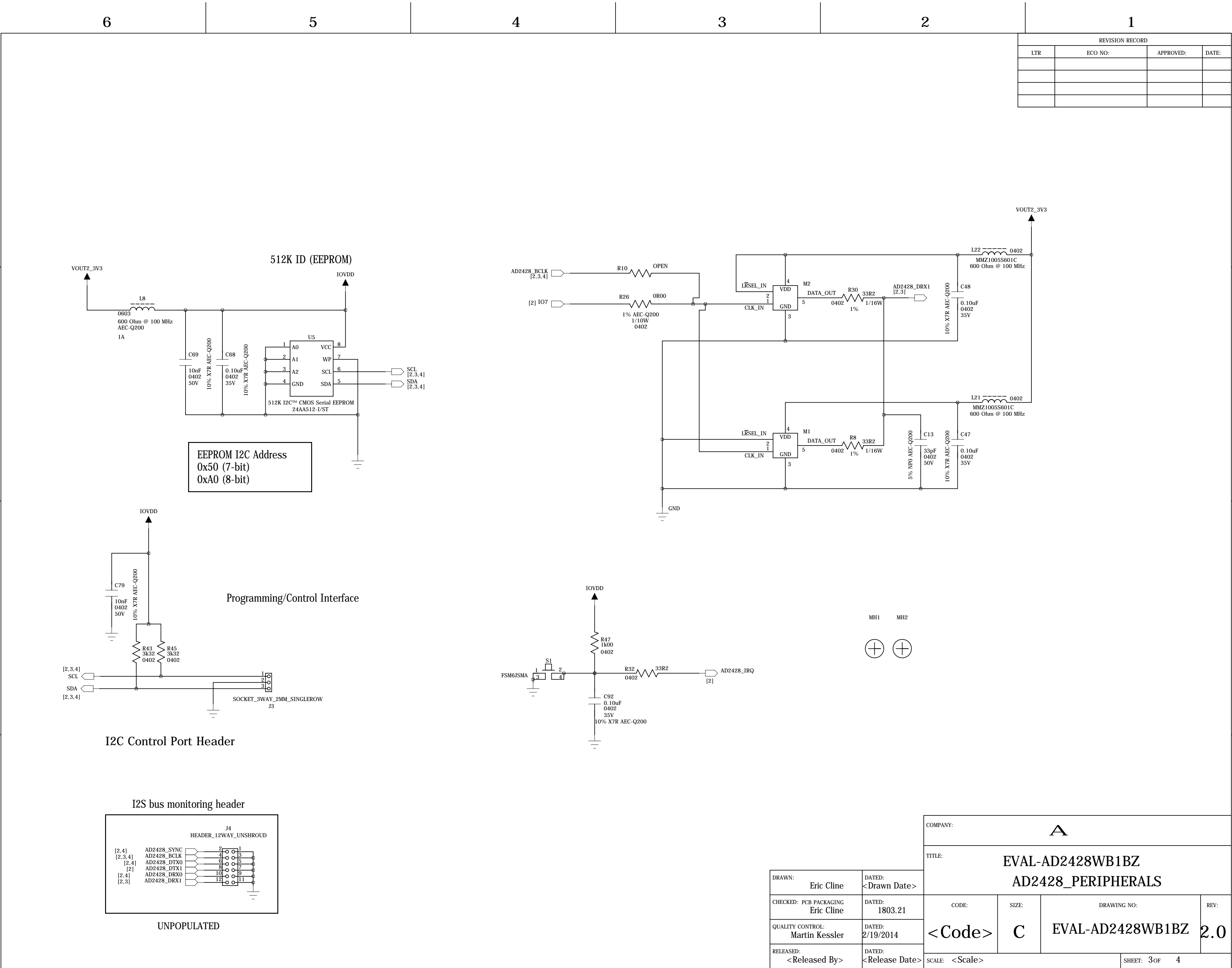


COMPANY: A			
TITLE: EVAL-AD2428WB1BZ AD2428			
CODE: <Code>	SIZE: C	DRAWING NO: EVAL-AD2428WB1BZ	REV: 2.0
SCALE: <Scale>		SHEET2 OF 4	

DRAWN: Eric Cline	DATED: <Drawn Date>
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QUALITY CONTROL: Martin Kessler	DATED: 2/19/2014
RELEASED: <Released By>	DATED: <Release Date>



A2B Bus Connector



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QUALITY CONTROL: Martin Kessler		DATED: 2/19/2014	
RELEASED: <Released By>		DATED: <Release Date>	
COMPANY: A		TITLE: EVAL-AD2428WB1BZ AD2428_PERIPHERALS	
CODE: <Code>		SIZE: C	DRAWING NO: EVAL-AD2428WB1BZ
SCALE: <Scale>		REV: 2.0	
SHEET: 3 OF 4			4

6

5

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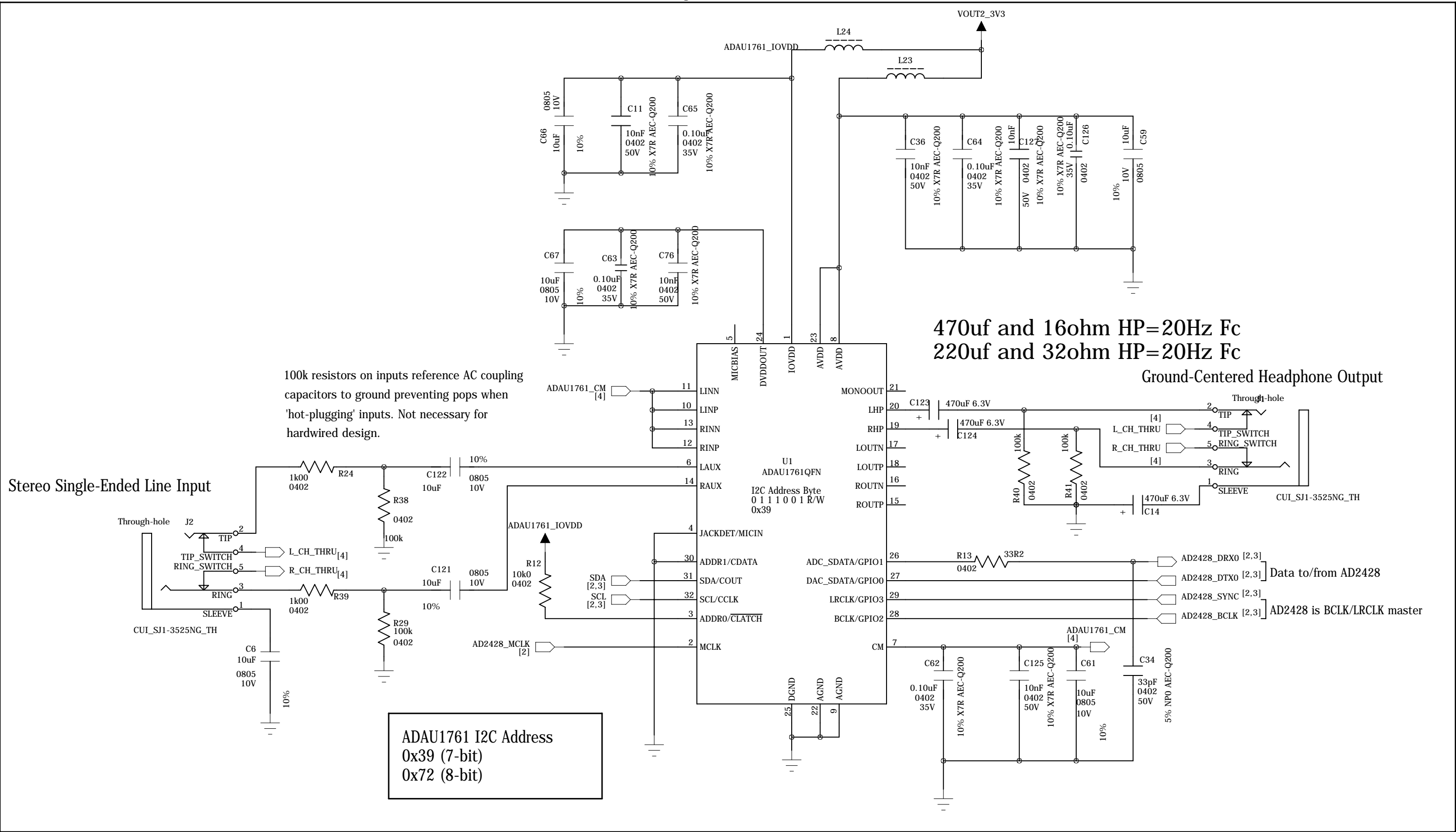
3

2

1

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

ADAU1761 Audio CODEC with Sigma DSP



COMPANY: A			
TITLE: EVAL-AD2428WB1BZ ADAU1761_CODEC			
CODE: <Code>	SIZE: C	DRAWING NO: EVAL-AD2428WB1BZ	REV: 2.0
SCALE: <Scale>			SHEET: 4 OF 4

DRAWN: Eric Cline	DATED: <Drawn Date>
CHECKED: PCB PACKAGING Eric Cline	DATED: 1803.21
QUALITY CONTROL: Martin Kessler	DATED: 2/19/2014
RELEASED: <Released By>	DATED: <Release Date>