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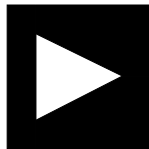
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ADSP-BF526 EZ-BOARD SCHEMATIC



**ANALOG
DEVICES**

20 Cotton Road
Nashua, NH 03063
PH: 1-800-ANALOGD

Title ADSP-526 EZ-BOARD
TITLE

Size C	Board No. A0212-2007	Rev 1.1A
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Date 1-28-2010_15:55	Sheet 1 of 15
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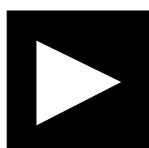
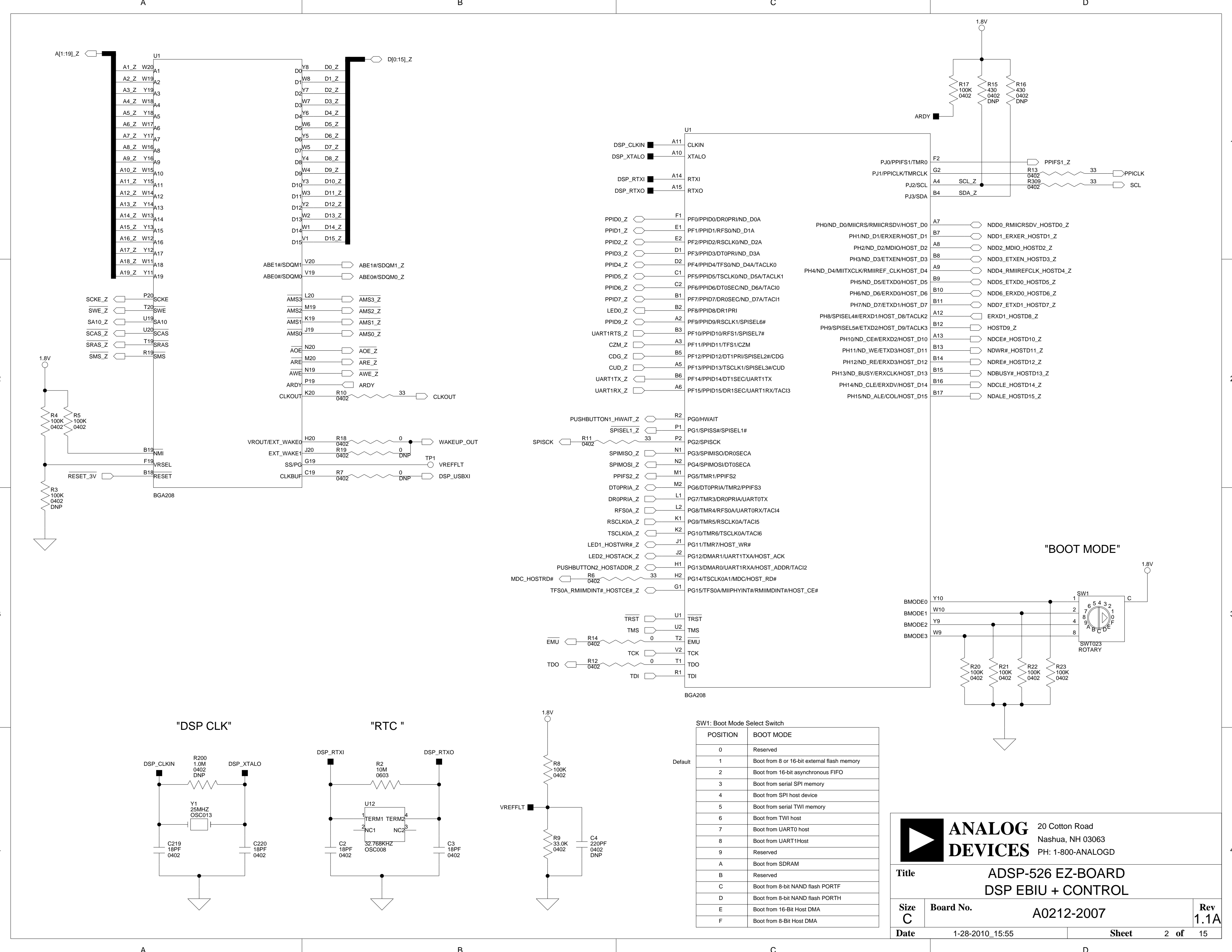
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**ANALOG
DEVICES**

20 Cotton Road
Nashua, NH 03063
PH: 1-800-ANALOGD

Title			ADSP-526 EZ-BOARD DSP EBIU + CONTROL		
Size C	Board No.		A0212-2007		Rev 1.1A
Date	1-28-2010_15:55		Sheet	2 of	15

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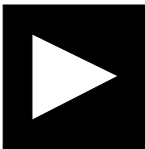
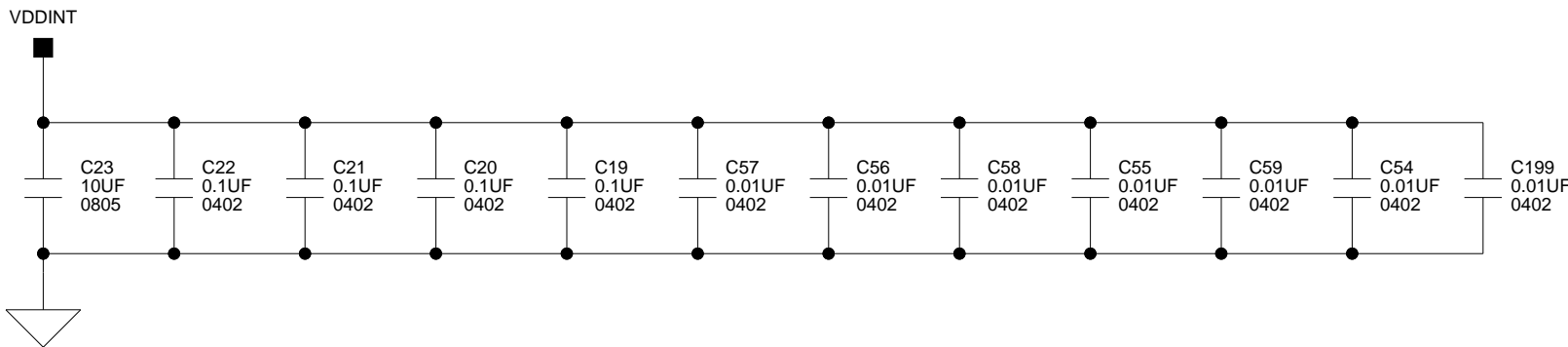
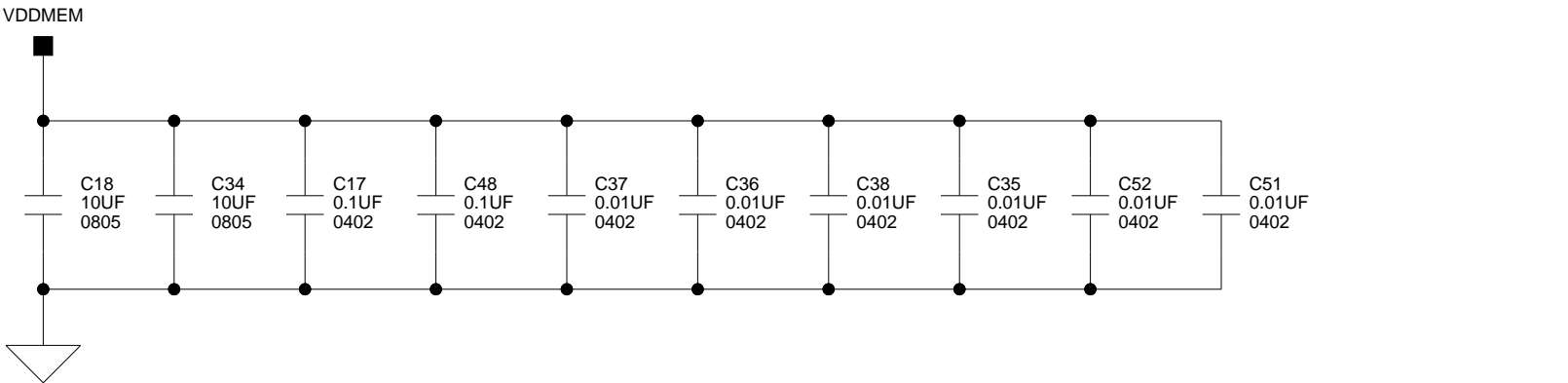
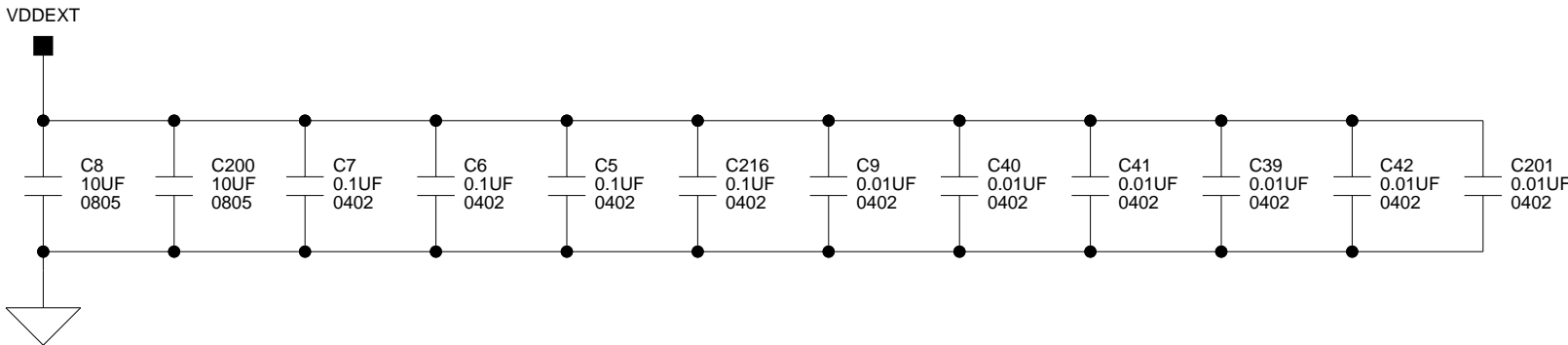
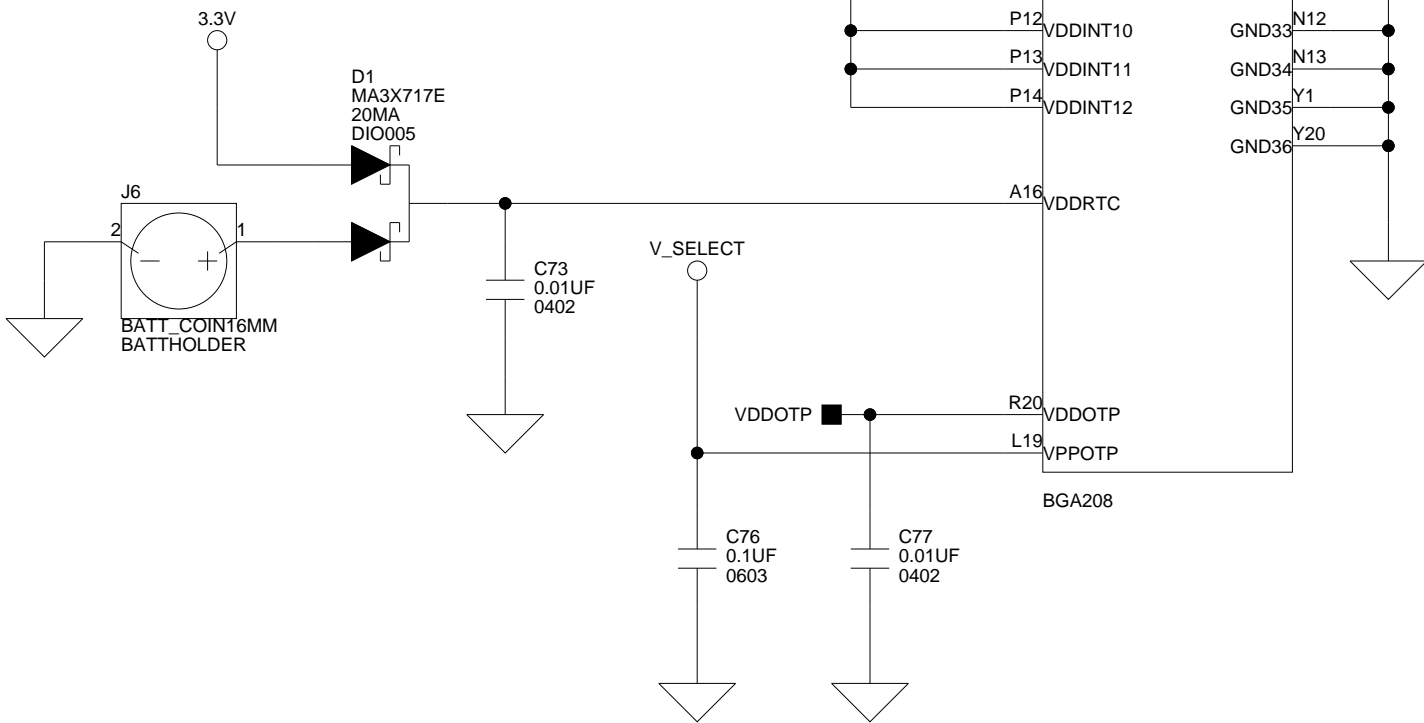
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"RTC BATTERY"



**ANALOG
DEVICES**

20 Cotton Road
Nashua, NH 03063
PH: 1-800-ANALOGD

Title **ADSP-526 EZ-BOARD
DSP POWER, BYPASS CAPS**

Size **C** Board No. **A0212-2007**

Date **1-28-2010_15:55**

Sheet **4 of 15**

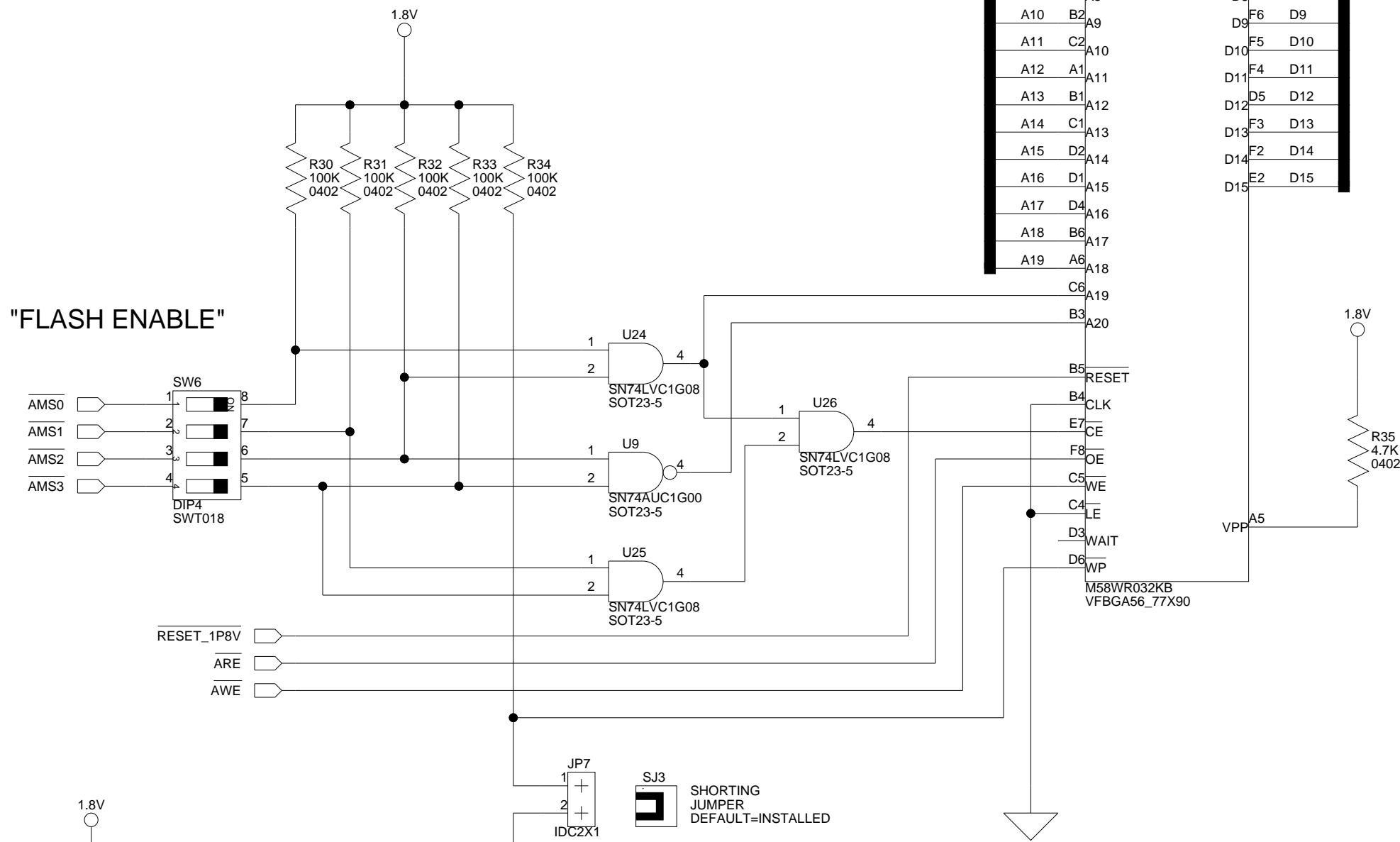
Rev
1.1A

SW6: FLASH Enable

POS.	FROM	TO	DEFAULT	ALTERNATE FUNCTION / OFF MODE
SW6.1	DSP (U1)	FLASH (U16)	ON	(Expansion Interface)
SW6.2	DSP (U1)	FLASH (U16)	ON	(Expansion Interface)
SW6.3	DSP (U1)	FLASH (U16)	ON	(Expansion Interface)
SW6.4	DSP (U1)	FLASH (U16)	ON	(Expansion Interface)

4 MB FLASH
(2M x 16)

64MB SDRAM (32M x 16)

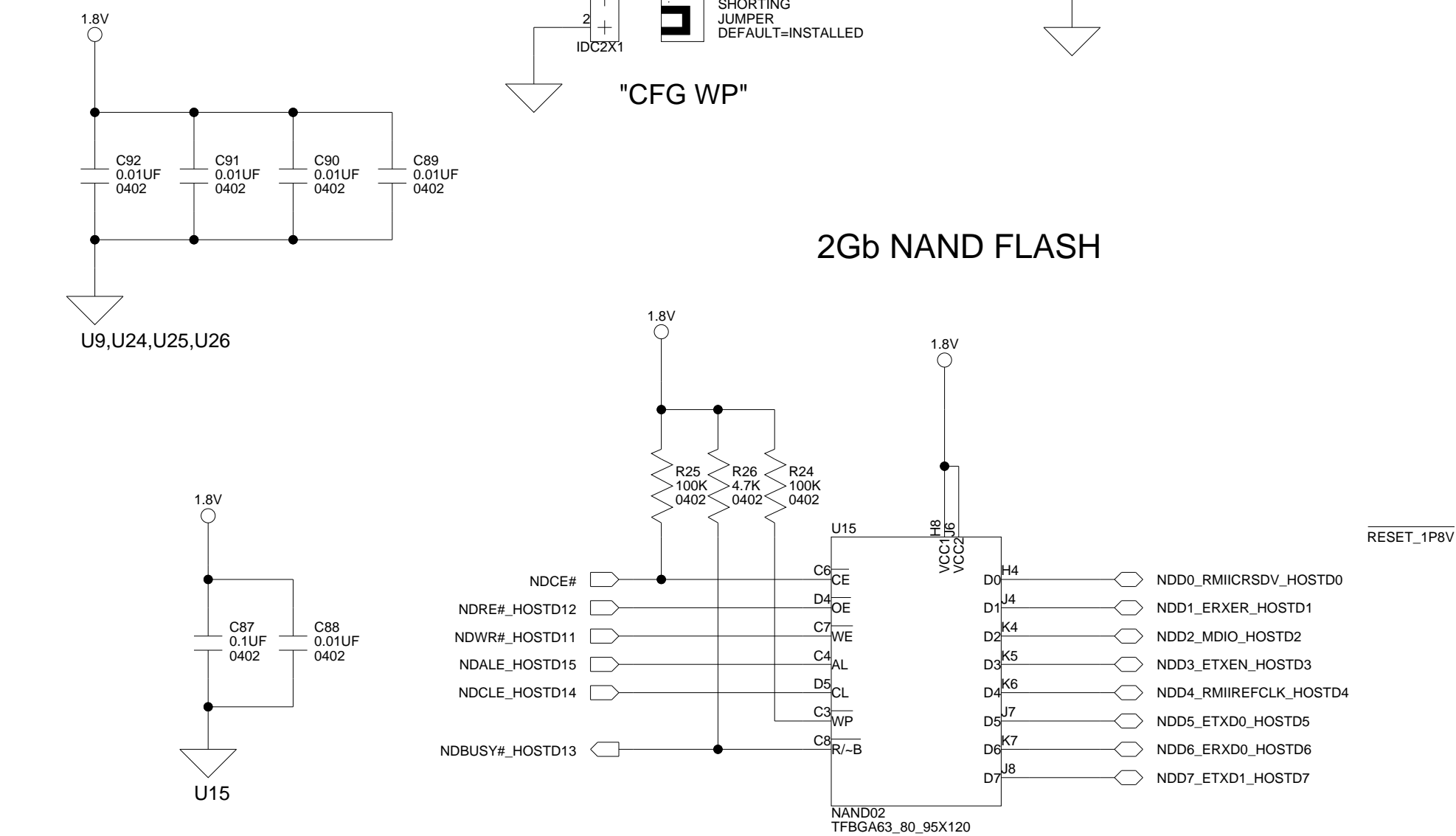


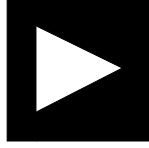
MEMORY MAP

ADDRESS RANGE	SELECT LINE	TYPE
0x2030 0000 - 0x203F FFFF	ASYNC BANK 3	FLASH
0x2020 0000 - 0x202F FFFF	ASYNC BANK 2	FLASH
0x2010 0000 - 0x201F FFFF	ASYNC BANK 1	FLASH
0x2000 0000 - 0x200F FFFF	ASYNC BANK 0	FLASH
0x0000 0000 - 0x03FF FFFF	NONE	SDRAM

2Gb NAND FLASH

4 Mb SPI FLASH

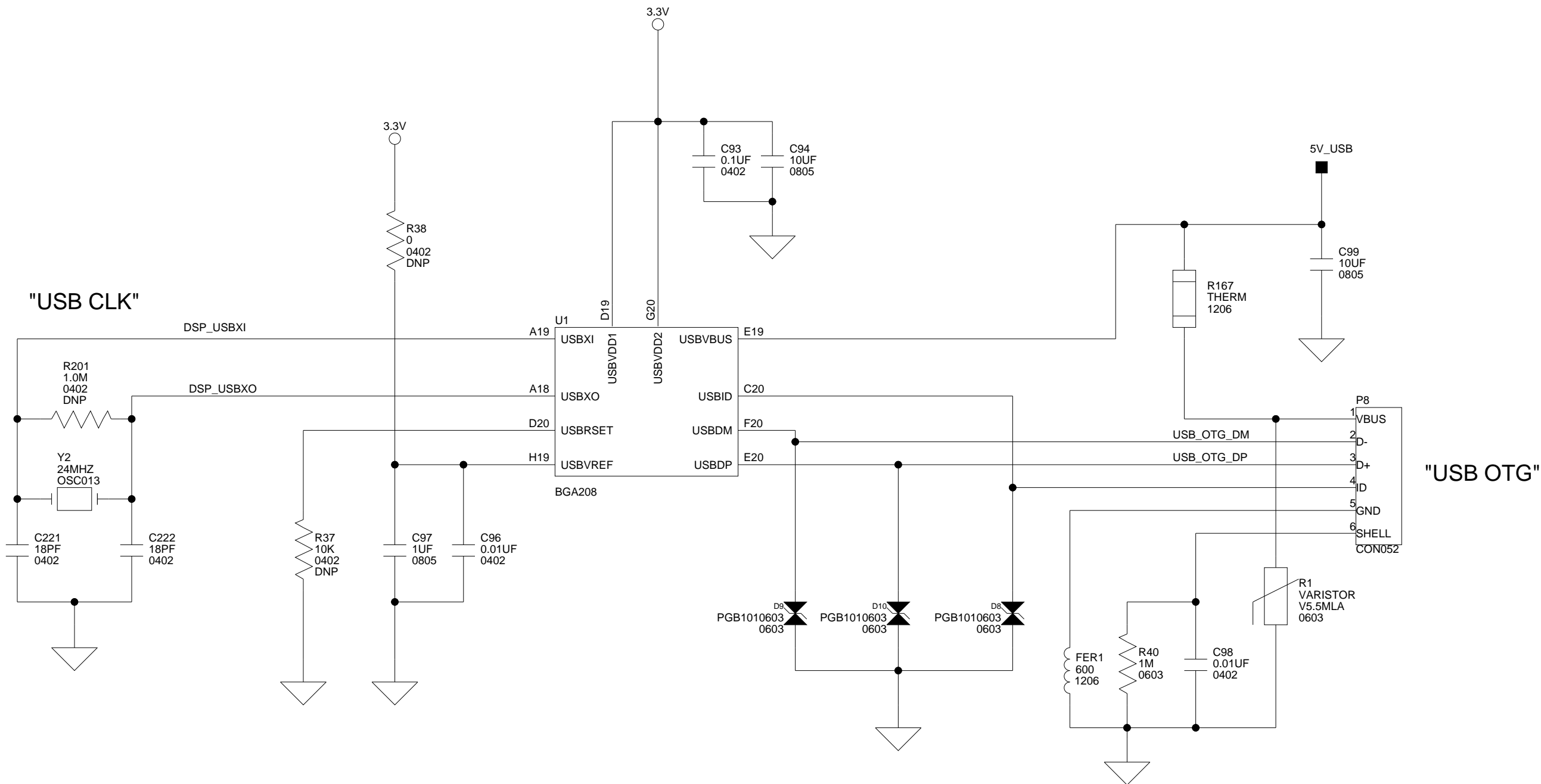


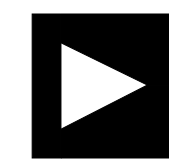
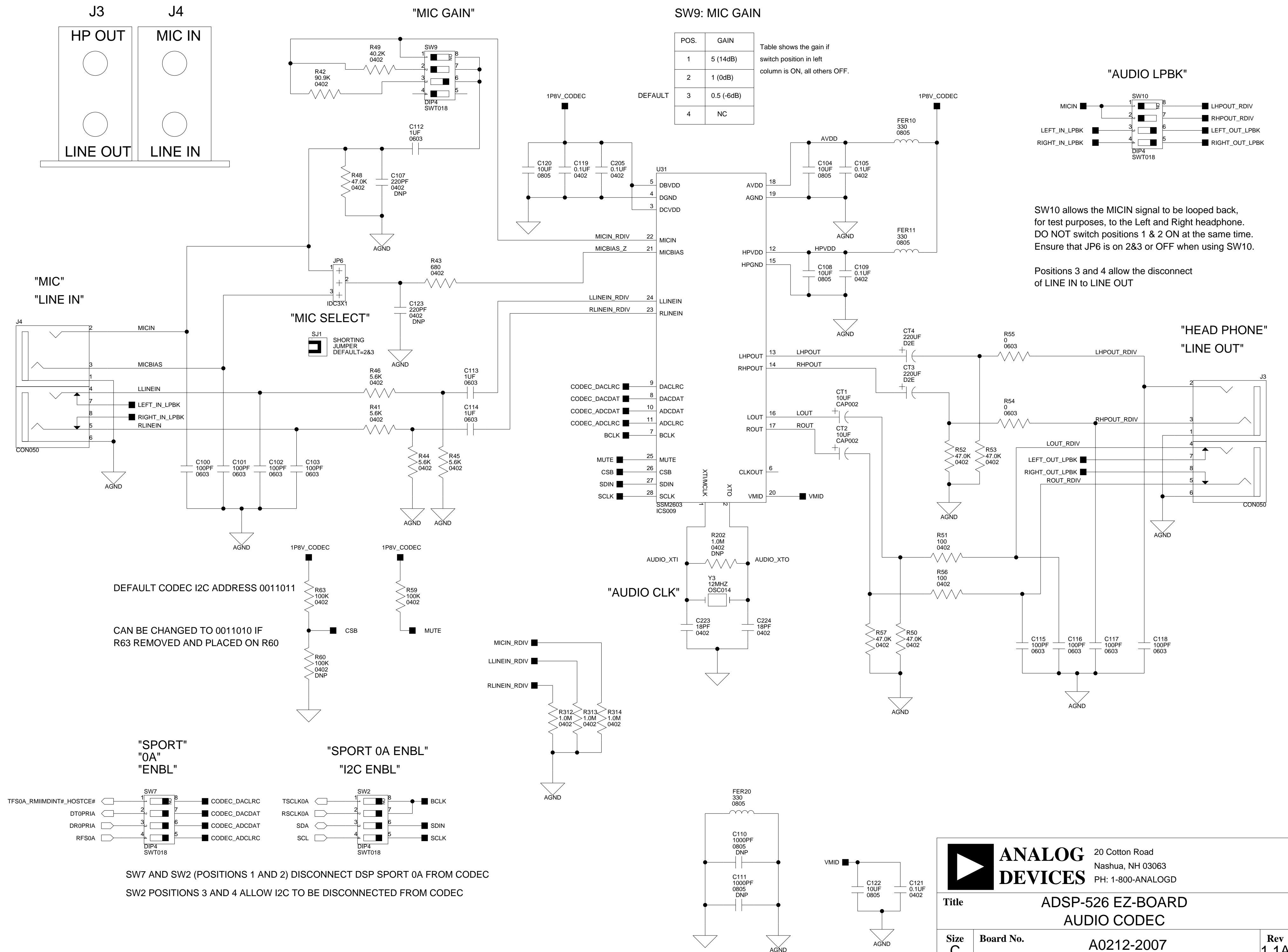


**ANALOG
DEVICES**

20 Cotton Road
Nashua, NH 03063
PH: 1-800-ANALOGD

Title ADSP-526 EZ-BOARD MEMORY		
Size C	Board No. A0212-2007	Rev 1.1A
Date 1-28-2010_15:55	Sheet 5 of	15

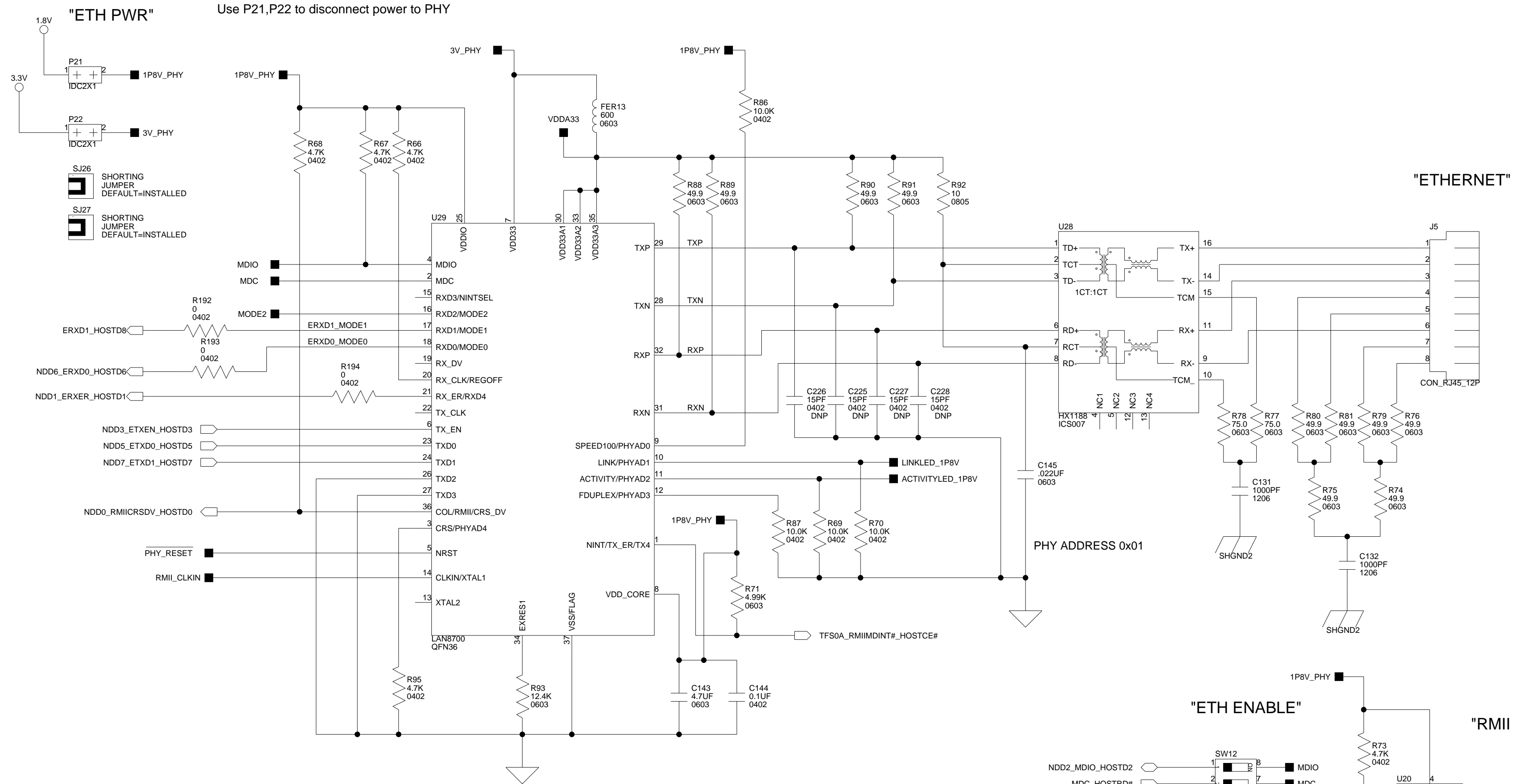




**ANALOG
DEVICES**

20 Cotton Road
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PH: 1-800-ANALOGD

Title			ADSP-526 EZ-BOARD AUDIO CODEC	
Size C	Board No.		A0212-2007	
Date	2-19-2010_10:14		Sheet	7 of 15
Rev		1.1A		

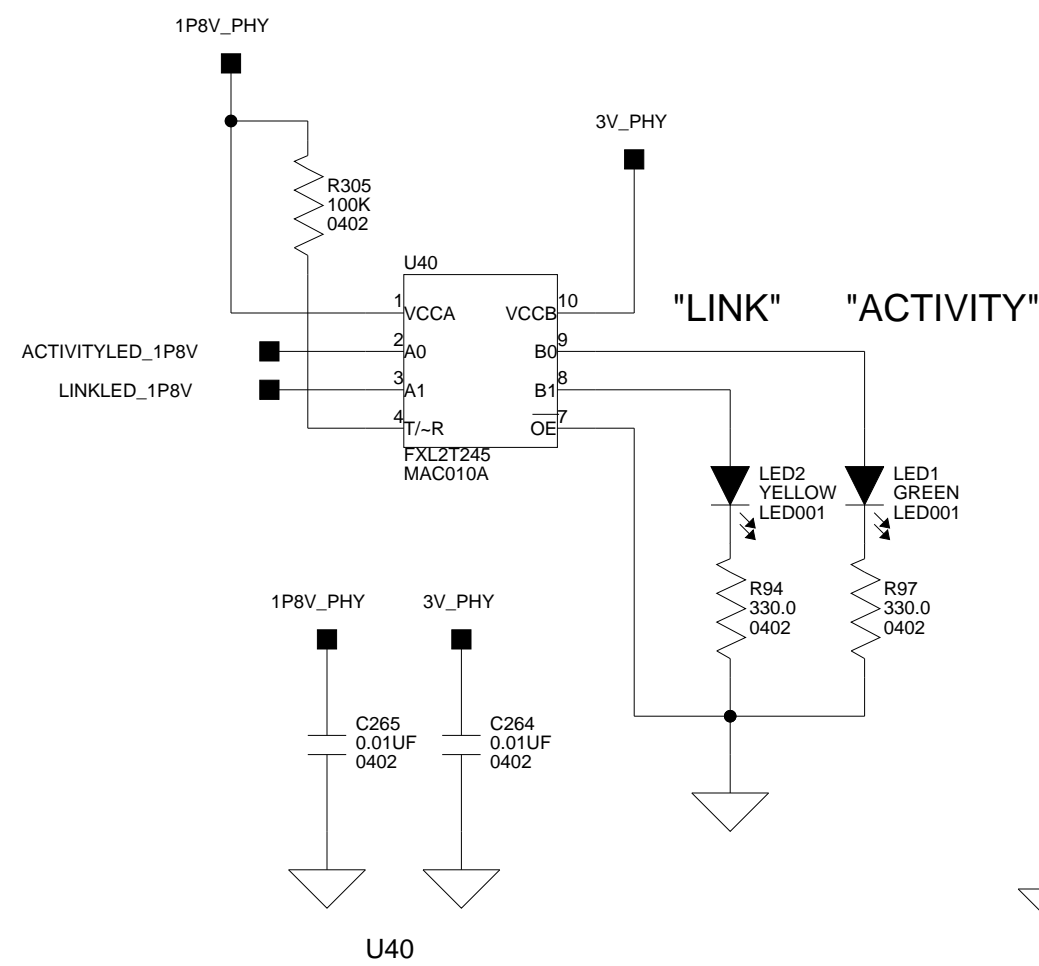
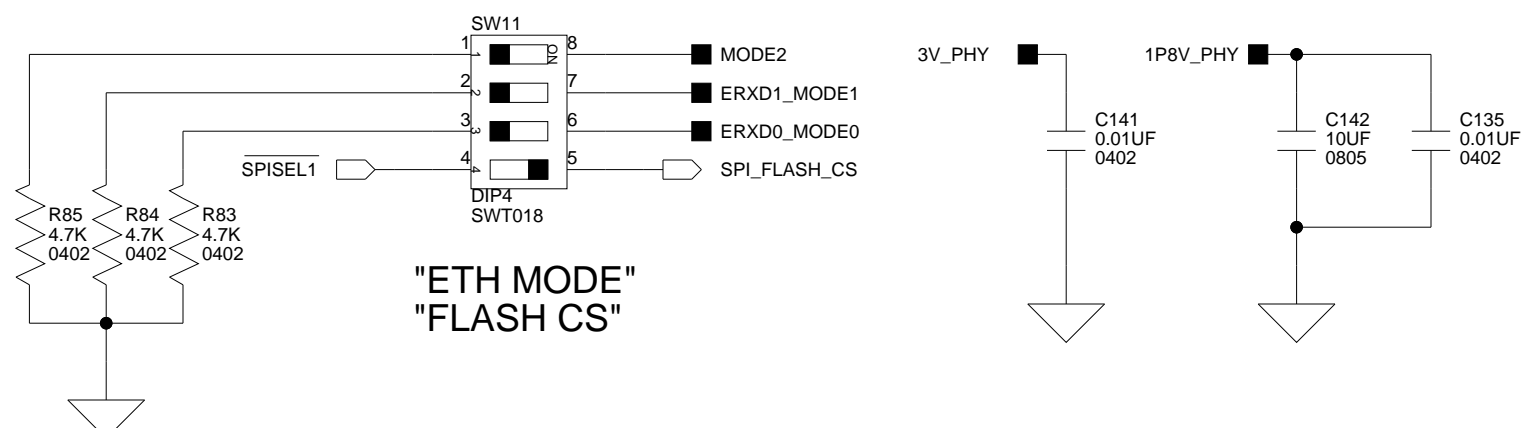


SW11: Ethernet Mode Select (SW9.1, SW9.2, SW9.3)

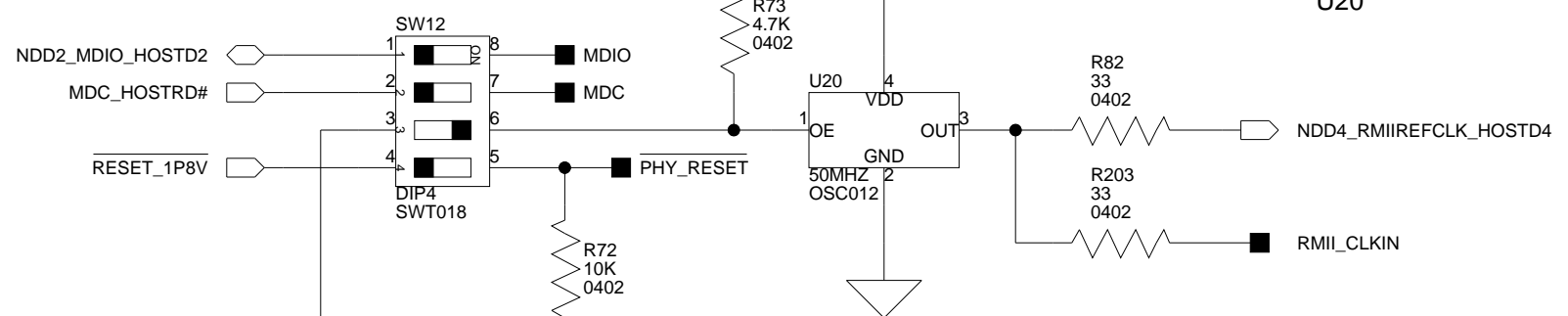
LAN8700 has internal mode pull-up pins. Setting of OFF = 1.

MODE[2:0]	MODE DEFINITIONS	
111	All Capable, Auto Negotiation	DEFAULT
110	Power Down Mode	
101	Repeater Mode, Auto Negotiation	
100	100Base-TX Half duplex Advertised, Auto Negotiaion	
011	100Base-TX Full Duplex	
010	100Base-TX Half Duplex	
001	10Base-T Full Duplex	
000	10Base-T Half Duplex	

SW11.4 disconnects SPISEL1, for expansion interface (P1.26, P2.21)



"ETH ENABLE"



SW12: ETH Enable

POS.	FROM	TO	DEFAULT	FUNCTIONS
SW1.1	DSP (U1, PH2)	PHY (U29)	OFF	ON (MDIO PHY U29), OFF (NAND U15, HOST connector P9.27)
SW1.2	DSP (U1, PG14)	PHY (U29)	OFF	ON (MDC PHY U29), OFF (HOST connector P9.2)
SW1.3	GND	RMII CLK (U20)	ON	ON (RMII CLK disabled), OFF (RMII CLK enabled)
SW1.4	RESET IC (U22)	PHY (U29)	OFF	ON (PHY not held in reset), OFF (PHY held in reset)

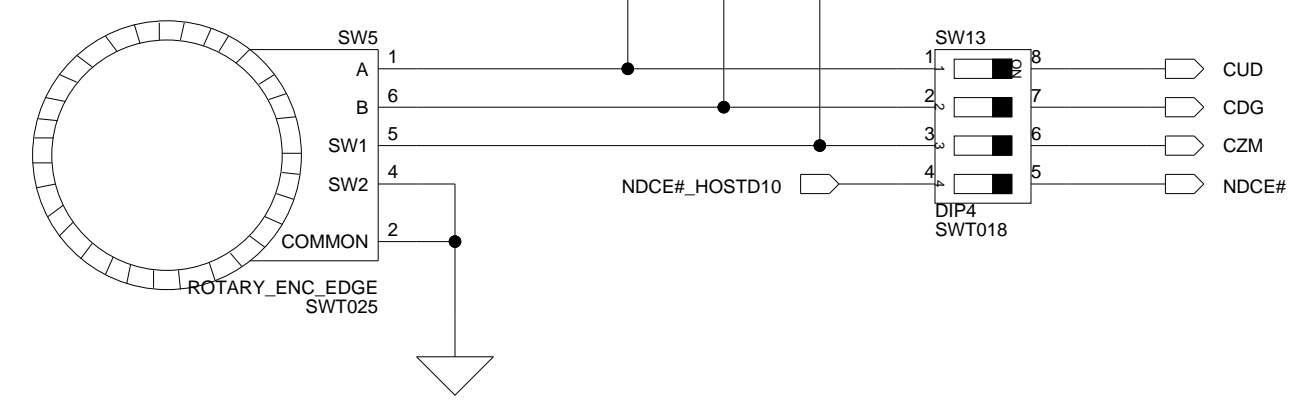


**ANALOG
DEVICES**

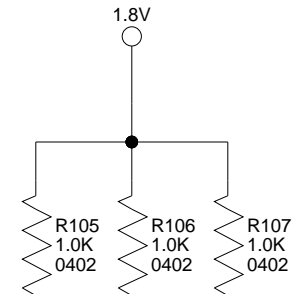
20 Cotton Road
Nashua, NH 03063
PH: 1-800-ANALOGD

Title					ADSP-526 EZ-BOARD RMII PHY				
Size C		Board No. A0212-2007				Rev 1.1A			
Date		1-28-2010_15:55				Sheet		8 of 15	

"ENCODER"

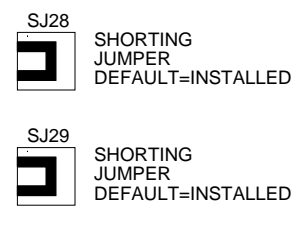
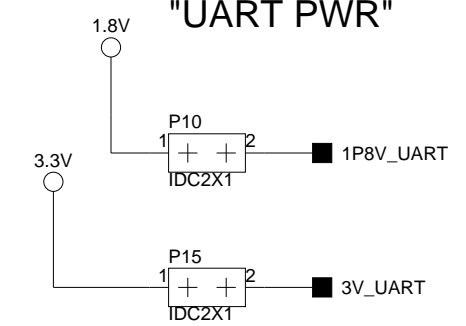


"ROTARY"
"NAND"
"ENABLE"

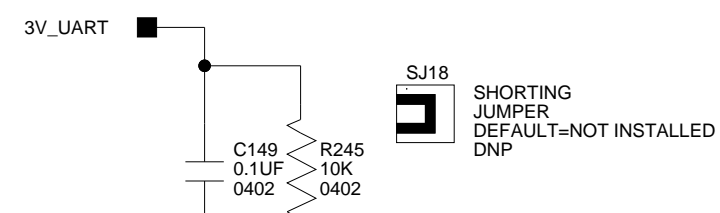


Use P21,P22 to disconnect power to UART

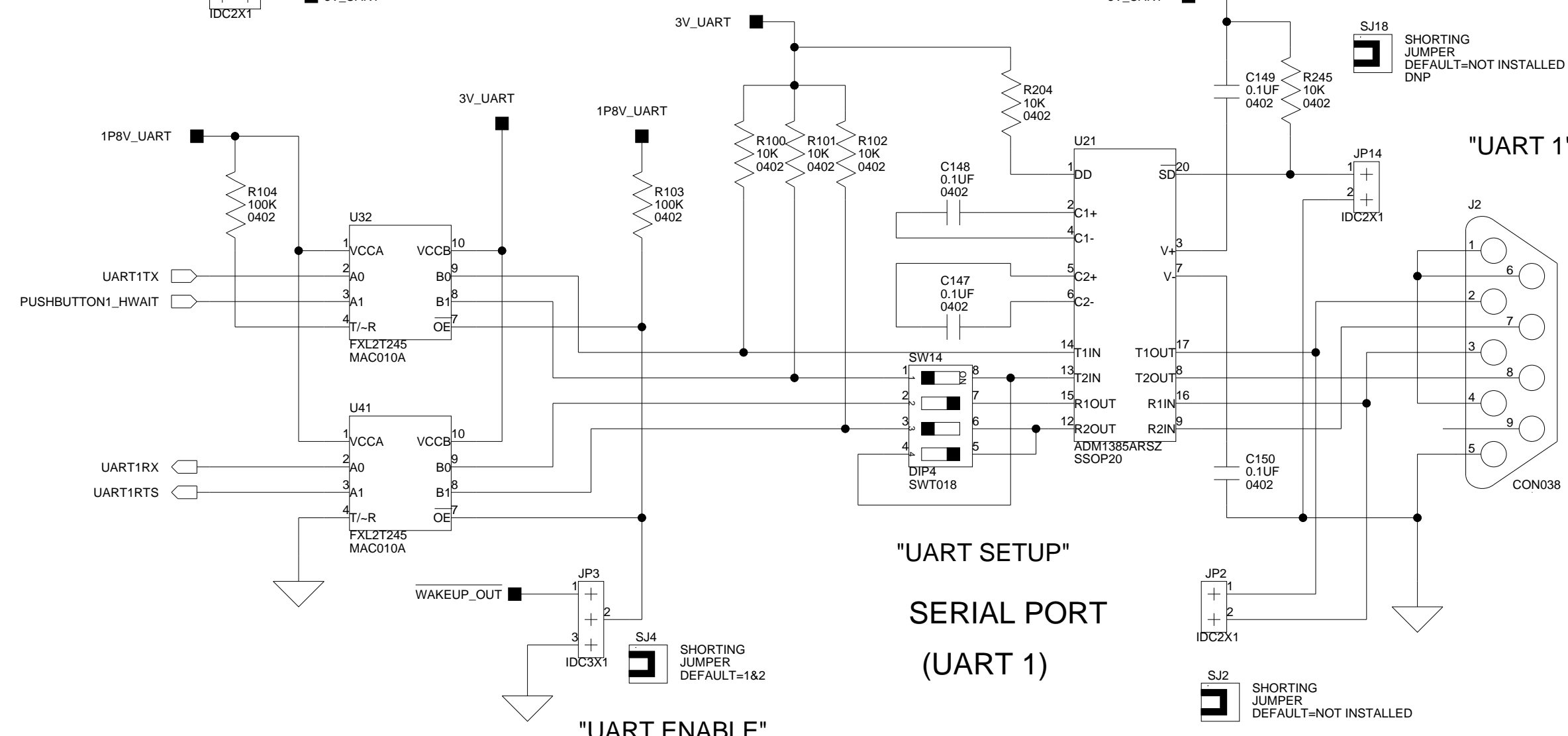
"UART PWR"



"UART SD"



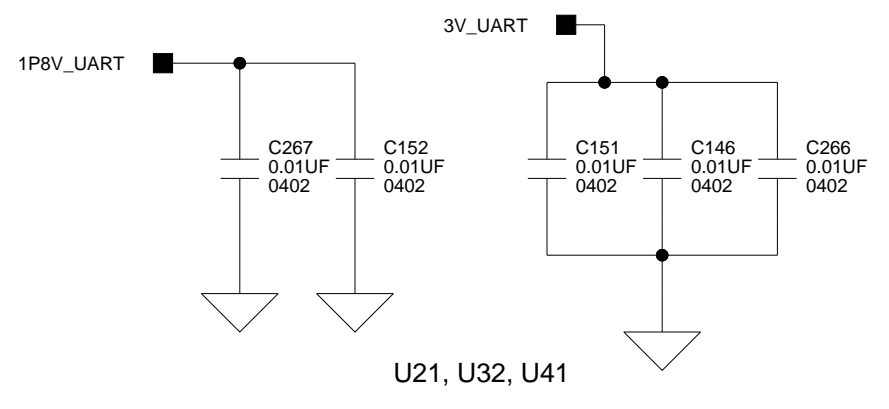
"UART 1"



"UART SETUP"
SERIAL PORT
(UART 1)

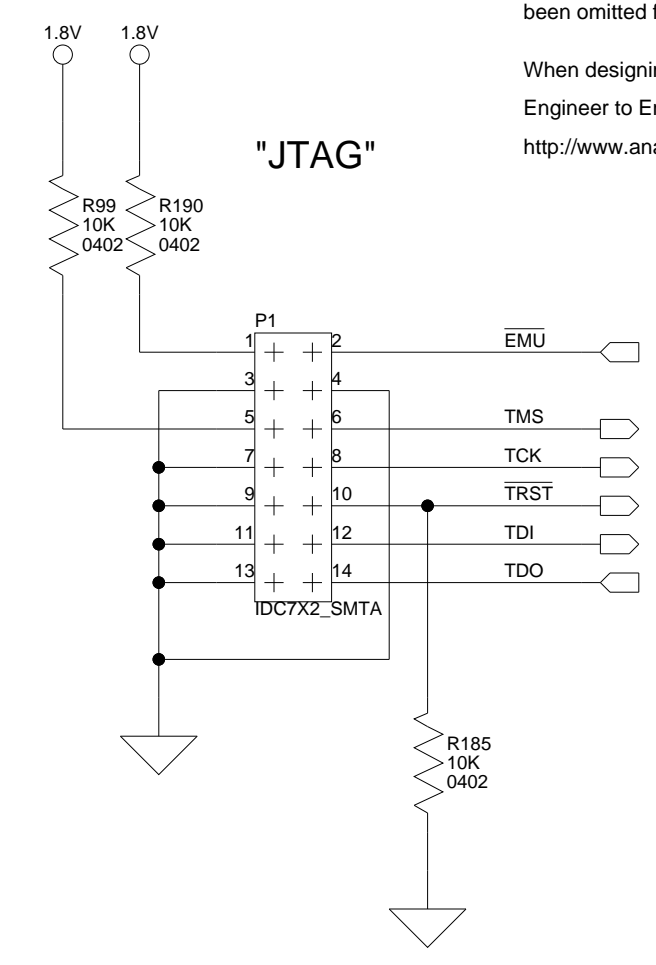
"UART ENABLE"

"UART LPBK"



U21, U32, U41

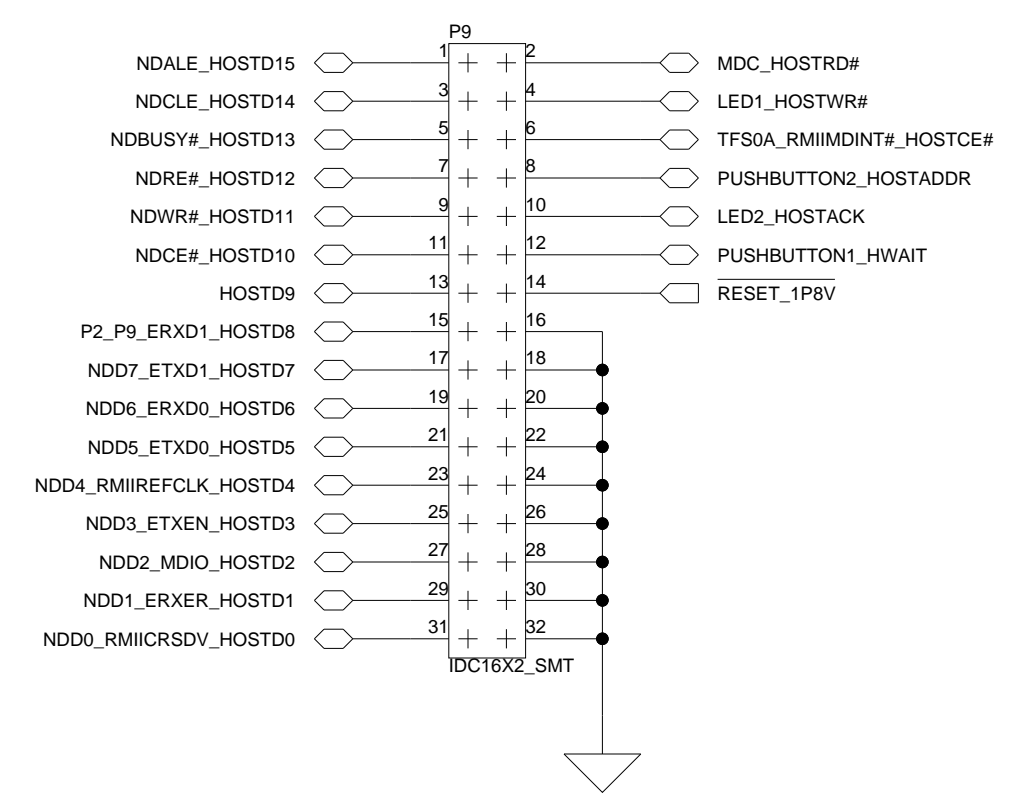
"JTAG"

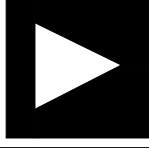


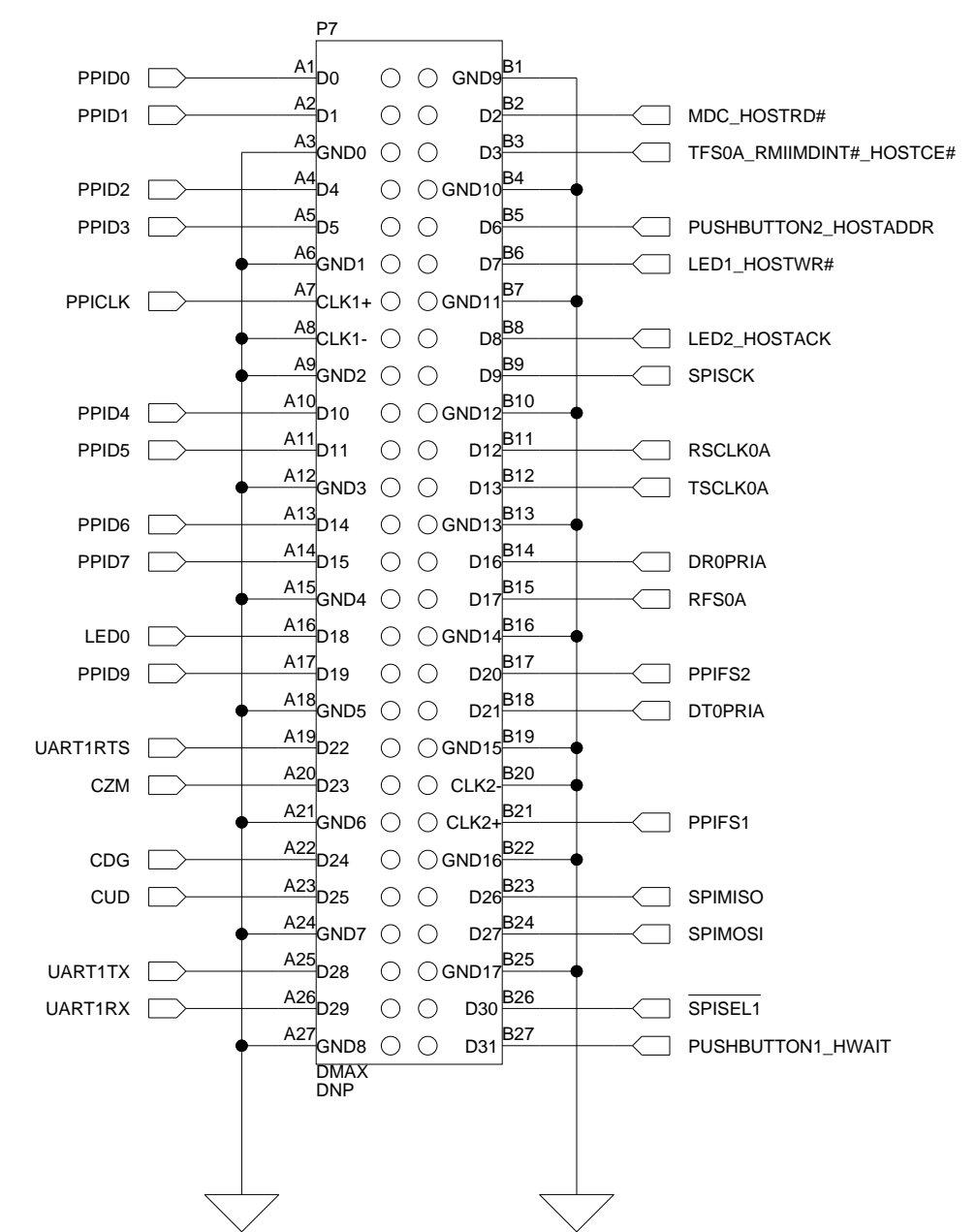
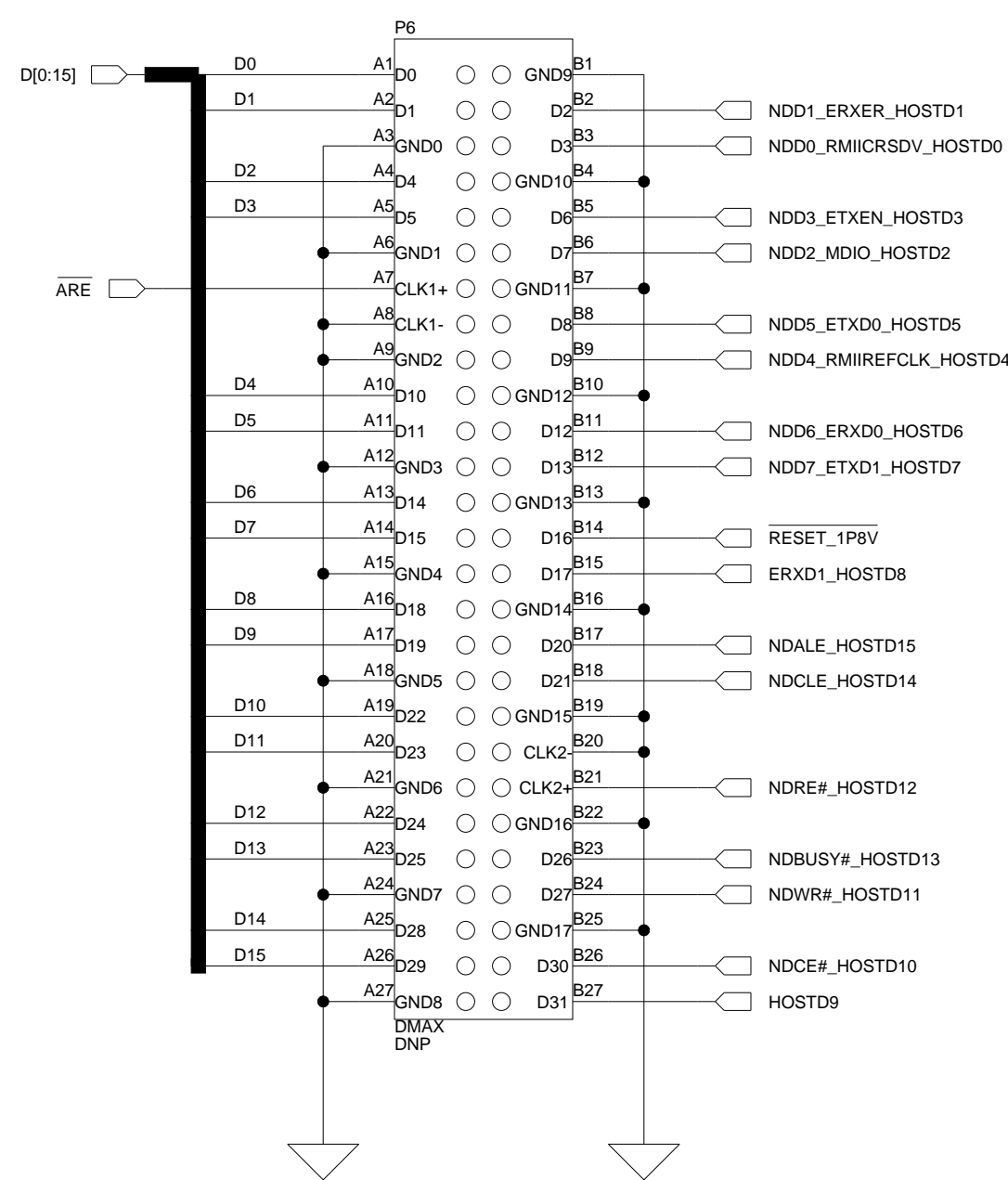
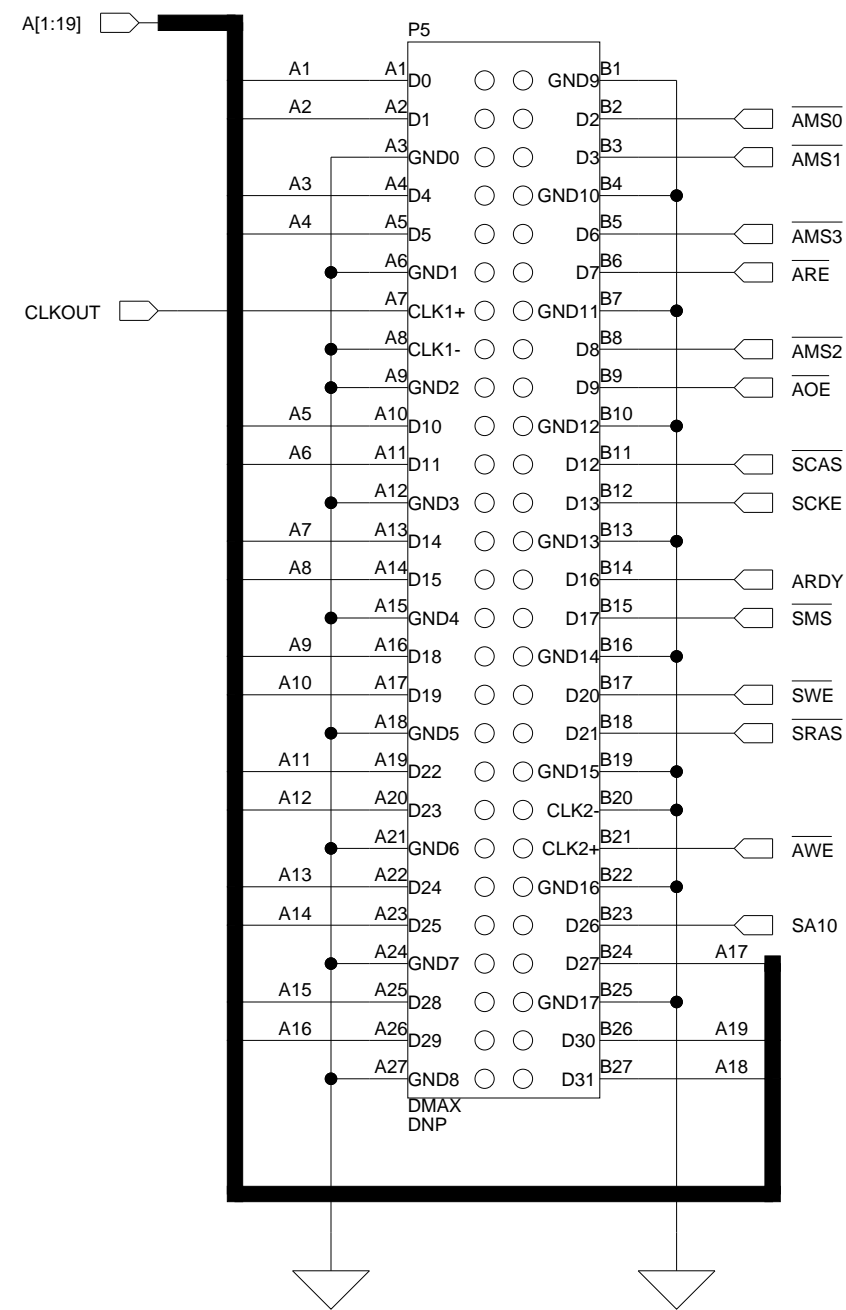
All USB interface circuitry is considered proprietary and has been omitted from this schematic.

When designing your JTAG interface please refer to the Engineer to Engineer Note EE-68 which can be found at <http://www.analog.com>

"HOST"



		ANALOG DEVICES		20 Cotton Road Nashua, NH 03063 PH: 1-800-ANALOGD	
Title ADSP-526 EZ-BOARD ROTARY ENCODER, JTAG, RS232, HOST					
Size C	Board No. A0212-2007			Rev 1.1A	
Date 1-28-2010_15:55		Sheet 9 of 15			

ANALOG
DEVICES

20 Cotton Road
Nashua, NH 03063
PH: 1-800-ANALOGD

Title	ADSP-526 EZ-BOARD LOGIC ANALYZER CONN
--------------	--

Size C	Board No. A0212-2007	Rev 1.1A
Date 1-28-2010 15:55	Sheet 10	of 15

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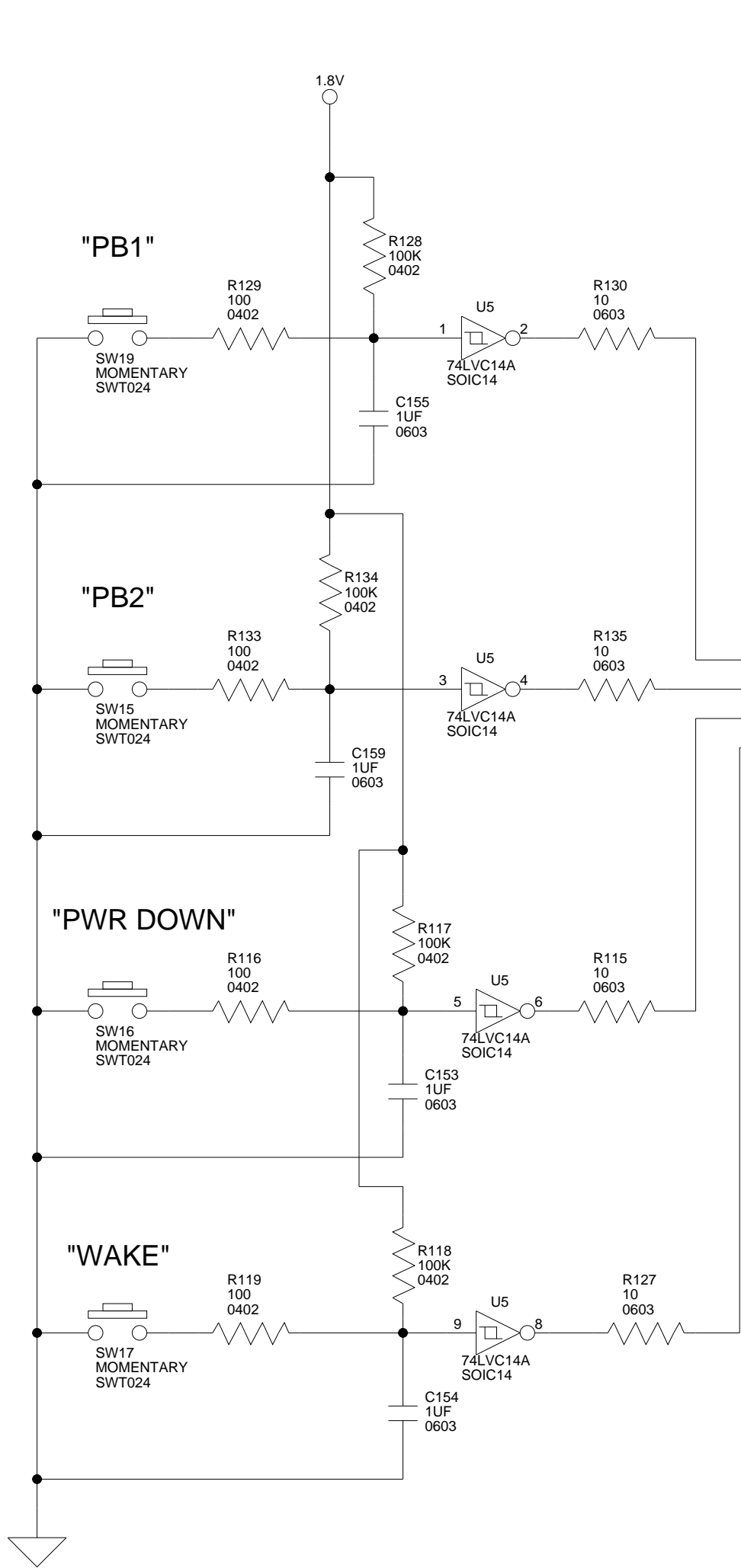
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"GPIO ENABLE"

SW20

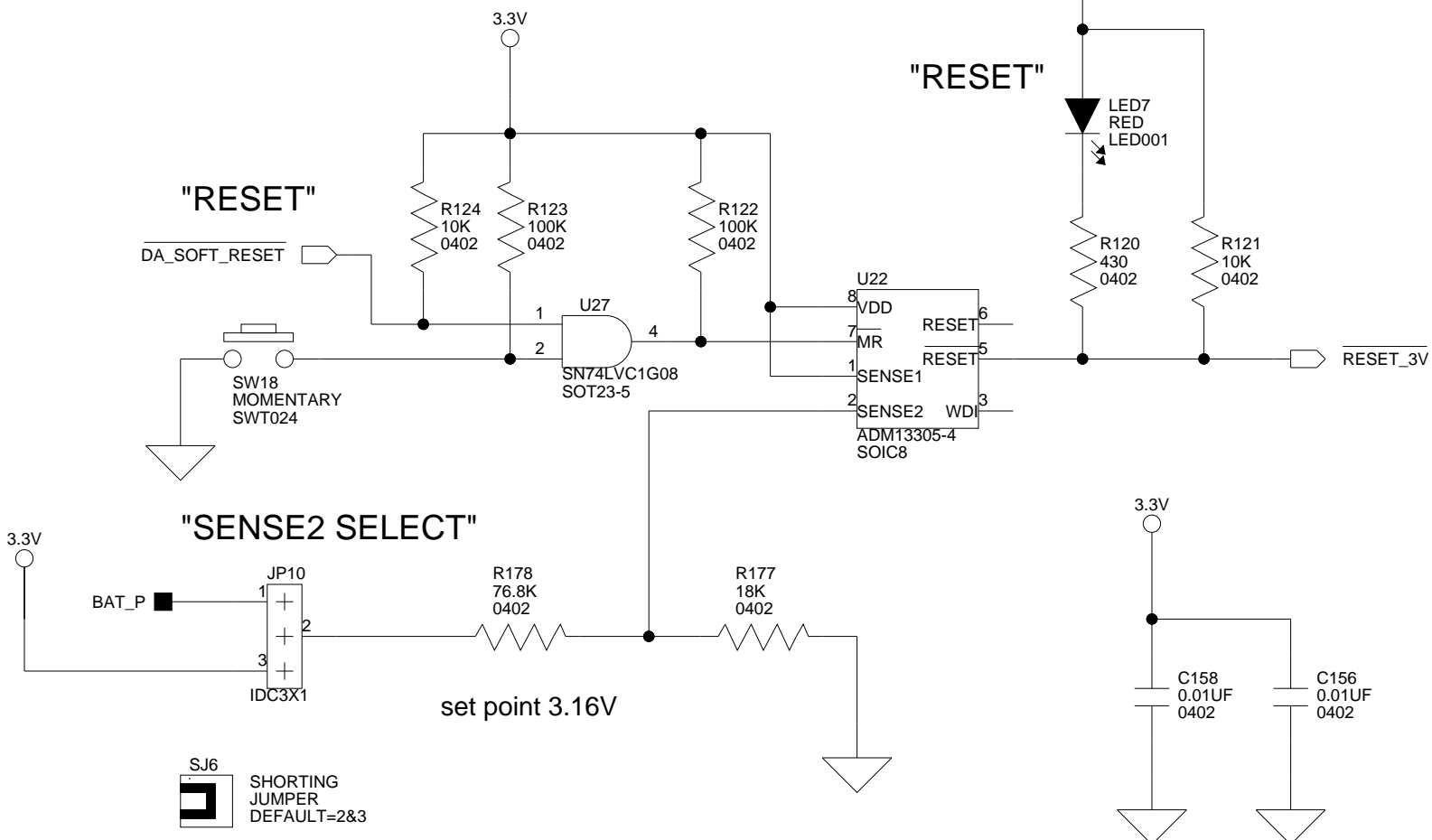
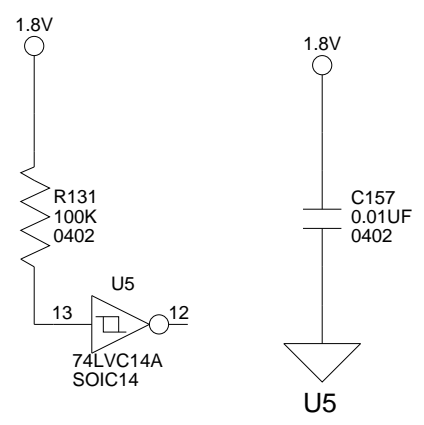
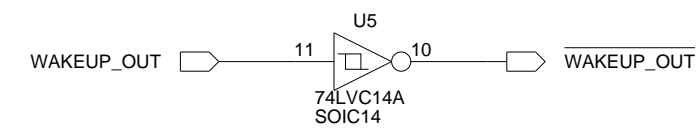
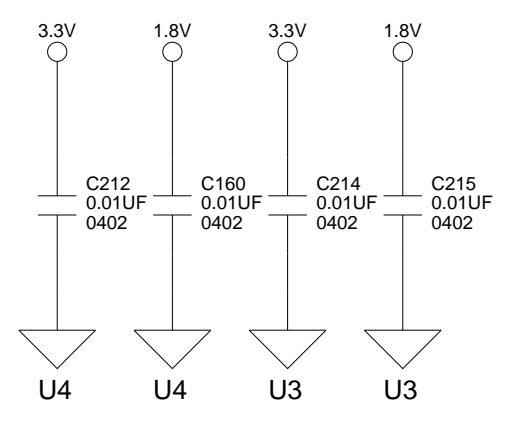
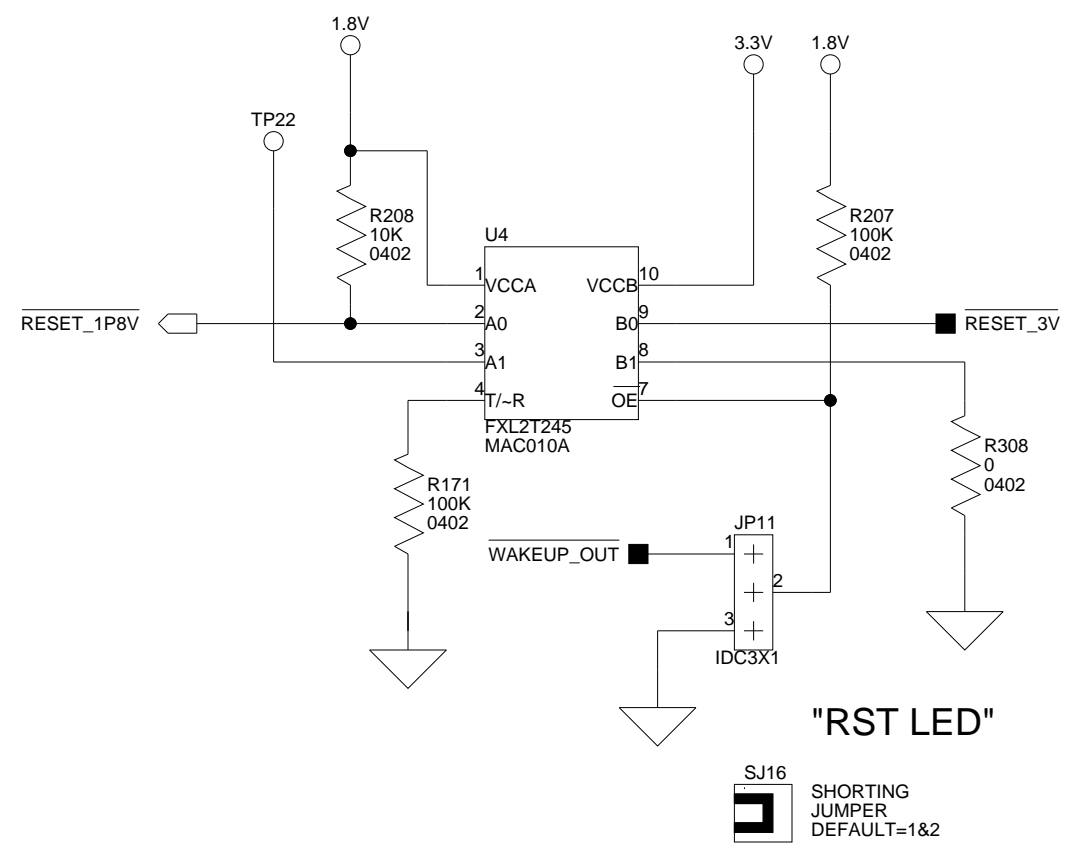
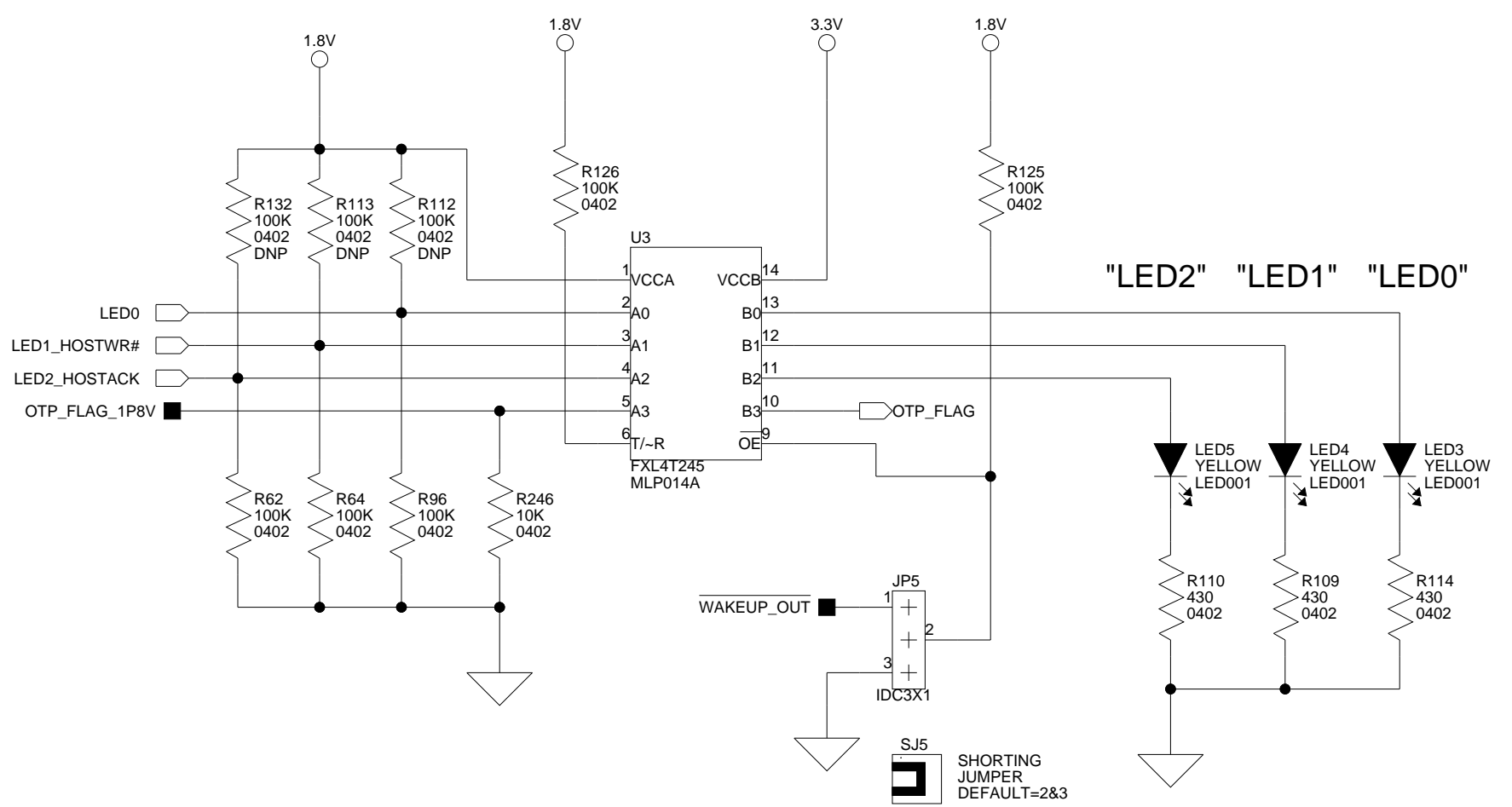
1	12	PUSHBUTTON1_HWAIT
2	11	PUSHBUTTON2_HOSTADDR
3	10	LED2_HOSTACK
4	9	TFS0A_RMIMDINT#_HOSTCE#
5	8	PUSHBUTTON2_HOSTADDR
6	7	PUSHBUTTON2_HOSTADDR

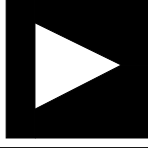
OTP_FLAG_1P8V

USB_VRSEL

DIP6 SWT017

SW20 Position (Default)	FROM	TO	FUNCTION
1 (ON)	Pushbutton 1 (SW19)	DSP (U1, PG0)	ON (PB1), OFF (UART 1 CTS U21, HOST connector P9.12, expansion interface P2.37, P4.37, J1.52)
2 (ON)	Pushbutton 2 (SW15)	DSP (U1, PG13)	ON (PB2), OFF (host connector P9.8, OTP flag for writes SW20.8, OTG voltage select SW13.7, expansion interface P2.40, P4.40, J1.53)
3 (OFF)	Power down pushbutton (SW16)	DSP (U1, PG12)	OFF (LED2 not driven by power down pushbutton). ON (SW16 drives PG12).
4 (OFF)	Wake pushbutton (SW16)	DSP (U1, PG15)	ON (connects wake pushbutton SW17 to PG15).
5 (OFF)	OTP_FLAG_1P8V (U3)	DSP (U1, PG13)	ON (PG13 controls OTP flag for OTP writes. NOTE requires SW20.2 OFF, SW20.6 OFF and JP16 installed.)
6 (OFF)	USB_VRSEL (U23)	DSP (U1, PG13)	ON (PG13 controls USB_VRSEL PG13 for OTG host power. NOTE requires SW20.2 OFF, SW20.5 OFF.





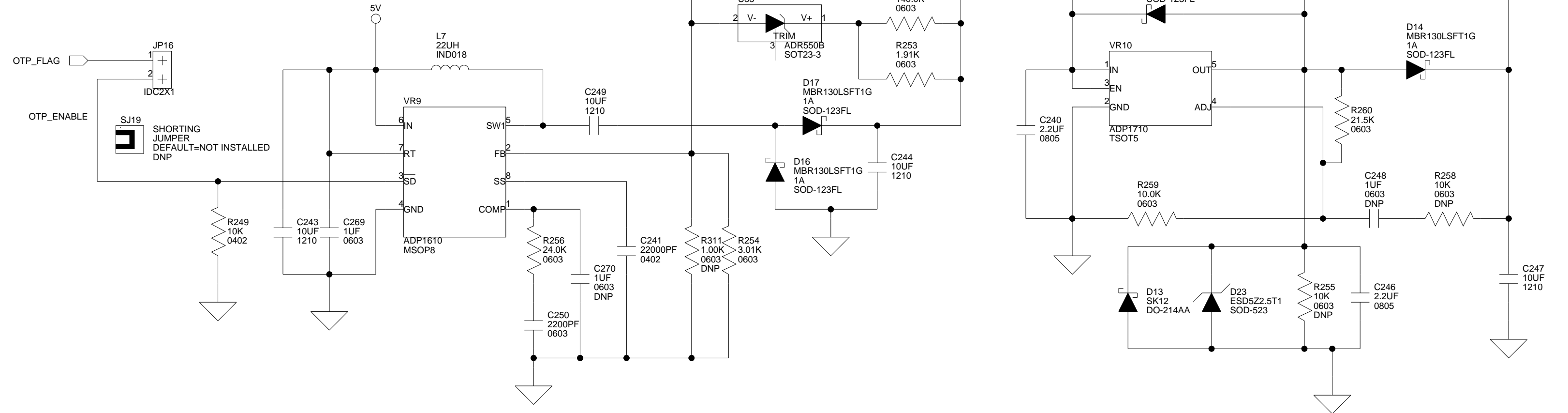
**ANALOG
DEVICES**

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Nashua, NH 03063
PH: 1-800-ANALOGD

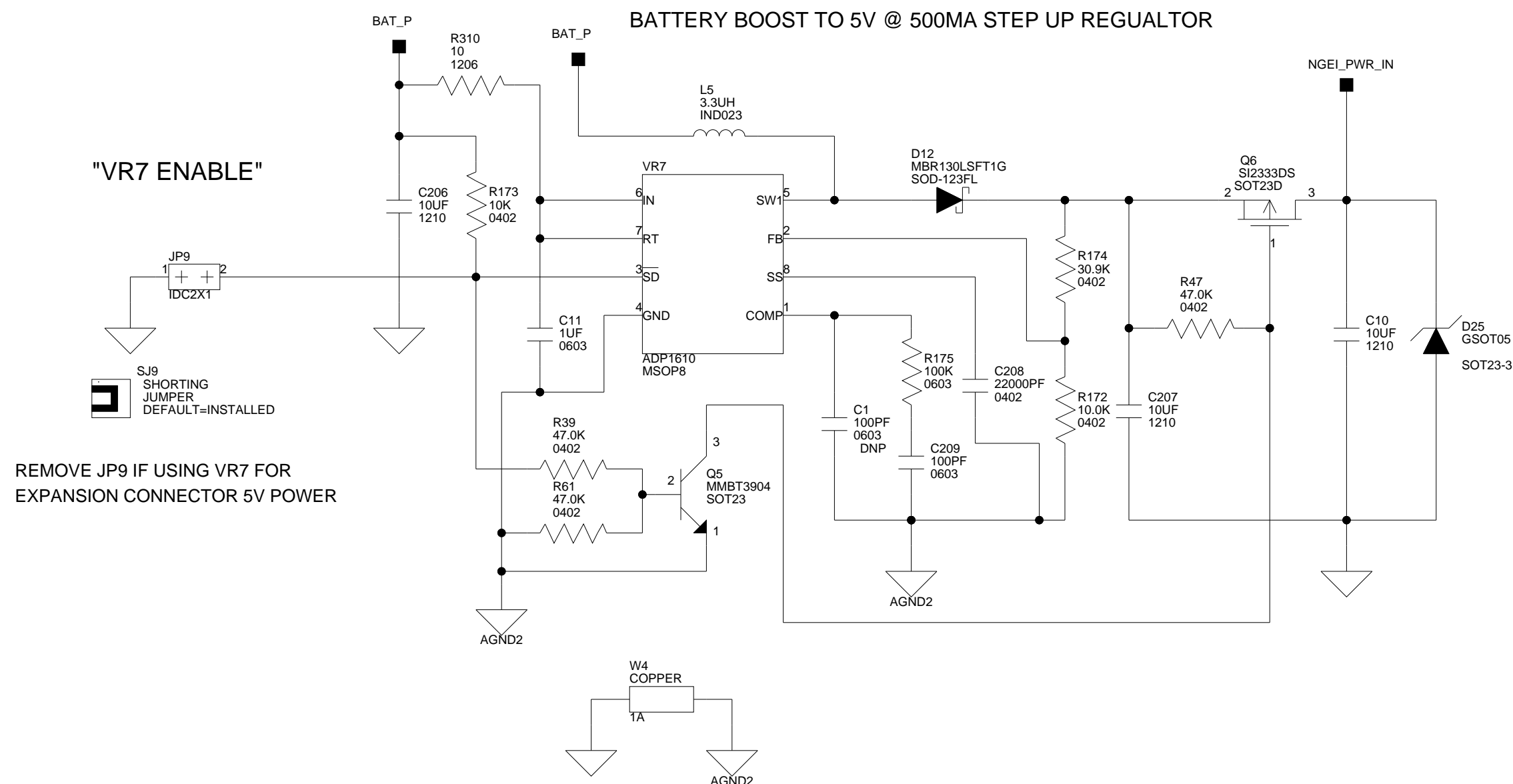
Title			ADSP-526 EZ-BOARD RESET, LEDS		
Size C	Board No.	A0212-2007			Rev 1.1A
Date	1-28-2010_15:55		Sheet	11 of	15

Warning: Only install jumper JP16 when programming OTP. See BF526 datasheet for exact details

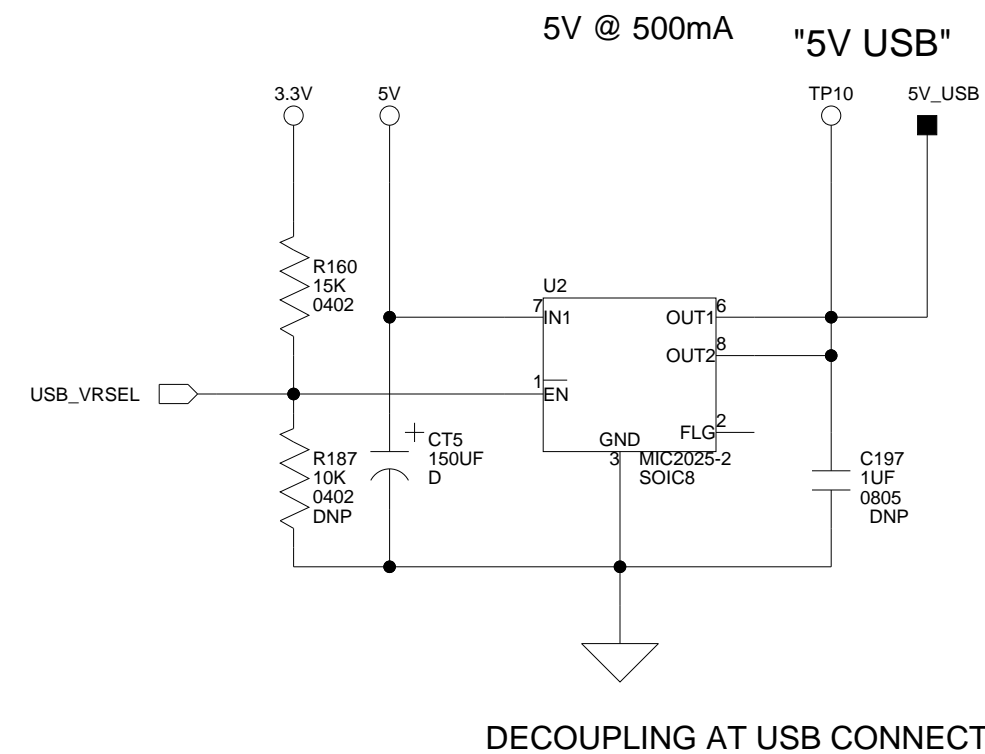
"OTP FLAG ENABLE"



"VR7 ENABLE"



REMOVE JP9 IF USING VR7 FOR
EXPANSION CONNECTOR 5V POWER



DECOUPLING AT USB CONNECTOR



20 Cotton Road
Nashua, NH 03063
PH: 1-800-ANALOGD

Title	ADSP-526 EZ-BOARD OTP POWER, 5V_USB FET
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Size C	Board No. A0212-2007	Rev 1.1A
Date 1-28-2010 15:55	Sheet 13	of 15

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