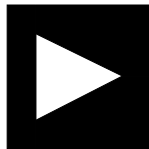


ADSP-BF527 EZ-KIT LITE SCHEMATIC



**ANALOG
DEVICES**

20 Cotton Road
Nashua, NH 03063
PH: 1-800-ANALOGD

Title		ADSP-BF527 EZ-KIT Lite TITLE		
Size C	Board No.		A0208-2006	Rev 2.2
Date	1-13-2010_14:04		Sheet	1 of 15

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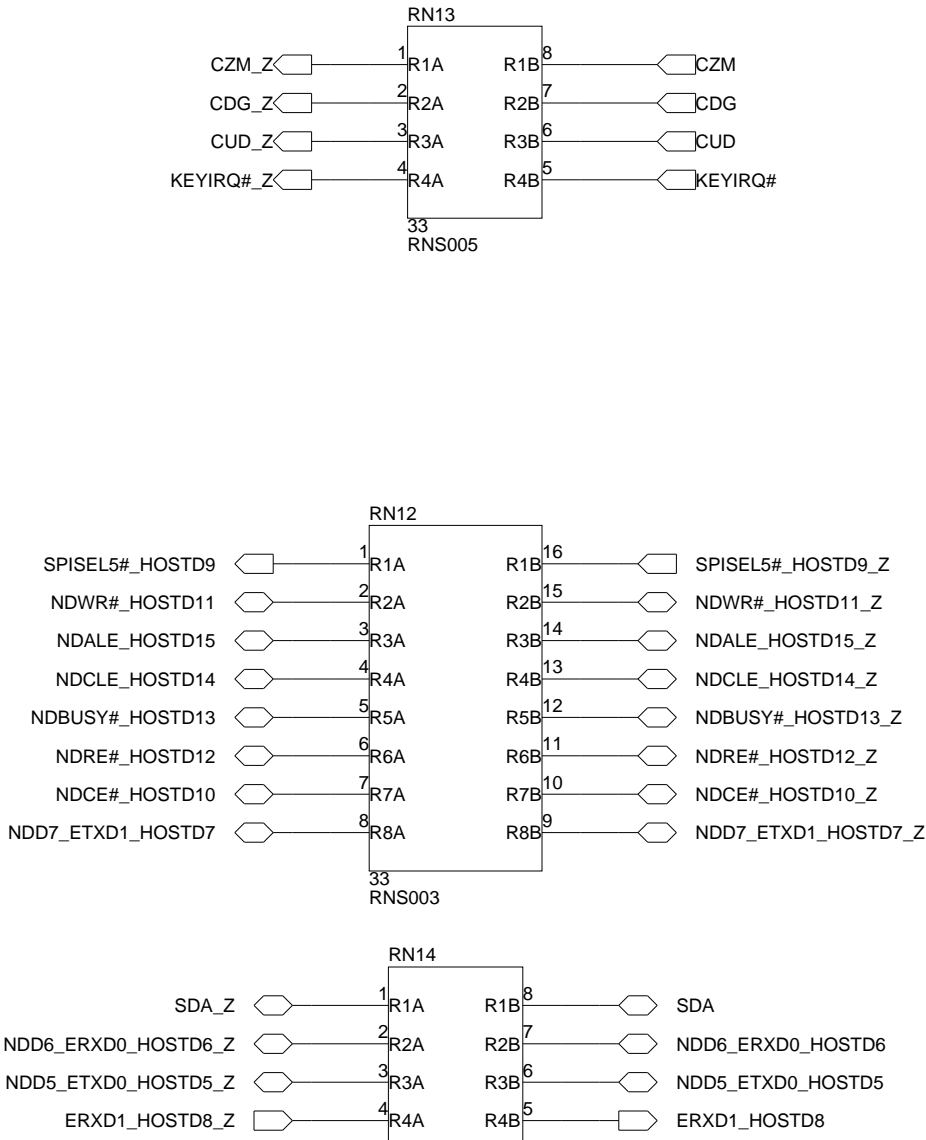
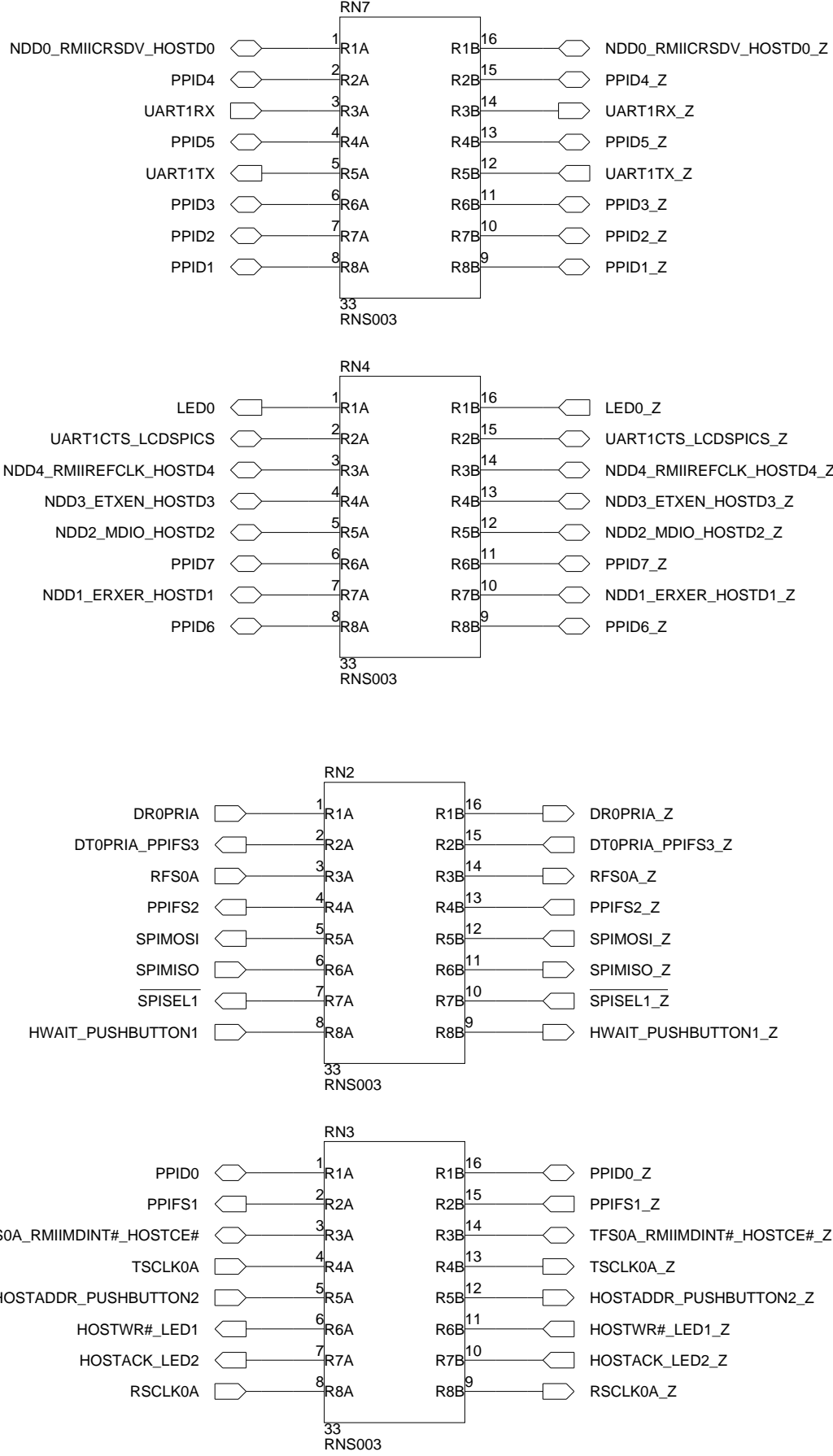
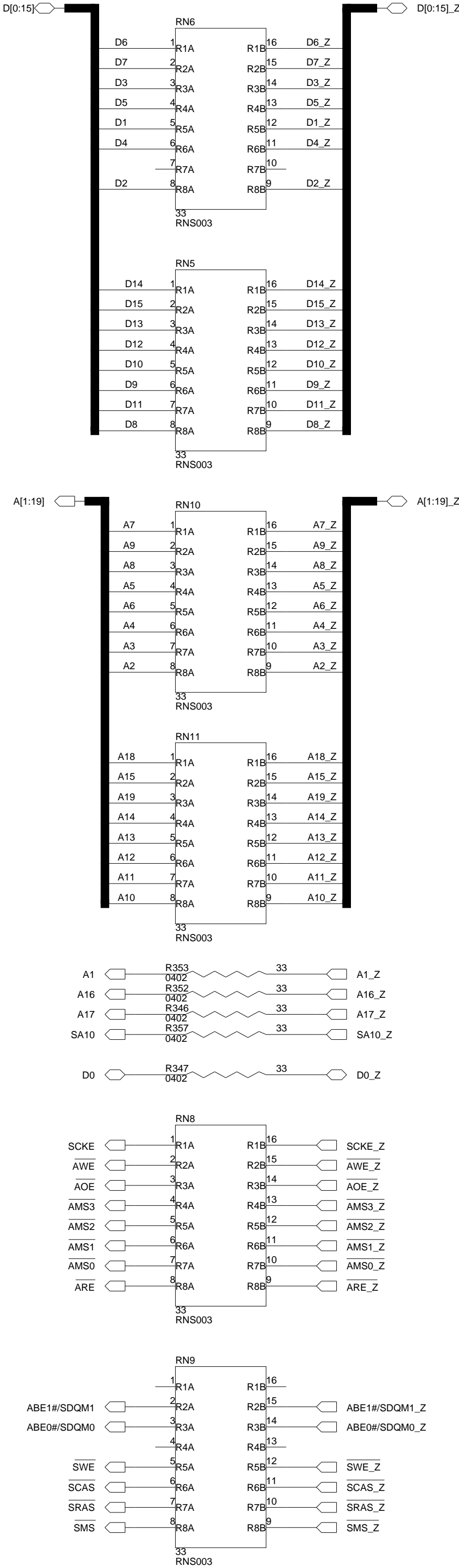
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		ANALOG DEVICES		20 Cotton Road Nashua, NH 03063 PH: 1-800-ANALOGD	
Title ADSP-BF527 EZ-KIT Lite SERIES TERMINATION					
Size C	Board No. A0208-2006			Rev 2.2	
Date	1-13-2010_14:04		Sheet	3 of	15

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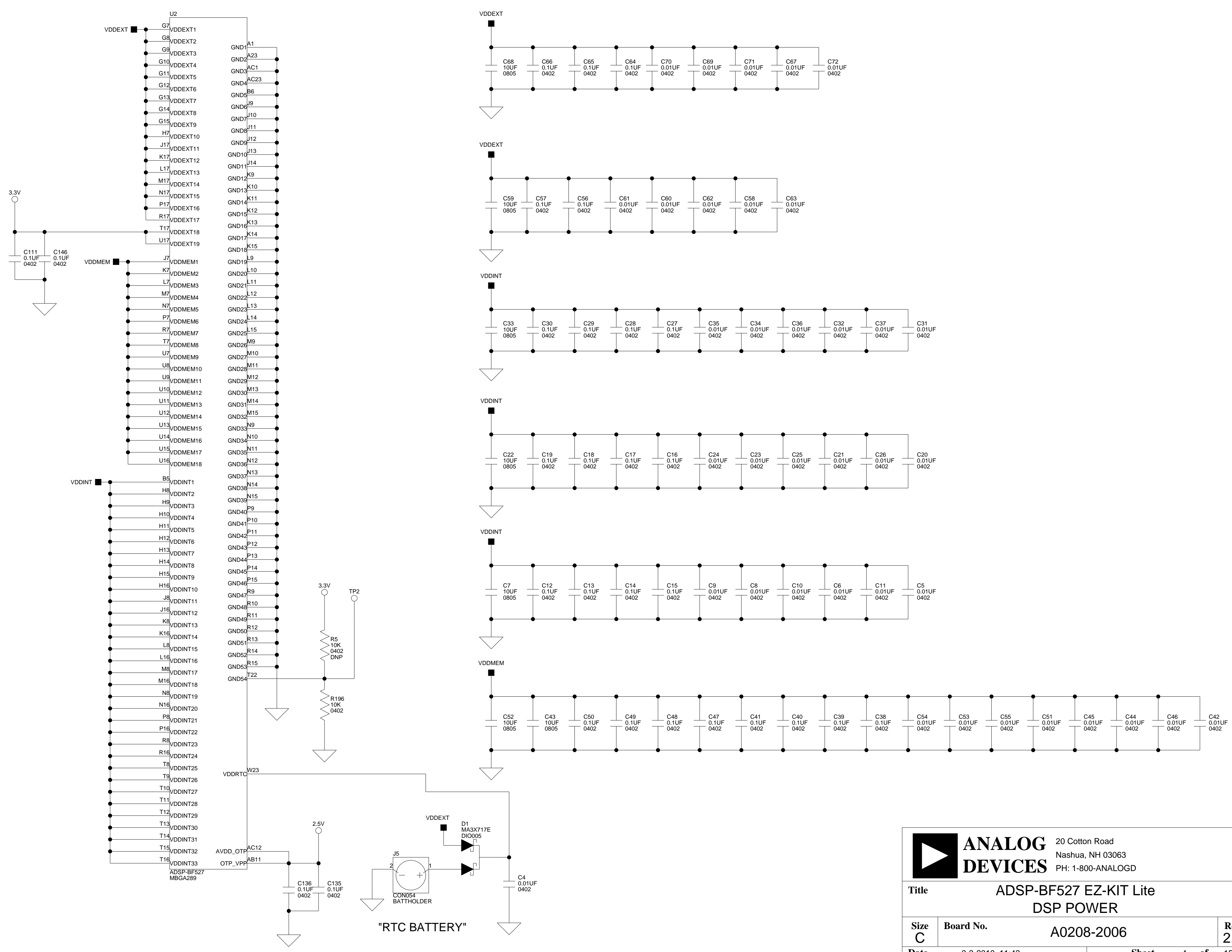
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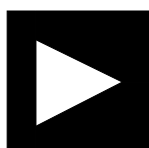


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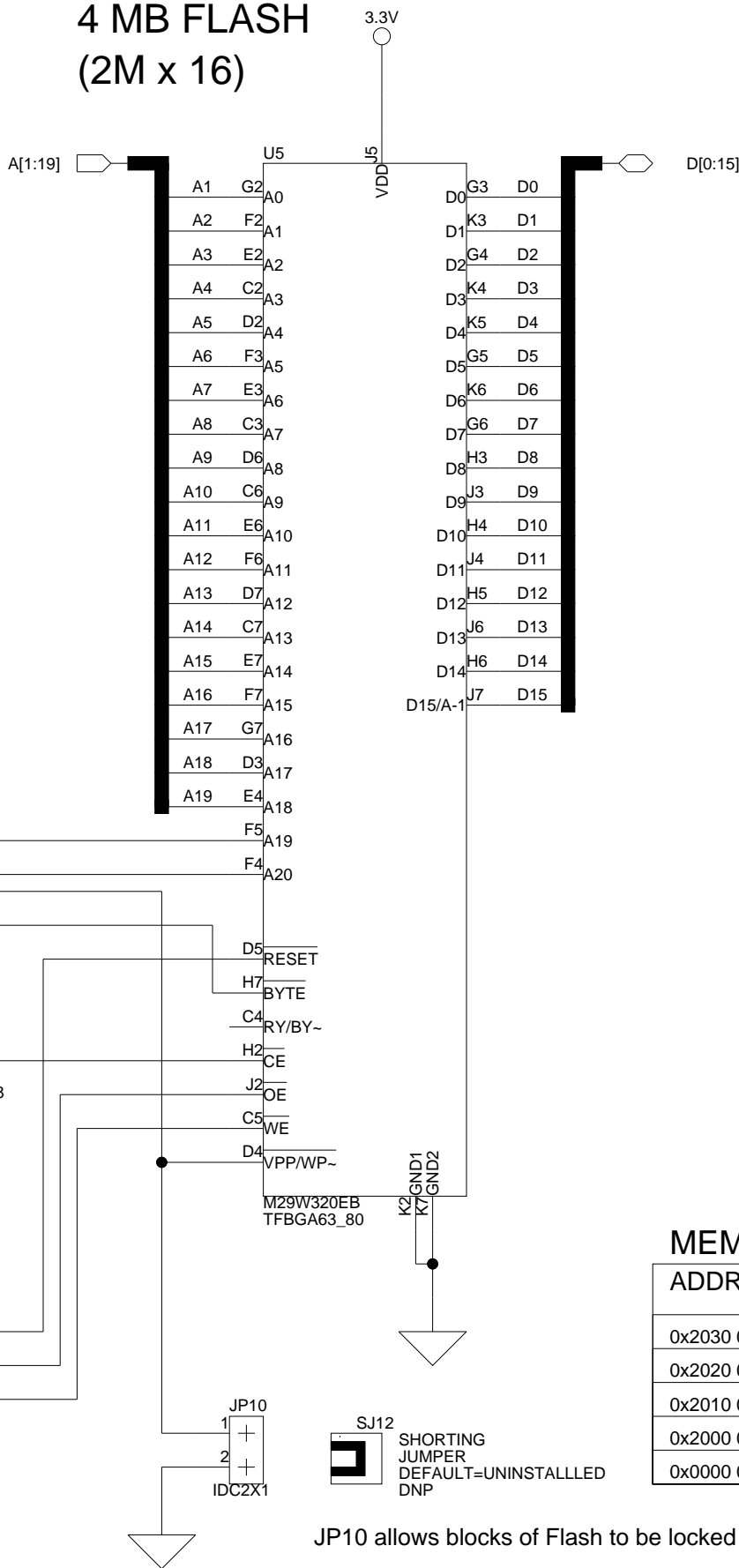
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		ANALOG DEVICES		20 Cotton Road Nashua, NH 03063 PH: 1-800-ANALOGD	
Title ADSP-BF527 EZ-KIT Lite DSP POWER					
Size C	Board No. A0208-2006			Rev 2.2	
Date	3-3-2010_11:43			Sheet	4 of 15

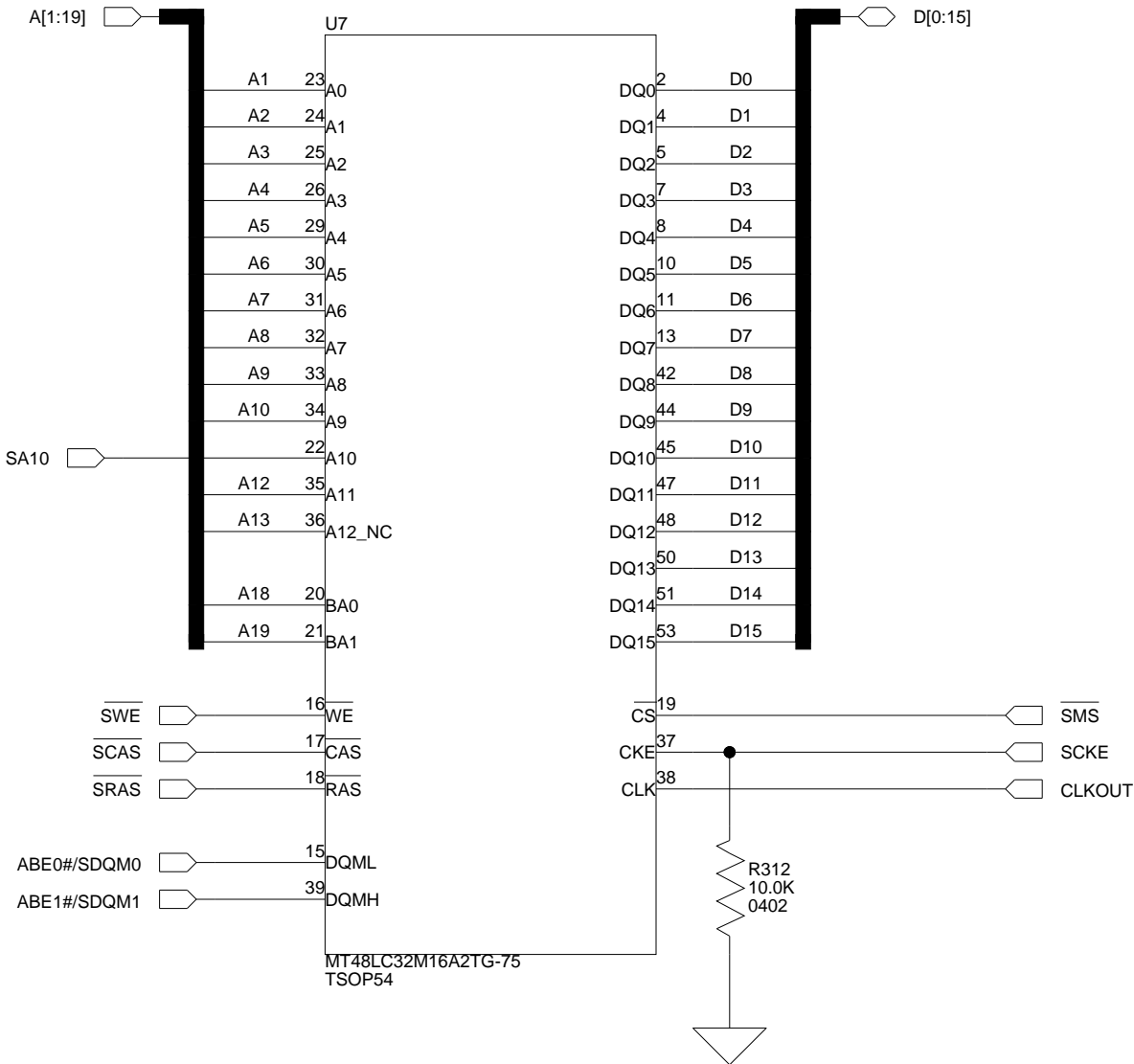
SW7: FLASH Enable

POS.	FROM	TO	DEFAULT	ALTERNATE FUNCTION / OFF MODE
SW7.1	DSP (U2)	FLASH (U5)	ON	J2.65 (Expansion Interface)
SW7.2	DSP (U2)	FLASH (U5)	ON	J2.63 (Expansion Interface)
SW7.3	DSP (U2)	FLASH (U5)	ON	J2.61 (Expansion Interface)
SW7.4	DSP (U2)	FLASH (U5)	ON	J2.59 (Expansion Interface)

4 MB FLASH
(2M x 16)



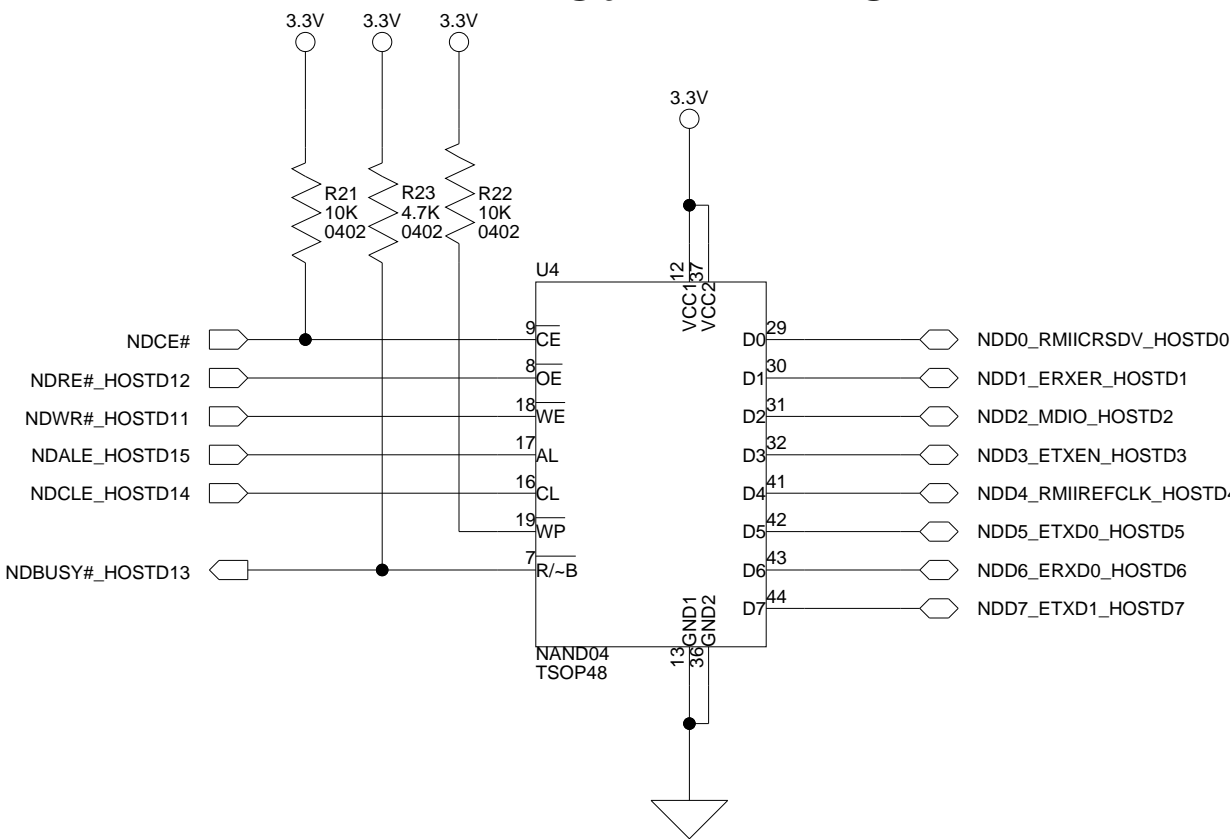
64MB SDRAM (32M x 16)



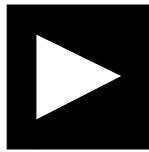
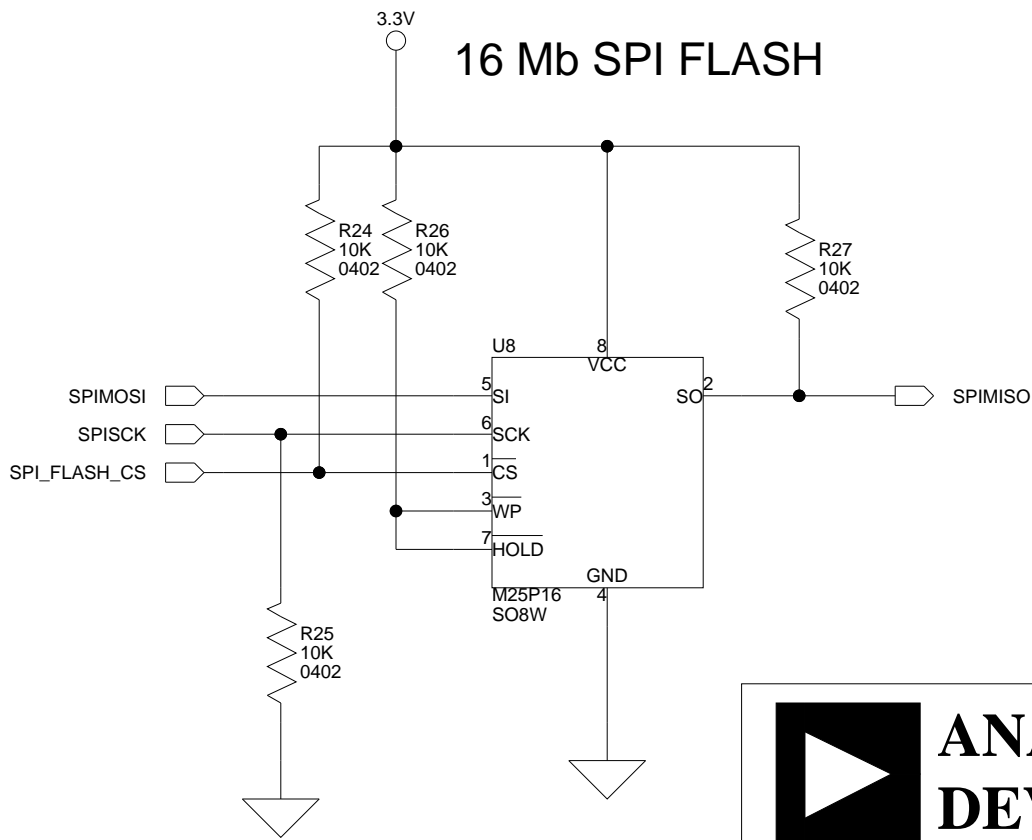
MEMORY MAP

ADDRESS RANGE	SELECT LINE	TYPE
0x2030 0000 - 0x203F FFFF	ASYNC BANK 3	FLASH
0x2020 0000 - 0x202F FFFF	ASYNC BANK 2	FLASH
0x2010 0000 - 0x201F FFFF	ASYNC BANK 1	FLASH
0x2000 0000 - 0x200F FFFF	ASYNC BANK 0	FLASH
0x0000 0000 - 0x03FF FFFF	NONE	SDRAM

4Gb NAND FLASH



16 Mb SPI FLASH



**ANALOG
DEVICES**

20 Cotton Road
Nashua, NH 03063
PH: 1-800-ANALOGD

Title **ADSP-BF527 EZ-KIT Lite
MEMORY**

Size **C** Board No. **A0208-2006**

Rev **2.2**

Date 1-22-2010_15:22

Sheet 5 of 15

"USB CLK"

"USB OTG"



ANALOG
DEVICES

20 Cotton Road
Nashua, NH 03063
PH: 1-800-ANALOGD

Title

ADSP-BF527 EZ-KIT Lite
DSP USB OTG

Size
C

Board No.

A0208-2006

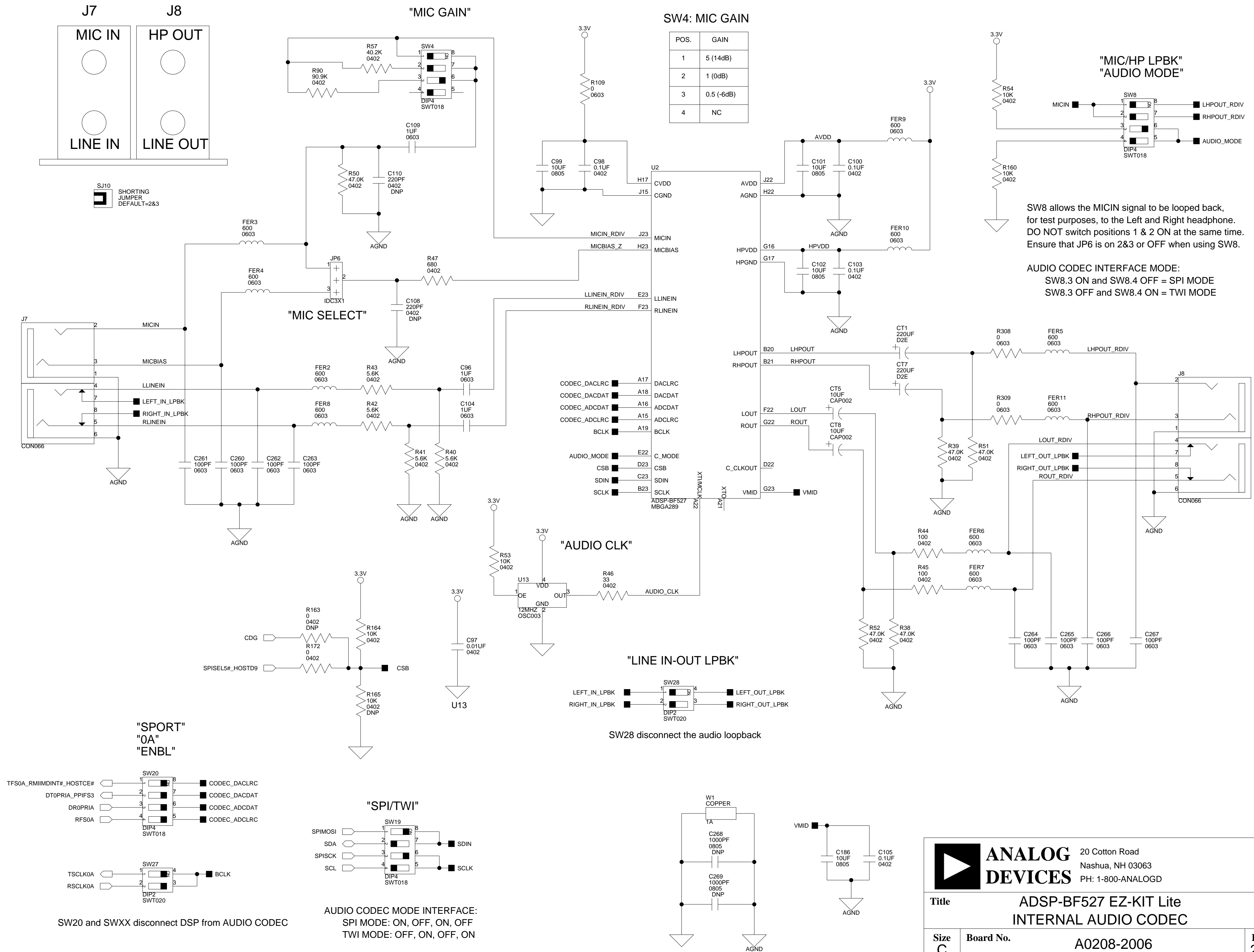
Rev
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Date

1-22-2010_15:22

Sheet

6 of 15



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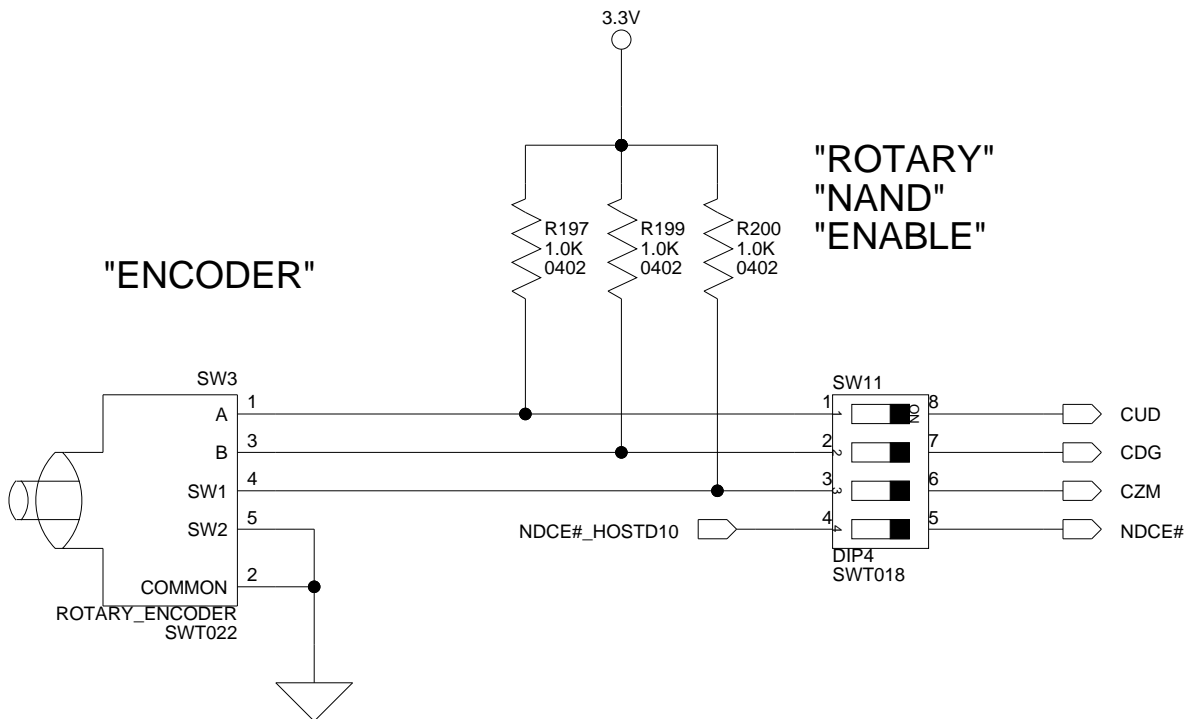
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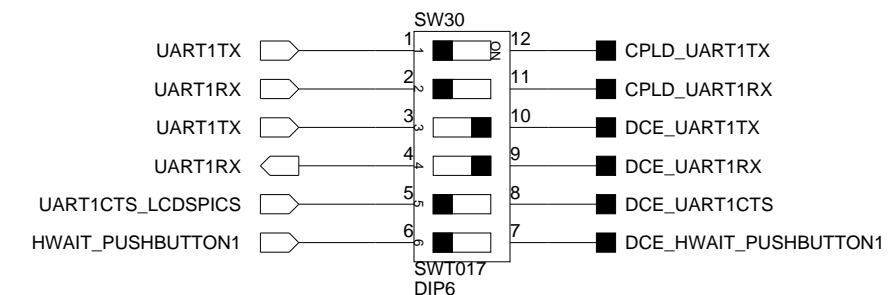
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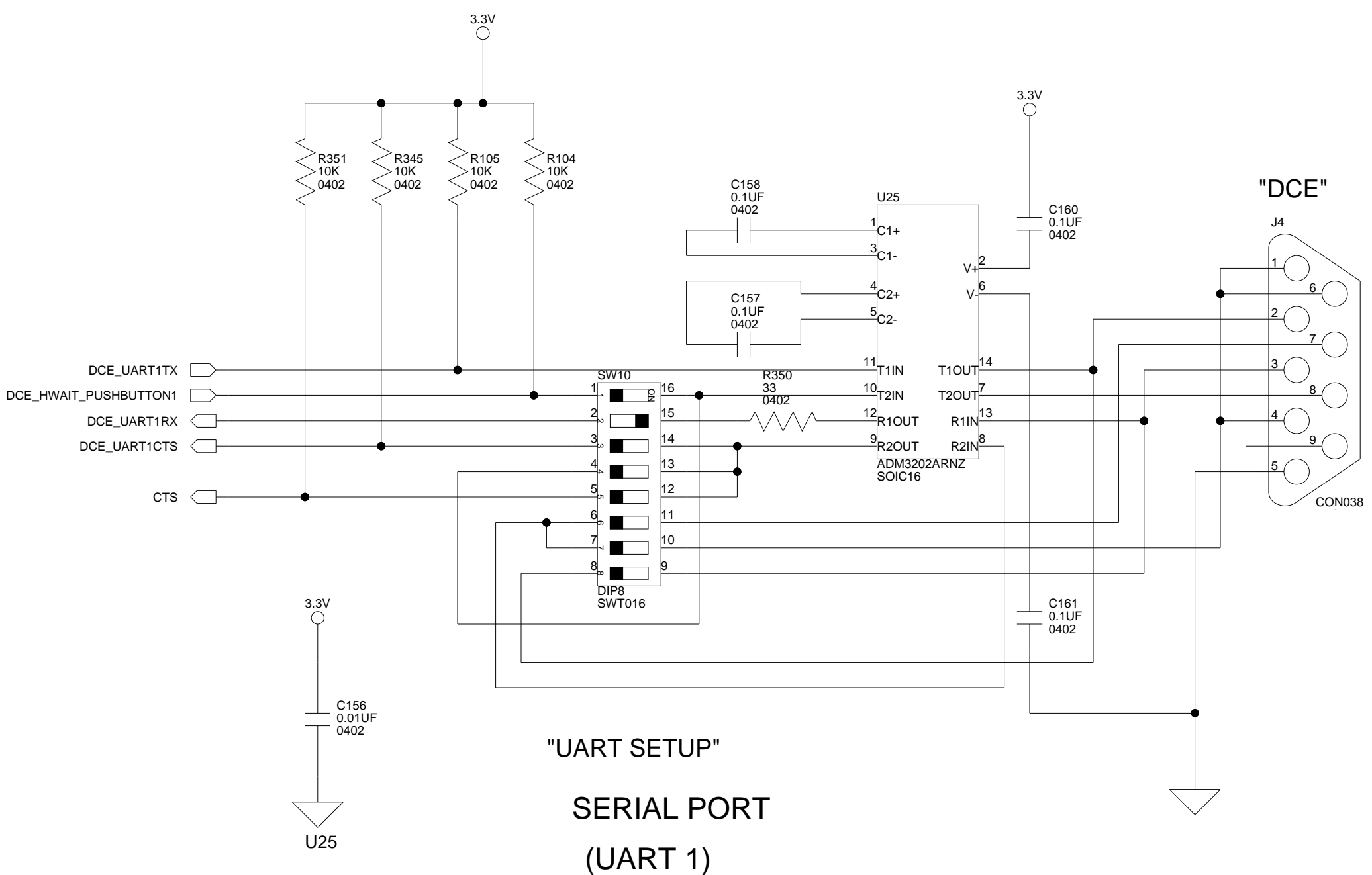
SW11: Rotary NAND enable

POS.	FROM	TO	DEFAULT	ALTERNATE FUNCTION / OFF MODE
SW11.1	Encoder (SW3)	DSP (U2, PF13)	ON	Expansion Interface (J2.34, J2.52) Stamp buffer (U34)
SW11.2	Encoder (SW3)	DSP (U2, PF12)	ON	Expansion Interface (J2.30, J2.51), Stamp buffer (U30)
SW11.3	Encoder (SW3)	DSP (U2, PF11)	ON	Expansion Interface (J2.32, J2.50), Stamp buffer (U30)
SW11.4	DSP (U2, PH10)	NAND (U4)	ON	Host connector (P13.11), Expansion Interface (J3.35)

"CPLD 14-15"
"DCE ENB"



Allows CPLD PPI data lines 14-15 and UART1 signals to be disconnected from processor.

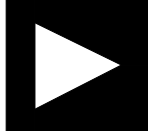


"UART SETUP"
SERIAL PORT
(UART 1)

SW10: UART Setup

Position	Function
1 and 3	Enable flow control
2	Disconnects the UART_RX signal from the processor; the UART_RX signal can be used for another function
4	Connects the RTS and CTS signals
5	Allows the host to reset the EZ-KIT Lite via the CTS signal
6 and 7	Select the source for the CTS signal
8	Loopback RX and TX data

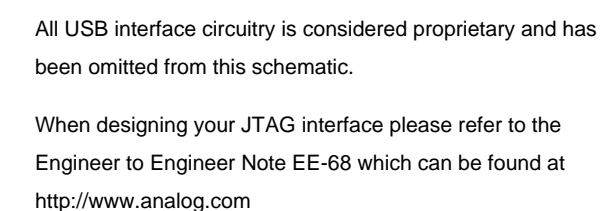
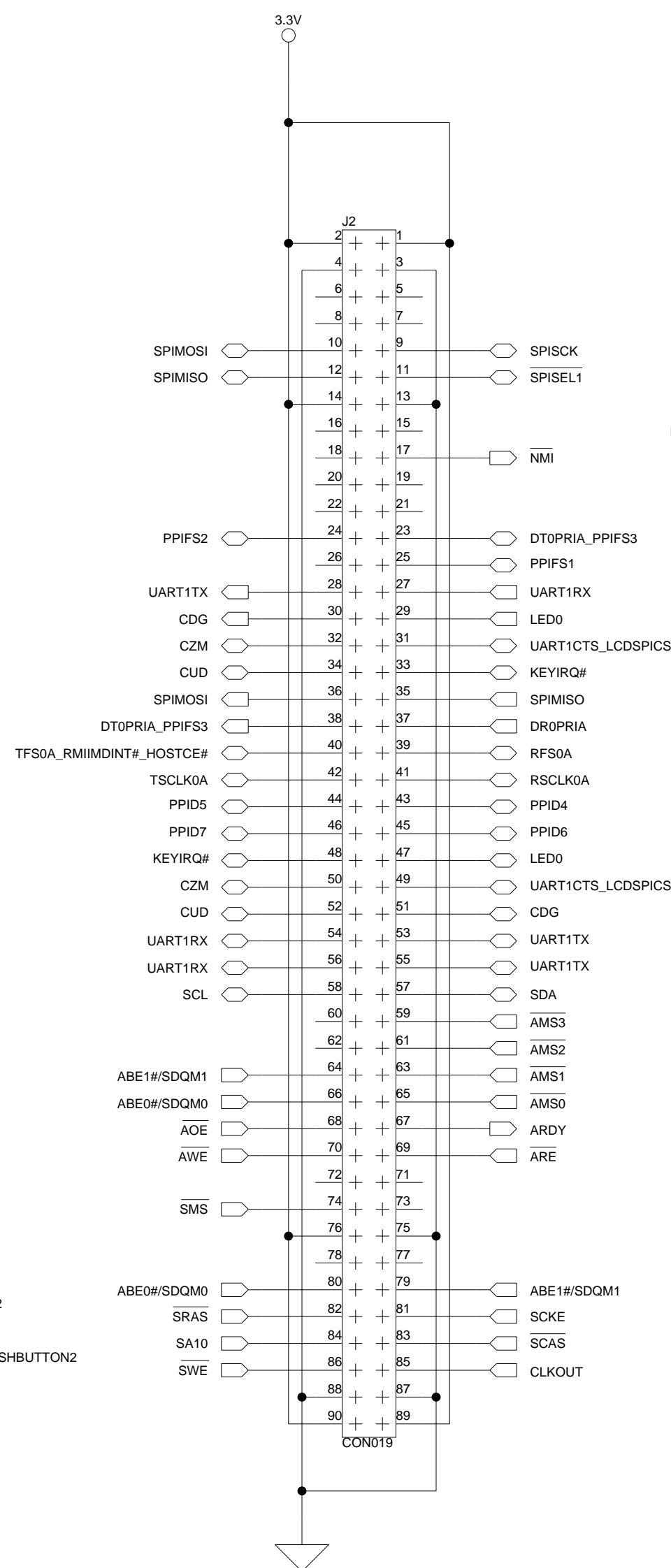
DEFAULT OFF, ON, OFF, OFF, OFF, OFF, OFF, OFF

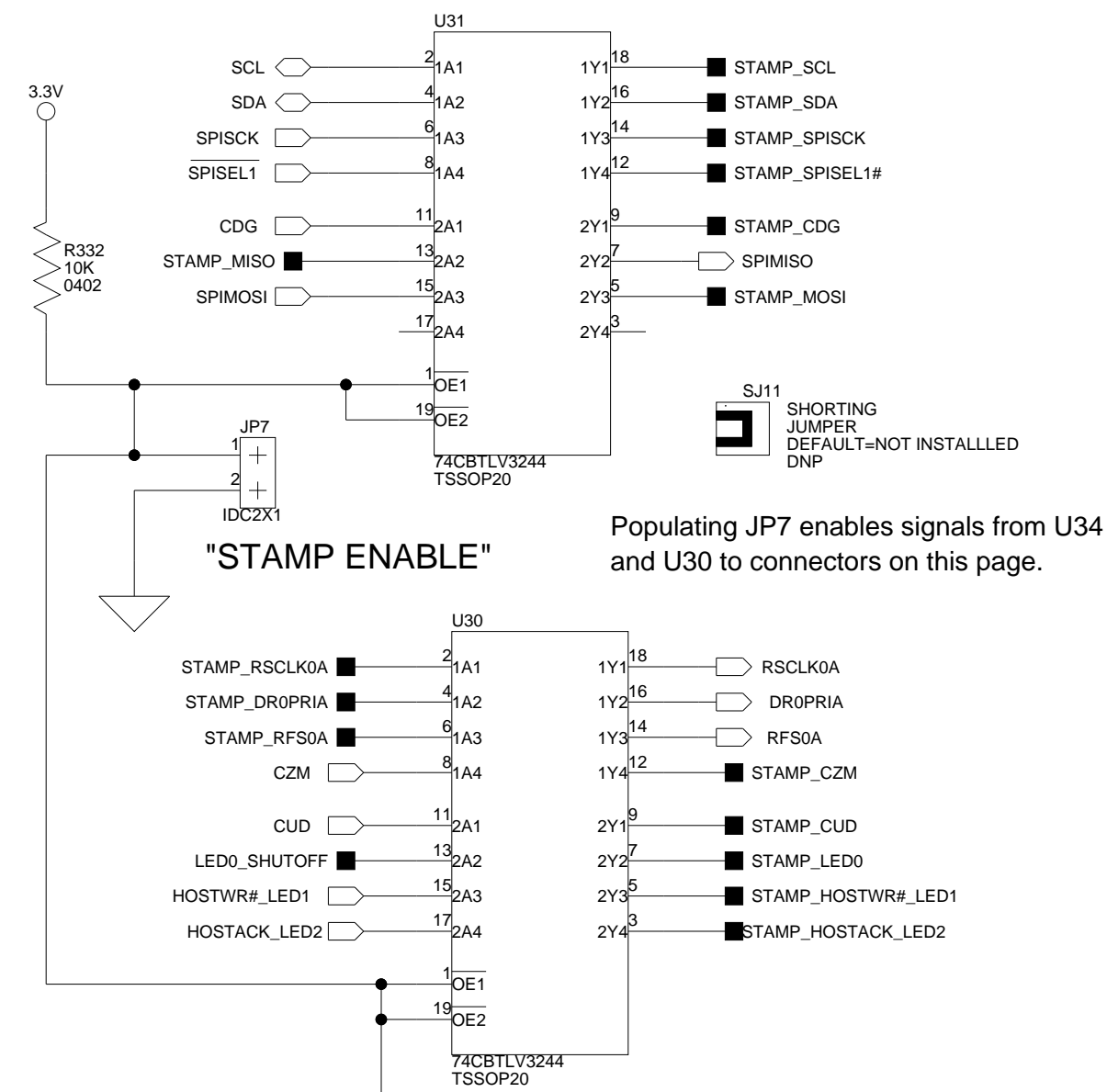
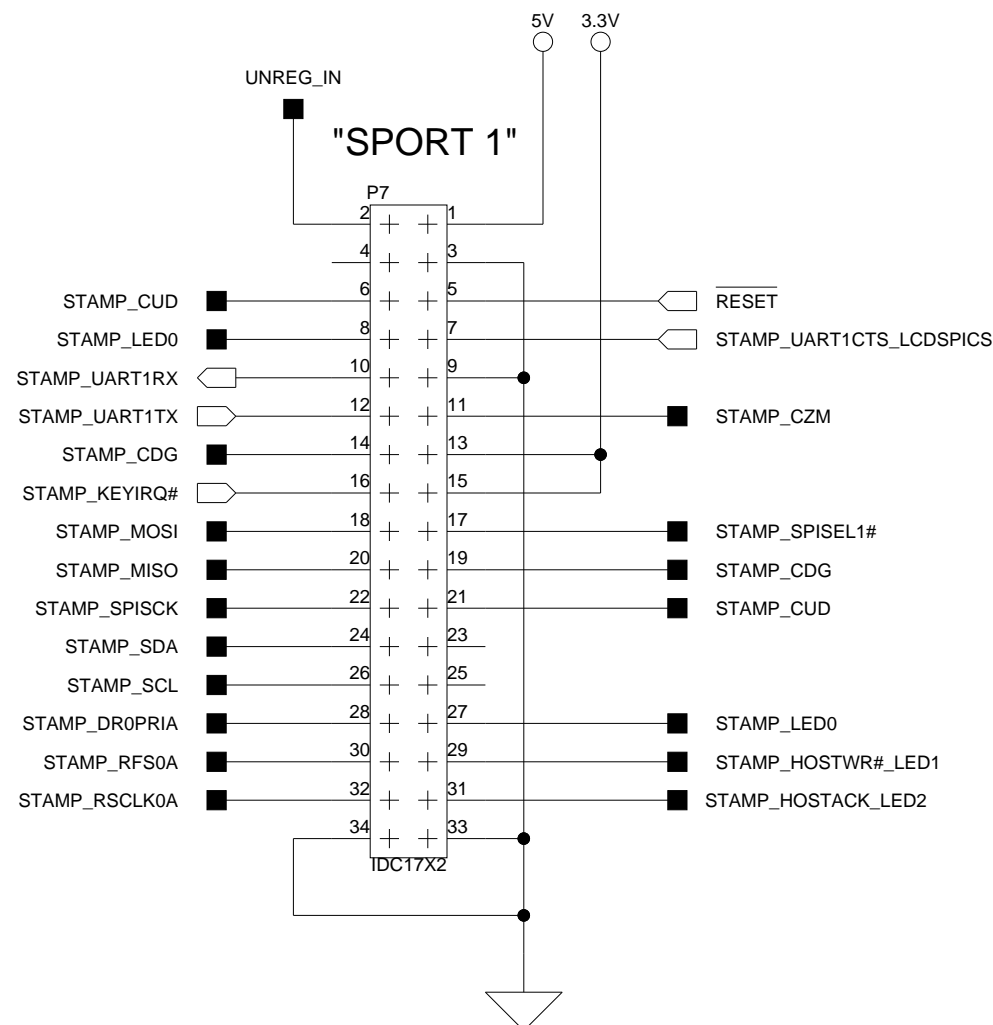
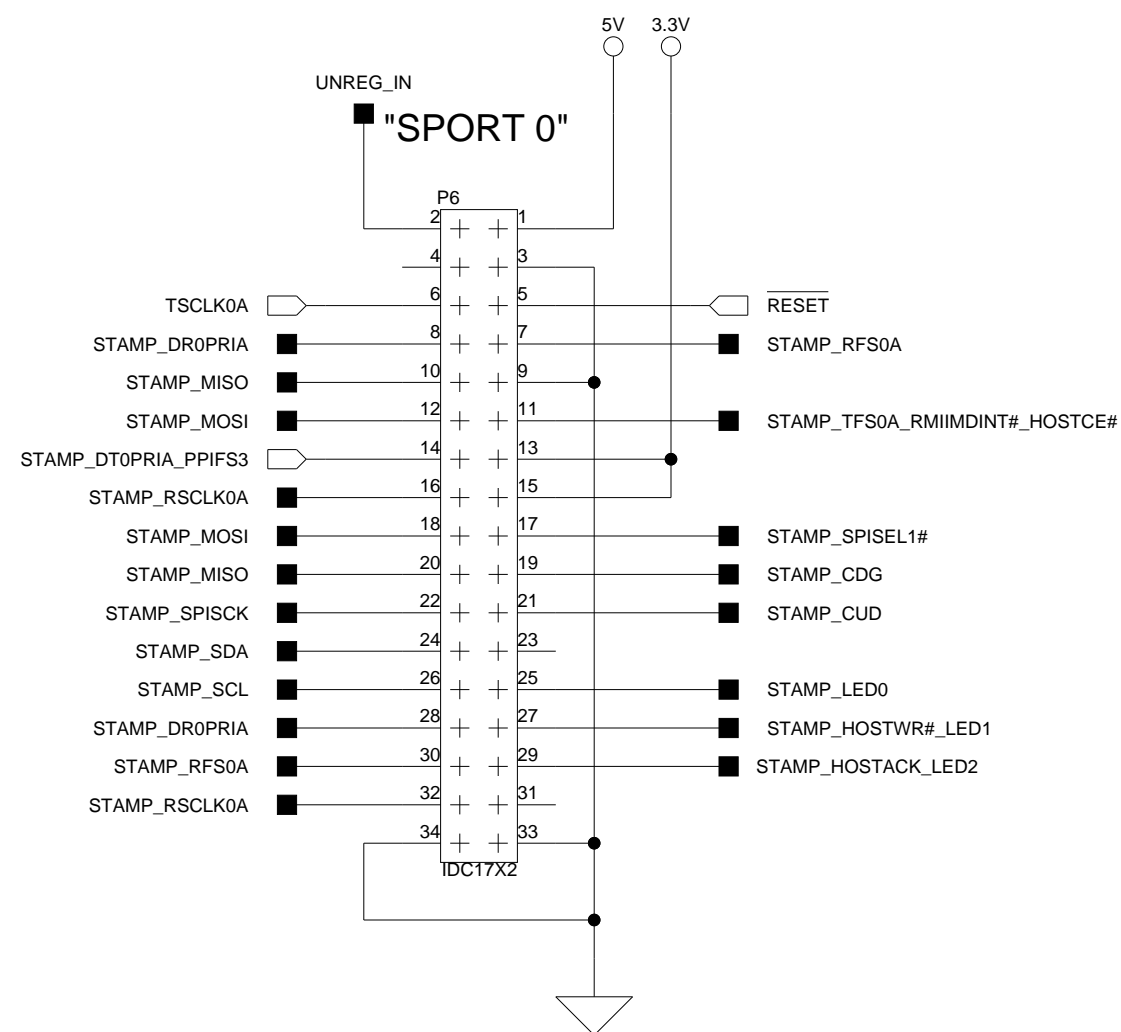


**ANALOG
DEVICES**

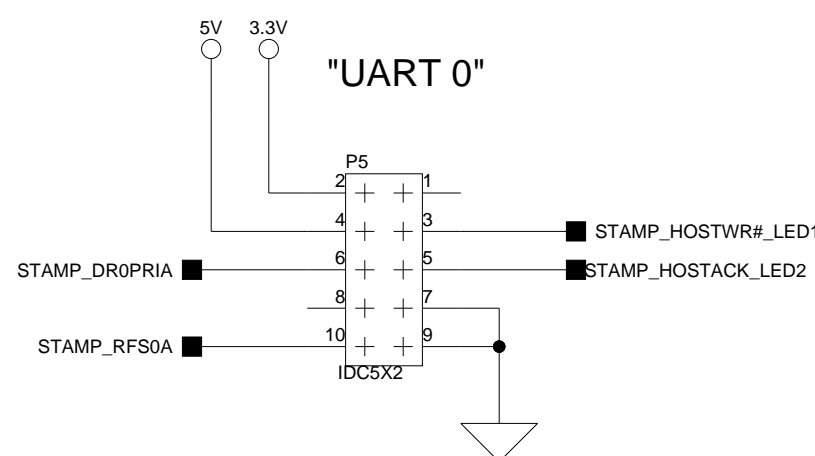
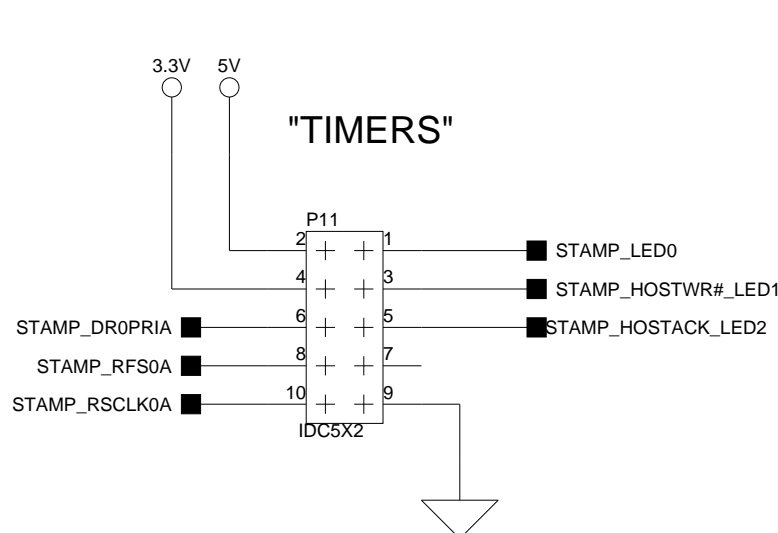
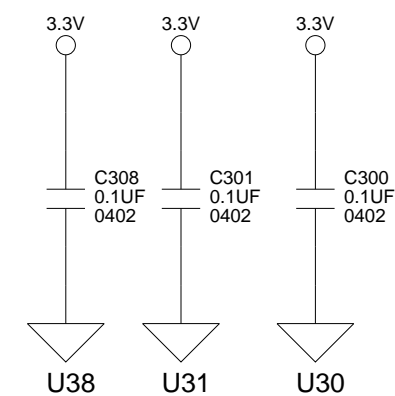
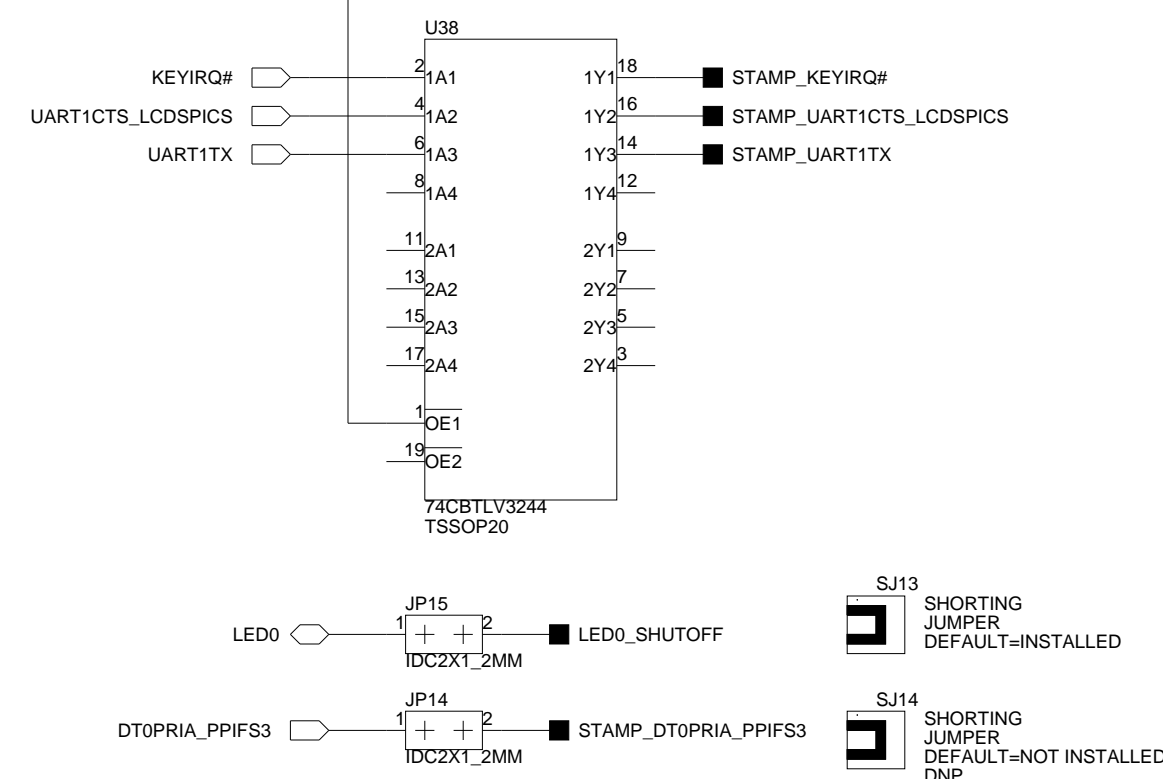
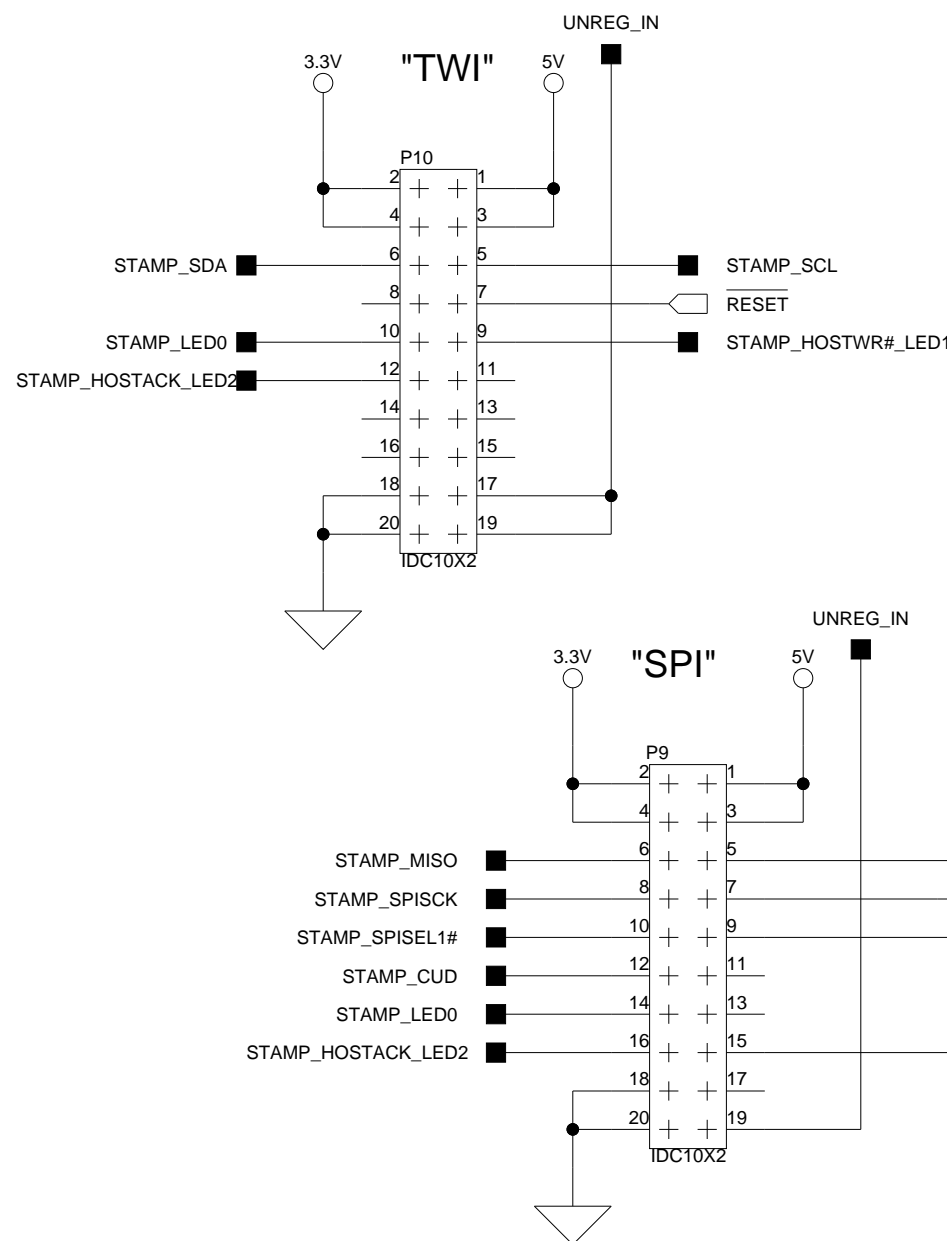
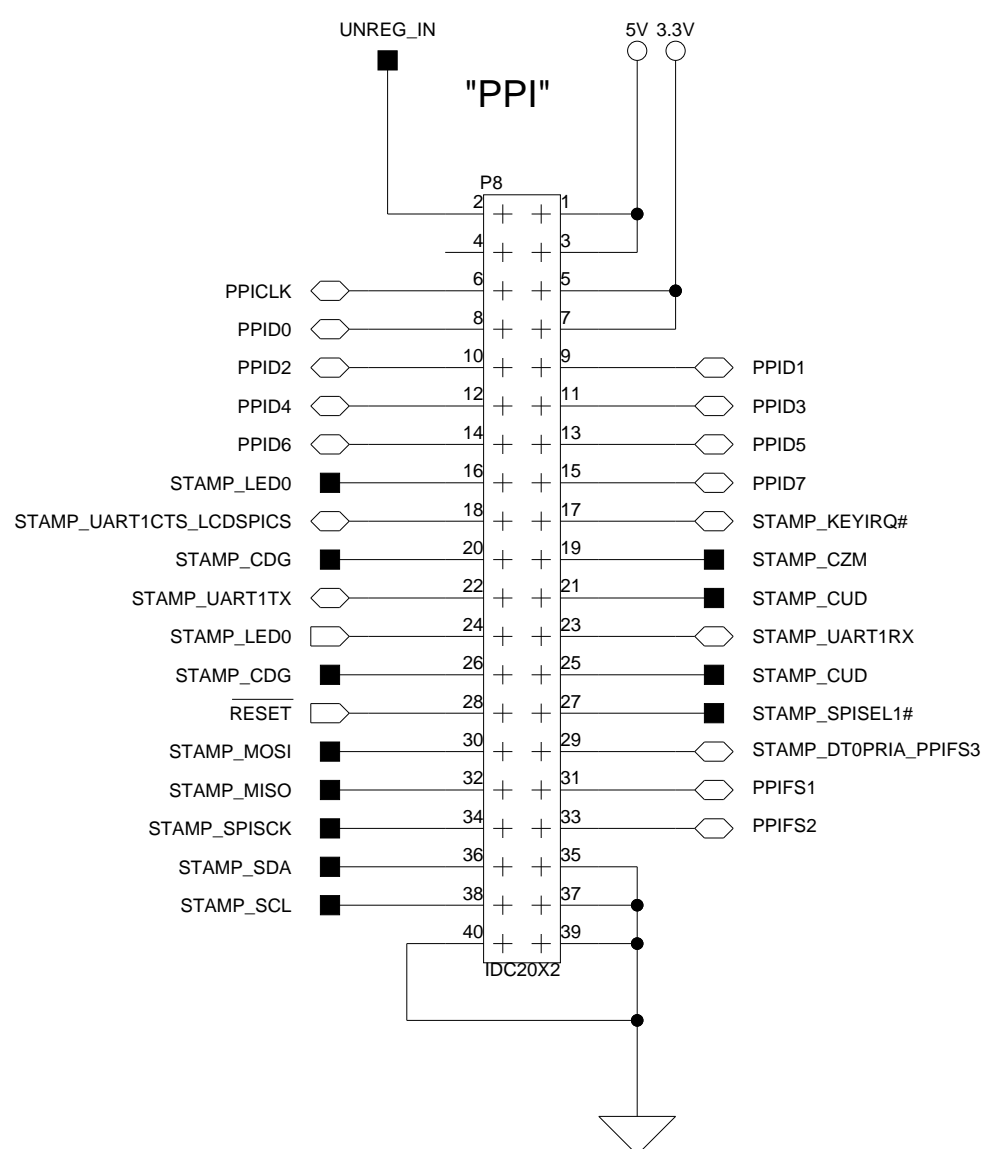
20 Cotton Road
Nashua, NH 03063
PH: 1-800-ANALOGD

Title ADSP-BF527 EZ-KIT Lite ROTARY SWITCH, RS232		
Size C	Board No. A0208-2006	Rev 2.2
Date 1-27-2010_11:46	Sheet 11 of	15





Populating JP7 enables signals from U34 and U30 to connectors on this page.



"TFS0A/HOSTCE"
"ENABLE"



SW21 disconnects TFS0A_RMIIMDINT#_HOSTCE# from SPORT conn P6.11 and HOST conn P13.6

		ANALOG DEVICES	20 Cotton Road Nashua, NH 03063 PH: 1-800-ANALOGD
Title ADSP-BF527 EZ-KIT Lite STAMP CONNECTORS			
Size C	Board No.	A0208-2006	
Date	1-20-2010_14:13	Sheet	14 of 15
		Rev	2.2

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