

DESCRIPTION

Demonstration circuit DC840 is a step-down converter, using the LTC3448 monolithic synchronous buck regulator. The DC840 has an input voltage range of 2.5V to 5.5V, and is capable of delivering up to 600mA of output current. The output voltage of DC840 can be set as low as 0.6V, the reference voltage of the LTC3448. The DC840 is especially made for noise sensitive applications, due to a unique design that allows the LTC3448 to automatically switch – in auto mode - from a switching regulator into a linear regulator at low output currents (In switch-mode, the LTC3448 operates in pulse-skipping mode at medium-low output currents). This operation of the LTC3448 maintains low output ripple voltage under all load conditions. There is a mode pin to select auto mode, ON (linear regulator only), or OFF (switching regulator only).

The DC840 is a high efficiency circuit - over 90%, and during shutdown, the DC840 consumes less than 1uA typically. It consumes only 32uA of quiescent current in linear regulation operation. Because of the frequency-select capability of the LTC3448, the DC840 can operate at fixed frequencies of 1.5 MHz or 2.25 MHz, allowing the exclusive use of low profile surface mount components. The DC840 can also be clocked by an external oscillator, due to a phase-lock loop circuit in the LTC3448. These features, plus the LTC3448 coming in low profile 8-Lead DFN or MSOP packages, make the DC840 an ideal circuit for use in battery-powered, hand-held applications.

Design files for this circuit board are available. Call the LTC factory.

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Table 1. Performance Summary ($T_A = 25^\circ\text{C}$)

PARAMETER	CONDITION	VALUE
Minimum Input Voltage		2.5V
Maximum Input Voltage		5.5V
Output Voltage V_{OUT}	$V_{IN} = 2.5\text{V to } 5.5\text{V}$, $I_{OUT1} = 0\text{A to } 600\text{ mA}$	1.2V $\pm 4\%$
Typical Output Ripple V_{OUT}	$V_{IN} = 5\text{V}$, $I_{OUT1} = 600\text{ mA}$	20mV _{p-p}
Output Regulation	Line	$\pm 1\%$
	Load	$\pm 1\%$
Nominal Switching Frequency		1.5 or 2.25 MHz

QUICK START PROCEDURE

DC840 is easy to set up to evaluate the performance of the LTC3448. Refer to Figure 1. for proper measurement equipment setup and follow the procedure outlined below. Before proceeding to test, insert

jumper JP1 shunt into the off (lower) position, connecting the RUN pin to ground (GND), which shuts down the circuit.

QUICK START GUIDE FOR DEMONSTRATION CIRCUIT 840

HIGH EFFICIENCY, LOW PROFILE, SYNCHRONOUS BUCK/LDO REGULATOR

NOTE: When measuring the input or output voltage ripple, care must be taken to avoid a long ground lead on the oscilloscope probe. Measure the input or output voltage ripple by touching the probe tip directly across the VIN or VOUT and GND terminals. See Figure 2 for proper scope probe technique.

1. Connect the input power supply and the load to the board. Do not hot-plug Vin or increase Vin over the rated maximum supply voltage of 5.5V, or the part may be damaged.
2. Insert shunts into the 1.5MHz position of jumper JP2 and into the jumper JP7, the 1.8V output voltage option. Also, insert the jumper JP3 shunt into the free-running position and the jumper JP4 shunt into the AUTO position.
3. Apply 3.3V at VIN. Measure VOUT; it should read 0V. If desired, one can measure the shutdown supply current at this point. The supply current will be approximately 1uA in shutdown.
4. Turn on the circuit by inserting the shunt in jumper JP1 into the ON (upper) position. The output voltage should be regulating. Measure VOUT - it should measure 1.8V +/- 2%.
5. Vary the input voltage from 2.5V to 5.5V and the load current from 0 to 600mA. Vout should read between 1.8V +/- 4%.
6. Measure the output ripple voltage at any output current level; it usually will measure less than 20mVAC.
7. Observe the voltage waveform at the switch node (pin 5 of the IC). Verify the switching frequency is between 1.2MHz and 1.8MHz (T = 0.833us and 0.556us), and that the switch node waveform is rectangular in shape.
8. Still observing the switch node, from no load, increase the output current until switching operation is observed. This should occur at approx. 11mA. Also observe the switch node as the load is decreased to 0A. The LTC3448 should turn into a linear regulator at about 2.5mA (hysteresis).
9. Insert jumper JP1 into the OFF (lower) position, and insert shunts into the other voltage options, 1.2V or 1.5V, and the other frequency operation, 2.25MHz. Re-insert the jumper JP1 shunt into the ON (upper) position. The DC840 should perform the same as in the 1.8Vout evaluation.

The options of the mode feature – AUTO, ON, and OFF can also be evaluated now: On for linear operation only, and OFF for switching operation only.

When finished, turn off the circuit (connecting the RUN pin to ground) by inserting the shunt in jumper JP1 into the OFF (lower) position.

Warning - if the power for the demo board is carried in long leads, the input voltage at the part could “ring”. To eliminate this, insert a small tantalum capacitor (for instance, an AVX part # TAJW476M010R) on the pads between the input power and return terminals on the bottom of the demo board. The (greater) ESR of the tantalum will dampen the (possible) ringing voltage due to the use of long input leads. On a normal, typical PCB, with short traces, the capacitor is not needed.

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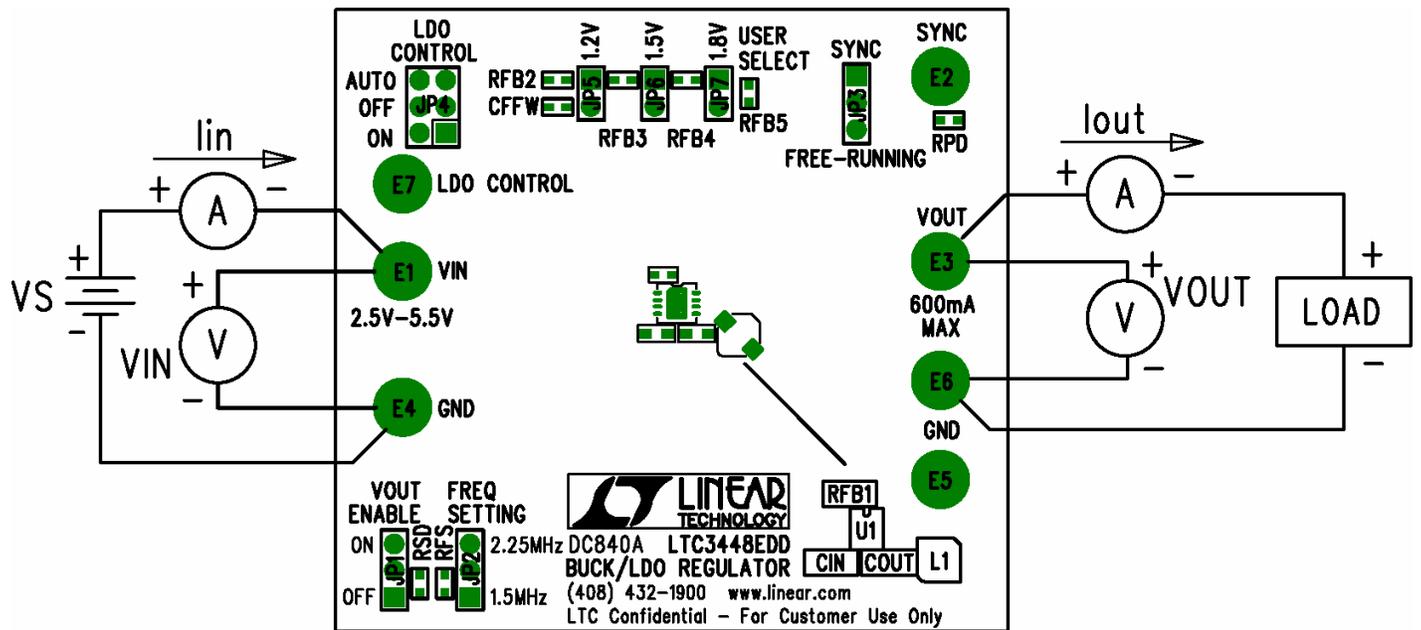


Figure 1. Proper Measurement Equipment Setup

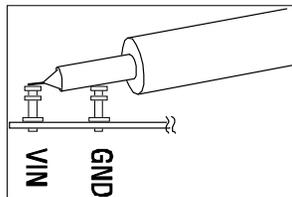
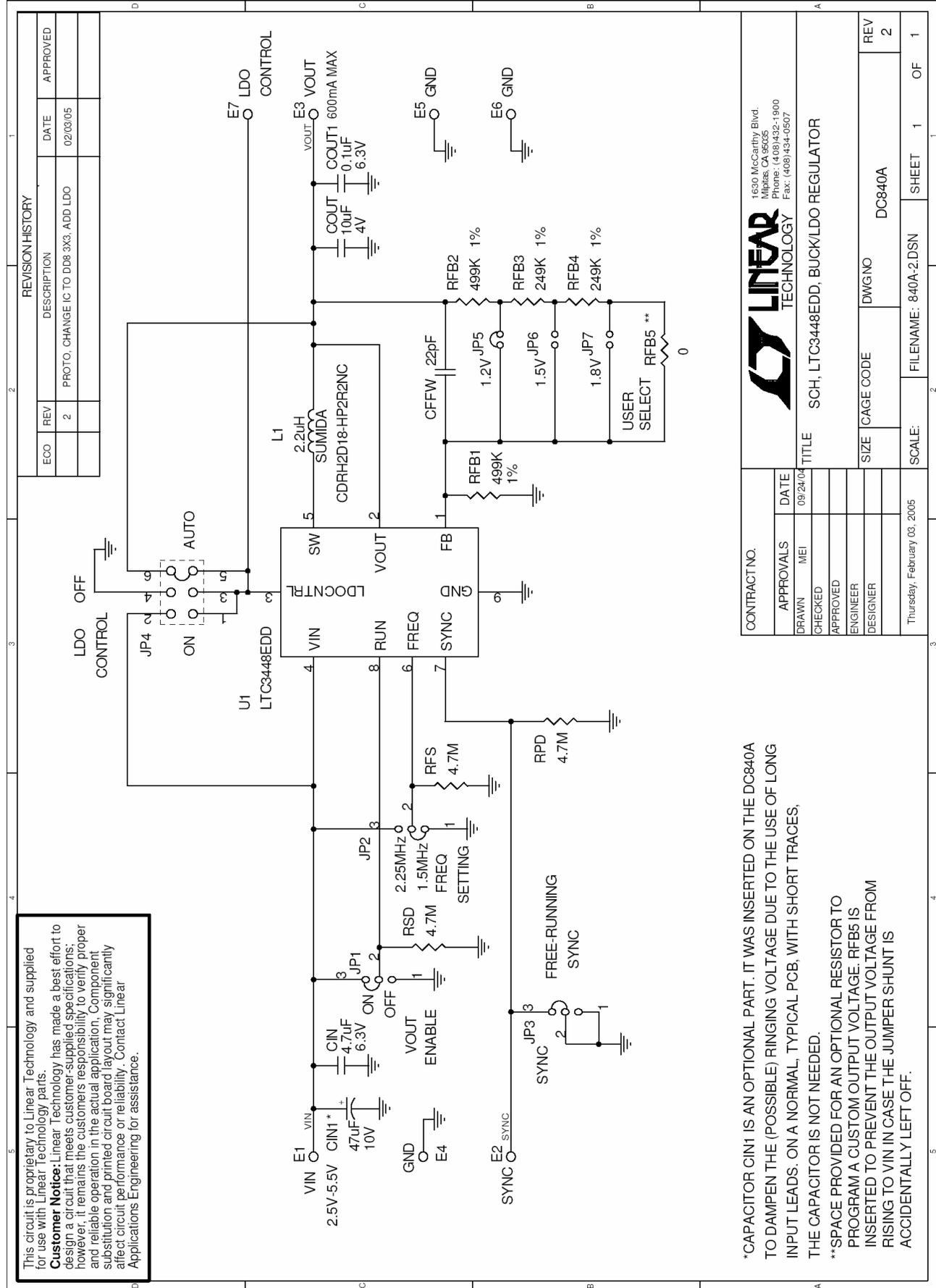


Figure 2. Measuring Input or Output Ripple

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This circuit is proprietary to Linear Technology and supplied for use with Linear Technology parts.
Customer Notice: Linear Technology has made a best effort to design a circuit that meets customer-supplied specifications; however, it remains the customer's responsibility to verify proper and reliable operation in the actual application. Component substitution and printed circuit board layout may significantly affect circuit performance or reliability. Contact Linear Applications Engineering for assistance.

*CAPACITOR CIN1 IS AN OPTIONAL PART. IT WAS INSERTED ON THE DC840A TO DAMPEN THE (POSSIBLE) RINGING VOLTAGE DUE TO THE USE OF LONG INPUT LEADS. ON A NORMAL, TYPICAL PCB, WITH SHORT TRACES, THE CAPACITOR IS NOT NEEDED.
 **SPACE PROVIDED FOR AN OPTIONAL RESISTOR TO PROGRAM A CUSTOM OUTPUT VOLTAGE. RFB5 IS INSERTED TO PREVENT THE OUTPUT VOLTAGE FROM RISING TO VIN IN CASE THE JUMPER SHUNT IS ACCIDENTALLY LEFT OFF.

REVISION HISTORY		
ECO	REV	DESCRIPTION
	2	PROTO. CHANGE IC TO DD8-3X3. ADD LDO
		DATE
		APPROVED

CONTRACT NO.		DATE	
APPROVALS	MEI	09/24/01	
DRAWN	CHECKED		
APPROVED	ENGINEER		
DESIGNER			
TITLE		SCH, LTC3448EDD, BUCK/LDO REGULATOR	
SIZE	ICAGE CODE	DWGNO	DC840A
REV			2
SCALE:		FILENAME: 840A-2.DSN	SHEET 1 OF 1



1630 McCarthy Blvd.
 Milpitas, CA 95025
 Phone: (408)432-1900
 Fax: (408)434-0507