DESCRIPTION

Demonstration circuit DC338B-A is for the evaluation of filter circuits using an LTC®1563-2 and DC338B-B for an LTC1563-3. LTC1563-2 and LTC1563-3 are dual 2nd order active RC filter building blocks with precision ±1.5% capacitors. The LTC1563-2 uses six equal value resistors to implement a 4th order Butterworth lowpass filter, and the LTC1563-3 uses six equal value resistors to implement a 4th order Bessel lowpass filter. The lowpass cutoff frequency (fC) range of an LTC1563-X filter is 256Hz to 256kHz.

For testing and evaluation, the DC338B-A is configured as a single 4th order, 25.6kHz Butterworth lowpass filter; and the DC338B-B is configured as a single 4th order, 25.6kHz Bessel lowpass filter.

For other LTC1563-X configurations, the DC338B has unused pads for 0805 surface mount resistors and capacitors preconfigured with PCB traces to allow for the following LTC1563-X filter circuits:
1. 4th order lowpass filter
2. 5th order lowpass filter
3. 4th order narrow bandpass filter
4. 4th order wide bandpass filter

Refer to the LT1563-X data sheet for additional information about filter circuit configurations.

Design files for this circuit board are available at http://www.linear.com/demo/DC338B

ELECTRICAL CHARACTERISTICS

The ● denotes specifications which apply over the full operating temperature range, otherwise specifications are TA = 25°C

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>CONDITIONS</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNITS</th>
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<tbody>
<tr>
<td>Total Supply Voltage</td>
<td></td>
<td>2.7</td>
<td>11</td>
<td></td>
<td>V</td>
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<tr>
<td>Supply Current</td>
<td>VS = 2.7V, LP MODE</td>
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<td>1</td>
<td>1.8</td>
<td>mA</td>
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<tr>
<td></td>
<td>VS = ±5V, HS MODE</td>
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<td>15</td>
<td>23</td>
<td>mA</td>
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<tr>
<td>Lowpass Cutoff Frequency Range</td>
<td>LP MODE</td>
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<td>25.6</td>
<td>kHz</td>
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<tr>
<td>Lowpass Cutoff Frequency Range</td>
<td>HS MODE</td>
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<tr>
<td>Bandpass Center Frequency Range</td>
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<td></td>
<td>kHz</td>
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<td>HS MODE</td>
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<td></td>
<td>kHz</td>
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<tr>
<td>Output Voltage High, LPA and LPB</td>
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<td></td>
<td>V+ – 50mV</td>
<td>V</td>
</tr>
<tr>
<td>Output Voltage Low, LPA and LPB</td>
<td>RL = 10k</td>
<td></td>
<td></td>
<td>V– + 50mV</td>
<td>V</td>
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<td>DC Voltage Offset</td>
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<td>6±</td>
<td></td>
<td>mV</td>
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<tr>
<td></td>
<td>VS = ±5V, HS MODE</td>
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<td>3±</td>
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<td>mV</td>
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<td>LTC1563-X OP AMP GBW</td>
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<td>MHz</td>
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<td>VS = ±5V, HS MODE</td>
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<td></td>
<td>MHz</td>
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Capacitor Values:

**LTC1563-2**, SIDE A C1A = 53.8pF, C2A = 64.2pF. SIDE B C1B = 39.1pF, C2B = 87.9pF.

**LTC1563-3**, SIDE A C1A = 34.9pF, C2A = 38.8pF. SIDE B C1B = 26.8pF, C2B = 40.3pF.

Typical Capacitor Specifications:

Side A to Side B capacitor mismatch ±1.5%, part to part capacitor variation ±2%. Note: There is a stray 5pF capacitor from SA and SB node to ground.
QUICK START PROCEDURE

See Figure 1 for proper measurement equipment setup and follow the procedure below.

1. Place jumpers in the following positions: JP4 to DUAL SUPPLY, JP3 to ACTIVE, JP5 to HIGH SPEED.
2. With power off, connect a dual 5V power supply to +V and –V.
3. Connect a 10kHz, 1Vp-p, sine wave generator to VIN and GND turrets.
4. Set the scaling of an oscilloscope to 1V/100µs per division.
5. Connect an oscilloscope probe from VOUT and GND to oscilloscope channel 1.
6. Power up the system and the oscilloscope should show a 10kHz 1Vp-p sine wave.
7. To test stopband attenuation, set the input frequency to 100kHz and the output voltage drops to less than 5mVp-p and 25mV for the DC338B-A and DC338B-B, respectively.

Figure 1. Quick Start Test Equipment Setup
QUICK START PROCEDURE

DC338B Default Configuration

For testing and evaluation, the DC338B-A is configured as a single 4th order, 25.6kHz Butterworth lowpass filter (the Figure 2 circuit) and the DC338B-B is configured as a single 4th order, 25.6kHz Bessel lowpass filter (the Figure 3 circuit).

Reconfiguring the DC338B

Removing the default passive components (RA1, RB1 RSA, R31, R21, RA2, RB2 RSB, R32 and R22), the DC338B can be configured for variety of lowpass or bandpass filter circuits. Figures 2 thru 6 highlight easy to design and simulate with LTspice® LTC1563 filter circuits.

Note: The LTC1563-X LTspice models only, the high speed mode (HP) with op amps at the maximum GBW (listed under Electrical Characteristics). The GBW limit must be considered when simulating LTspice circuits. For example, if an LTC1563 circuit operates in low power mode then the maximum lowpass cutoff frequency is 25.6kHz and an LTspice simulation showing a typical frequency response at cutoff frequencies greater than 25.6kHz is overly optimistic.

LTspice is a high performance simulator, schematic capture and waveform viewer available for free download at www.linear.com/LTspice.
QUICK START PROCEDURE

The LTspice file for this circuit is available at www.linear.com/demo/DC338B.

Figure 2. 4th Order Butterworth Lowpass Filter (DC338A-A Default Configuration)
QUICK START PROCEDURE

4TH ORDER BESSEL LOWPASS FILTER
MAXIMUM $f_c$ IS:
- 25 kHz FOR LOW POWER AND
- 256 kHz FOR HIGH SPEED

The LTspice file for this circuit is available at www.linear.com/demo/DC338B.
QUICK START PROCEDURE

The LTspice file for this circuit is available at www.linear.com/demo/DC338B.
Figure 5. 4th Order Narrow Bandpass Filter

The LTspice file for this circuit is available at www.linear.com/demo/DC338B.
QUICK START PROCEDURE

The LTspice file for this circuit is available at www.linear.com/demo/DC338B.
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