



## Product/Process Change Notice - PCN 26\_0027 Rev. -

Analog Devices, Inc. One Analog Way, Wilmington, MA 01887, USA

This notice is to inform you of a change that will be made to certain ADI products (see Appendix A) that you may have purchased in the last 2 years. An acceptance or concern response should be submitted to ADI promptly. Any requests for samples of changed material or additional information must be made within 30 days of the notification. In accordance with JEDEC Standard 046, customers should acknowledge receipt of the PCN within 30 days of the PCN delivery. ADI contact information is listed below. Note: Revised fields are indicated by a red field name. See Appendix B for revision history.

**Lack of acknowledgment of the PCN within 30 days constitutes acceptance of the change. After the acknowledgment, a lack of additional requests within 90 days constitutes acceptance of the change.**

<b>PCN Title:</b>	Addition of Analog Devices, Ireland as an Alternate Fab Site 0.35um Process
<b>Publication Date:</b>	05-May-2026
<b>Effectivity Date:</b>	07-Aug-2026 <i>(the earliest date that a customer could expect to receive changed material)</i>
<b>Revision Description:</b>	Initial Release.

### Description Of Change:

Qualification of Analog Devices International, Ireland (ADLK) as an alternate wafer fab site to TSMC Fab11 and TSMC Fab3 for 0.35um CMOS process.

### Reason For Change:

This change will ensure manufacturing agility and continuity of supply.

### Impact of the change (positive or negative) on fit, form, function & reliability:

There will be no change to fit, form, function or reliability.

### Product Identification: *(this section will describe how to identify the changed material)*

Traceability will be maintained via standard ADI lot traceability.

### Summary of Supporting Information:

Qualification has been performed per AEC-Q100, Stress Test Qualification for Integrated Circuits. See attached Qualification Results.

### Supporting Documents:

**Attachment 1: Type:** Delta Qualification Matrix

ADI\_PCN\_26\_0027\_Rev\_-\_C35 ADLK Fab\_Delta-Qualification-Matrix-ZVEI-5\_2\_1.xlsm

**Attachment 2: Type:** Qualification Results Summary

ADI\_PCN\_26\_0027\_Rev\_-\_Qualification\_Results\_0p35um\_CMOS\_Wafer\_Fab\_Site\_at\_ADI\_LimerickrevE.pdf

Note: If applicable, the device material declaration will be updated due to material change.

### ADI Contact Information:

For questions on this PCN, please send an email to the regional contacts below or contact your local ADI sales representatives.

**Americas:**

**Europe:**

**Japan:**

**Korea:**

**Rest of Asia:**

PCN\_Americas@analog.com PCN\_Europe@analog.com PCN\_Japan@analog.com PCN\_Korea@analog.com PCN\_ROA@analog.com

**Appendix A - Affected ADI Models:**

**Added Parts On This Revision - Product Family / Model Number (4)**

ADAU1962 / ADW80031BSTZ    ADAU1962 / ADW80031BSTZ-RL    ADAU1966 / ADAU1966WBSTZ    ADAU1966 / ADAU1966WBSTZRL

**Appendix B - Revision History:**

<b>Rev</b>	<b>Publish Date</b>	<b>Effectivity Date</b>	<b>Rev Description</b>
Rev. -	05-May-2026	07-Aug-2026	Initial Release.