



Product/Process Change Notice - PCN 25_0126 Rev. B

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This notice is to inform you of a change that will be made to certain ADI products (see Appendix A) that you may have purchased in the last 2 years. An acceptance or concern response should be submitted to ADI promptly. Any requests for samples of changed material or additional information must be made within 30 days of the notification. In accordance with JEDEC Standard 046, customers should acknowledge receipt of the PCN within 30 days of the PCN delivery. ADI contact information is listed below. Note: Revised fields are indicated by a red field name. See Appendix B for revision history.

Lack of acknowledgment of the PCN within 30 days constitutes acceptance of the change. After the acknowledgment, a lack of additional requests within 90 days constitutes acceptance of the change.

Note: Revised fields are indicated by a red field name. See Appendix B for revision history.

PCN Title:	AD4080 Data Sheet Revision
Publication Date:	19-Aug-2025
Effectivity Date:	07-Nov-2025 <i>(the earliest date that a customer could expect to receive changed material)</i>
Revision Description:	Correction to PCN: Showing the output leakage current for DIGITAL OUTPUTS (GPIOx).

Description Of Change:

From:

Gain Error Drift Typical = 0.095ppm/C

DIGITAL INPUTS (CNV, CS, SCLK, and SDI)

Input High Voltage, $V_{IH}(\min) = 0.73 \times V_{DD11}$ (V)

DIGITAL INPUTS (GPIOx, DCS, and DCLK)

Input High Voltage, $V_{IH}(\min) = 0.73 \times I_{OVDD}$ (V)

no information on the Digital Output leakage currents for:

DIGITAL OUTPUTS (GPIOx)

&

DIGITAL OUTPUTS (SDOx)

To:

Gain Error Drift Typical = 0.2ppm/C

DIGITAL INPUTS (CNV, CS, SCLK, and SDI)

Input High Voltage, $V_{IH}(\min) = 0.92$ (V)

DIGITAL INPUTS (GPIOx, DCS, and DCLK)

Input High Voltage, $V_{IH}(\min) = 0.92$ (V)

Showing the output leakage current for:

DIGITAL OUTPUTS (GPIOx)

Input Low Current (IIL) -1uA(min) to +1uA(max)

Input High Current (IIH) -1uA(min) to +1uA(max)

&

DIGITAL OUTPUTS (SDOx)

Input Low Current (IIL) -1uA(min) to +1uA(max)

Input High Current (IIH) -1uA(min) to +1uA(max)

Reason For Change:

To correct the data sheet for proper levels of driving the digital inputs and correct gain error drift in the specification table.

Impact of the change (positive or negative) on fit, form, function & reliability:

No change to fit, form, function or reliability. This is a data sheet change only.

Summary of Supporting Information:

Changes will be reflected in Product Data Sheet Rev. B. See attached Data Sheet Specification Comparison located in the Support Documents section of this PCN.

Supporting Documents:

Attachment 1: Type: Datasheet Specification Comparison

ADI_PCN_25_0126_Rev_B_AD4080 Data Sheet Comparison.pdf

Note: If applicable, the device material declaration will be updated due to material change.

ADI Contact Information:

For questions on this PCN, please send an email to the regional contacts below or contact your local ADI sales representatives.

Americas:	Europe:	Japan:	Korea:	Rest of Asia:
PCN_Americas@analog.com	PCN_Europe@analog.com	PCN_Japan@analog.com	PCN_Korea@analog.com	PCN_ROA@analog.com

Appendix A - Affected ADI Models:**Existing Parts - Product Family / Model Number (3)**

AD4080 / AD4080BBCZ

AD4080 / AD4080BBCZ-RL

AD4080 / AD4080BBCZ-RL7

Appendix B - Revision History:

Rev	Publish Date	Effectivity Date	Rev Description
Rev. -	28-Apr-2025	31-Jul-2025	Initial Release.
Rev. A	05-Aug-2025	07-Nov-2025	Corrections: 1)Add update to digital levels and gain error drift in Specification Table 1. 2)Improve descriptions in the Description of Change section. 3)Supporting Document, Data Sheet Comparison updated.
Rev. B	19-Aug-2025	07-Nov-2025	Correction to PCN: Showing the output leakage current for DIGITAL OUTPUTS (GPIOx).