



Reliability Report

Report Title: LTC3637 MSOP Die Rev Qualification

Report Number: 22014

Revision: A

Date: 8 January 2025

Summary

This report documents the successful completion of the reliability qualification requirements for the release of the LTC3637 product in a 16-MINI_SO_EP package. The LTC3637 is a high efficiency step-down DC/DC regulator with an internal high side power switch that draws only 12 μ A DC supply current while maintaining a regulated output voltage at no load.

AECQ100 Qualification Test Methods and Summary

AEC Test Group	AEC Stress Test Name	Abbreviation	AEC Test#	Reference
Group A ACCELERATED ENVIRONMENT STRESS TESTS	Preconditioning	PC	A1	Table 2, and Table 4
	Temperature Humidity Bias or Biased-HAST	THB or HAST	A2	
	Autoclave or Unbiased HAST or Temperature Humidity (without Bias)	AC, UHST, or TH	A3	
	Temperature Cycle	TC	A4	
	Power Temperature Cycling	PTC	A5	
	High Temperature Storage Life	HTSL	A6	
Group B ACCELERATED LIFETIME SIMULATION TESTS	High Temperature Operating Life	HTOL	B1	Table 2, and Table 4
	Early Life Failure Rate	ELFR	B2	
	NVM Endurance, Data Retention, and Operational Life	EDR	B3	
Group C PACKAGE ASSEMBLY INTEGRITY TESTS	Wire Bond Shear	WBS	C1	<ul style="list-style-type: none"> • Test C2 (and C1 for Cu Wire) are shown in Table 4. • Tests C3-6 are qualified and controlled with inline monitors and may be viewed on-site at Analog Devices.
	Wire Bond Pull Strength	WBP	C2	
	Solderability	SD	C3	
	Physical Dimensions	PD	C4	
	Solder Ball Shear	SBS	C5	
	Lead Integrity	LI	C6	
Group D DIE FABRICATION RELIABILITY TESTS	Electromigration	EM	D1	Die Fabrication Reliability data may be viewed on-site at Analog Devices.
	Time Dependent Dielectric Breakdown	TDDDB	D2	
	Hot Carrier Injection	HCI	D3	
	Negative Bias Temperature Instability	BTI	D4	
	Stress Migration	SM	D5	
Group E ELECTRICAL VERIFICATION TESTS	Pre- and Post-Stress Electrical Test	TEST	E1	<ul style="list-style-type: none"> • For Tests E5, E6 and E7, ADI New Product Yield Analysis Testing Guidelines meet AEC Q100 requirements. • Results for Tests E7-E11 are available as applicable on a case-by-case basis. • Test E12 results may be viewed on-site at Analog Devices
	Electrostatic Discharge Human Body Model	HBM	E2	
	Electrostatic Discharge Charged Device Model	CDM	E3	
	Latch-Up	LU	E4	
	Electrical Distributions	ED	E5	
	Fault Grading	FG	E6	
	Characterization	CHAR	E7	
	Electromagnetic Compatibility	EMC	E9	
	Short Circuit Characterization	SC	E10	
	Soft Error Rate	SER	E11	
	Lead (Pb) Free	LF	E12	
	Group F DEFECT SCREENING TESTS	Process Average Test	PAT	
Statistical Bin/Yield Analysis		SBA	F2	
Group G CAVITY PACKAGE INTEGRITY TESTS	Mechanical Shock	MS	G1	< Applicable only for Cavity-Packages >
	Variable Frequency Vibration	VFV	G2	
	Constant Acceleration	CA	G3	
	Gross/Fine Leak	GFL	G4	
	Package Drop	DROP	G5	
	Lid Torque	LT	G6	
	Die Shear	DS	G7	
	Internal Water Vapor	IWV	G8	

Die/Fab Product Characteristics

Table 1: Die/Fab Product Characteristics- 0.6µm BiCMOS at Vanguard

Product Characteristics	Product(s) to be qualified
Generic/Root Part #	LTC3637
Die Id	8VL3637XV VP8578B-C3LZT1NAZ1
Die Size (mm)	1.66x 2.55
Wafer Fabrication Site	E_VANG0108
Wafer Fabrication Process	0.6µm BiCMOS
Die Substrate	Si
Metallization / # Layers	AlCu(0.5%)/0
Passivation	undoped-oxide/SiN

Table 1.2: Die/Fab Product Characteristics- 0.6µm BiCMOS at Vanguard

Product Characteristics	Product(s) to be qualified	Product(s) used for Substitution Data					
Generic/Root Part #	LTC3637	LTC3112	LTC3260	LTC3859AL	LTC3786	LTC3787	LTC3630A
Die Id	8VL3637XV VP8578B- C3LZT1NAZ1	3112	3260	6HL3859ALXV	8VL3786XV	8VL3787XV	L3630AXV
Die Size (mm)	1.66x 2.55	1.71 x 4.35	2.39 x 1.65	1.56 x 2.65	1.46 x 1.48	1.61 x 1.97	1.66 x 2.55
Wafer Fabrication Site	Vanguard Fab1	Vanguard Fab1	Vanguard Fab1	Vanguard Fab1	Vanguard Fab1	Vanguard Fab1	Vanguard Fab1
Wafer Fabrication Process	0.6µm BiCMOS	0.6µm BiCMOS	0.6µm BiCMOS	0.6µm BiCMOS	0.6µm BiCMOS	0.6µm BiCMOS	0.6µm BiCMOS
Die Substrate	Si	Si	Si	Si	Si	Si	Si
Metallization	AlCu(0.5%)	AlCu	AlCu	AlCu	AlCu	AlCu	AlCu
Passivation	undoped- oxide/SiN	undoped- oxide/SiN	undoped- oxide/SiN	undoped- oxide/SiN	undoped- oxide/SiN	undoped- oxide/SiN	undoped- oxide/SiN

Die/Fab Test Results
Table 2.1: Die/Fab Test Results - 0.6µm BiCMOS at Vanguard

Test Name	AEC #	Spec	Conditions	Generic/Root Part #	Lot #	Fail/SS	eTest Temp	
Early Life Failure Rate (ELFR)	B2	AEC-Q100-008	Ta=150C, 48 Hours	LTC3112	EO9424.ELFR	0/800	RH	
					EO9425.ELFR	0/800	RH	
					EO9433.ELFR	0/800	RH	
High Temperature Operating Life (HTOL)	B1	JESD22-A108	Ta=150C, Biased, 1,000 Hours	LTC3112	EO9424.HTOL	0/77	RHC	
					EO9425.HTOL	0/77	RHC	
					EO9433.HTOL	0/77	RHC	
				LTC3630A	Z50156.HTOL	0/77	RHC	
					Z51555.HTOL	0/77	RHC	
				LTC3637	Q16825.1HTOL	0/77	RHC	
			LTC3859AL	Z51542.HTOL	0/77	RHC		
				Z51545.HTOL	0/77	RHC		
				Z51778.1a.HTOL	0/77	RHC		
				Ta=150C, Biased, 2,000 Hours	LTC3637	Z51607.HTOL	0/77	RHC
High Temperature Storage Life (HTSL)	A6	JESD22-A103	150°C, 2,000 Hours	LTC3112	EO9425F.HTS	0/45	RH	
					EO9433F.HTS	0/45	RH	
				LTC3260	Q16427.4	0/45	RH	
				LTC3630A	Z51555.HTS	0/45	RH	
				LTC3786	1077418.1.HTS	0/45	RH	
				LTC3787	Z52233.HTS	0/45	RH	
			LTC3859AL	Z51545.HTS	0/45	RH		
				150°C, P4000	LTC3637	Z51607.HTS	0/45	RH
			175°C, 1,000 Hours	LTC3112	EO9424F1.HTS	0/45	R	
					EO9425F1.HTS	0/45	RH	
					EO9433F1.HTS	0/45	R	
			Highly Accelerated Temperature and Humidity Stress Test (HAST) ¹	A2	JESD22-A110	130C 85%RH 33.3 psia, Biased, 96 Hours	LTC3787	Z52074.JHAST
Z52233.JHAST	0/77	RH						
Z52670.JHAST	0/77	RH						
LTC3859AL	Z51542.JHAST	0/77					RH	
	Z51545.JHAST	0/77					RH	
	Z51778.1a.JHAST	0/77					RH	
130C 85%RH 33.3 psia, Biased, P192	LTC3112	EO9549K.BHAST				0/77	RH	
		EO9550K.BHAST				0/77	RH	
		EO9551K.BHAST				0/77	RH	
Highly Accelerated Temperature and Humidity Stress Test (HAST) ¹	A2	JESD22-A110	130C 85%RH 33.3 psia, Biased, 96 Hours	LTC3786	Z52672.2aJHAST	0/77	RH	
					Z52705.1aJHAST	0/77	RH	
					Z52784.3aJHAST	0/77	RH	

¹These samples were subjected to preconditioning at MSL 1 with 3x reflow peak temp of 260°C prior to the start of the stress test.

Package/Assembly Product Characteristics
Table 3: Package/Assembly Product Characteristics - 16-MINI_SO_EP at ANALOG DEVICES (PNG)

Product Characteristics	Product(s) to be qualified	Product(s) used for Substitution Data				
Generic/Root Part #	LTC3637	LTC3621	LTC3639	LT8365	LT8361	LT8362
Package	16-MINI_SO_EP	8-MINI_SO_EP	16-MINI_SO_EP	16-MINI_SO_EP	16-MINI_SO_EP	16-MINI_SO_EP
Body Size (mm)	3.00 x 4.04 x 1.10	3.0 x 4.9 x 1.1	3.00 x 4.04 x 1.10	4.10 x 3.00 x 0.86	4.04 x 3.00 x 0.86	4.04 x 3.00 x 0.86
Assembly Location	ADI Penang	ADI Penang	ADI Penang	ADI Penang	ADI Penang	ADI Penang
MSL/Peak Reflow Temperature(°C)	1 / 260°C	1 / 260°C	1 / 260°C	1/260C	1 / 260°C	1/260C
Mold Compound	Hitachi CEL-8240HF10L	Hitachi CEL 8240HF10L	Hitachi CEL 8240HF10L	Hitachi CEL 8240HF10L	Hitachi CEL 8240HF10L	Hitachi CEL 8240HF10L
Die Attach	Ablestik 8290A conductive	Ablestik 8290A conductive	Ablestik 8290	Ablestik 8290	Ablestik 8290	Ablestik 8290
Leadframe Material	Copper	Copper	Copper	Copper	Copper	Copper
Lead Finish	100Sn	100Sn	100Sn	100Sn	100Sn	100Sn
Wire Bond Material/Diameter (mils)	Tanaka GOLD GPG2 /1.00	Tanaka GOLD GPG-2 /1.00	Gold/ 1.00	Gold Tanaka GMG / 1.00	Gold Tanaka GMG / 1.00	Gold Tanaka GMG / 1.00

Package/Assembly Test Results

Table 4: Package/Assembly Test Results - MINI_SO_EP at ADPG

Test Name	AEC #	Spec	Conditions	Generic/Root Part #	Lot #	Fail/SS	eTest Temp
High Temperature Storage Life (HTSL)	A6	JESD22-A103	150°C, 2,000 Hours	LT8361	EO9316F.HTS	0/45	RH
				LT8362	Z48692.HTS	0/45	RH
					Z50011.HTS	0/45	RH
			LTC3639	Z52464.HTS	0/45	RH	
						150°C, P4000	LTC3637
Highly Accelerated Temperature and Humidity Stress Test (HAST) ¹	A2	JESD22-A110	130C 85%RH 33.3 psia, Biased, 96 Hours	LT8361	EO9316K.RCM	0/77	RH
				LTC3637	Z51607.JHAST	0/77	RH
				LTC3639	Z52442.JHAST	0/77	RH
					Z52464.JHAST	0/77	RH
					Z52502.JHAST	0/77	RH
				LT8365	EO9458K.BHAST	0/77	RH
					EO9534K.BHAST	0/77	RH
					EO9535K.BHAST	0/77	RH
Unbiased HAST (UHST) ¹	A3	JESD22-A118	130C 85%RH 33.3 psia, 96 Hours	LTC3639	Z52442.JUHAST	0/77	R
					Z52464.JUHAST	0/77	R
					Z52502.JUHAST	0/77	R
				LTC3621	Q21215.1.UH1	0/77	R
					Q21215.2.UH2	0/77	R
					Q21215.3.UH3	0/77	R
Solder Heat Resistance (SHR)	A1	J-STD-020	MSL-1	LTC3637	Q22014.1.SHdrev	0/30	R
Temperature Cycling (TC) ¹	A4	JESD22-A104	-65°C/+150°C, 2,000 Cycles	LT8361	EO9316B.TC	0/77	RH
				LT8362	Z48692.JTC	0/77	RH
				LTC3639	Z52442.JTC	0/77	RH
					Z52464.JTC	0/77	RH
					Z52502.JTC	0/77	RH
				LT8365	EO9534B.TC	0/77	RH
					EO9458B.TC	0/77	RH
			EO9535B.TC		0/77	RH	
			-65°C/+150°C, P4000	LTC3637	Z51607.JTC	0/77	RH

Temperature Cycling (TC) ¹	A4	JESD22-A104	-65°C/+150°C, 1,000 Cycles	LTC3621	Q21215.1.TC1	0/77	RH
					Q21215.2.TC2	0/77	RH
					Q21215.3.TC3	0/77	RH

¹ These samples were subjected to preconditioning at MSL 1 with 3x reflow peak temp of 260°C prior to the start of the stress test.

ESD and Latch-Up Test Results

Table 5: ESD Test Result

ESD Model	Generic/Root Part #	Package	ESD Test Spec	RC Network	Highest Pass Level	Class	eTest Temp
FICDM	LTC3637	16-MINI_SO_EP	AEC Q100-011	1Ω, Cpkg	±2000V	C3	RH
HBM	LTC3637	16-MINI_SO_EP	AEC-Q100-002	1.5kΩ, 100pF	±4000V	2A	RH

Table 6: Latch Up Test Result

LU Test Spec	Generic/Root Part #	Passing Current	Passing Over-Voltage	Temperature (T _A)	Class	eTest Temp
JESD78	LTC3637	+100mA, -100mA	+100V	150°C	II	RH

Approvals

Reliability Engineer: Princess Doctolero