

Test Product Qualification Report

Title:

AD1030 Correlation Report (Addition of STA Singapore as an Alternate Test Site for AD1030 Device)

Revision:

A

Date:

November 27, 2024

Project Background

All volumes are currently running only in JC3. As volume ramp is forecasted for this product, STA has been qualified as another test source.

Summary

The AD1030 is an analog front end (AFE) that includes a high precision analog-to-digital converter (ADC), voltage output digital-to-analog converters (VDACs), current output digital-to-analog converters (IDACs), thermoelectric cooler (TEC) controller, and analog comparators. This part is used in optical networking–100G/200G/400G or higher speed optical modules.

This device will be tested in STA using the MFLXMS, the same as JC3 platform.

This report documents, the successful completion of the product test transfer requirements for the release of testing AD1030 in STA.

TEST AND PRODUCT INFORMATION

Device:	AD1030
Package:	64-WLCSP-N/A
Leads:	64
FG Part names:	AD1030BCBZ-RL7
Tester:	MFLXMS
Handler:	P-8

Test Result

Table 1. The correlation between two sites passed based on the criteria. The Result of the correlation run on STA was correlated with the data in JC3, Using the same program, all parameters were considered acceptable, because the distribution is comparable to the sending site.

Generic	Package	Lot Size	Sending Site	Receiving Site	Mean Shift =< 5%	Sigma Ratio =<1.3
AD1030	64-WLCSP-N/A	100	JC3	STA	Passed	Passed

Table 2. The setup was verified using two sets of different hardwares in STA, which resulted with comparable good yield during testing. All parameters were analyzed and passed on both test program and datasheet.

Generic	Package	Lot Size	Receiving Site	Yield Comparable to JC3
AD1030	64-WLCSP-N/A	100	STA	Passed