

# TEST PRODUCT QUALIFICATION REPORT

**TITLE:**

ADSP-2156x 120pin LQFP\_EP

Test Second Source UTAC (Singapore) Qualification

**PCN NUMBER:**

23\_0112

**REVISION:**

A

**DATE:**

12 March 2024

## PROJECT BACKGROUND

Test correlation is carried out to qualify UTAC Singapore as an additional final test site for ADI devices to support production.

## SUMMARY

All references to ADSP-2156x in this report, apply to all 120pin LQFP\_EP ADSP-21562/3/5 products.

ADSP-2156x is released at UTAC as 2nd source test solution.

There is no change to the form, fit, function, quality, or reliability between platforms.

This report documents the successful completion of the product test correlation requirements of ADSP-2156x 120pin LQFP\_EP between STATS ChipPAC and UTAC sites.

Test product qualification was performed according to Analog Devices Specification

## TEST AND PRODUCT INFORMATION

Device	ADSP-2156x
Package	120pin LQFP_EP, 14x14x1.4mm, 0.4mm pitch
Tester Platform	HP93K_6

## Description and Test Results

Table 1 provides a description of the qualification tests conducted and corresponding test results for ADSP-2156x. All the units have undergone electrical tests on both the sending and receiving sites on the same test platform. Any device that did not meet the electrical qualification requirements without further analysis and data to prove passing, the qualification would be considered failed.

Table 1. Test Product Transfer Qualification Criteria

Generic	Package	Lot Size	Sending Site	Receiving Site	Mean Shift =< 5%	Sigma Ratio =< 1.3
ADSP-2156x	120pin LQFP_EP, 14x14x1.4mm, 0.4mm lead pitch	100	SCS	UTAC	Passed	Passed

The ADSP-2156x was qualified by running a qualification lot with 100 units both in STATS ChipPAC and UTAC. Data between sites were analyzed as summarized in Table 1.

A passing result was recorded when the yield from receiving site met or exceeded yield from sending site as summarized in Table 2. Succeeding lots with increased quantity will be closely monitored once the device

has started production at UTAC.

Table 2. Test Product Transfer Qualification Lot Run

GENERIC	Package	Lot Size	Test Site	Results
ADSP-2156x	120pin LQFP_EP, 14x14x1.4mm, 0.4mm lead pitch	100	UTAC	Passed

No valid rejects were encountered during the said evaluation in both sending and receiving sites.

### Rejects Verifications

5 valid rejects tested in STATS ChipPAC and UTAC having the same result.

Table 3. Setup verification using Reject units.

Unit #	SCS	UTAC
1	Failed	Failed
2	Failed	Failed
3	Failed	Failed
4	Failed	Failed
5	Failed	Failed

## ADSP-2156x Initial Process Study (Test Temperature = 125 degC)

Key Datasheet Parameter	Datasheet Specs				Site (STATS-ChipPac Singapore)			Site (UTAC Singapore)				
	Min	Nom	Max	Units	Mean	Stdv	Cpk	Mean	Stdv	Cpk	GR&R (%)	NDC
SIDD			0.88	A	0.489	0.077	>1.67	0.518	0.071	>1.67	2.84676	50
IIL			10	uA	-0.495	0.114259	>10	-0.445	0.115556	>10	0.147	1918
IIH			10	uA	0.201	0.080185	>10	0.01	0.055185	>10	0.251	1126
IOZL			10	uA	-0.436	0.110926	>10	-0.393	0.097407	>10	0.0872	3245
IOZH			10	uA	0.18	0.048704	>10	0.164	0.042593	>10	0.105	2702
VIL	-0.3		0.8	V	1.15	0.003704	>10	1.14	0.003704	>10	9.096	11
VIH	2		3.6	V	1.295	0.007407	>10	1.295	0.009259	>10	2.909	61
VOL			0.4	V	0.138	0.002906	>10	0.136	0.003121	>10	0.6212	228
VOH	2.4			V	2.99	0.003484	>10	2.99	0.001634	>10	0.1277	494