

Package Material Set Change for AD9988 and AD9082

**Qualification Results Summary of the AD9988 a
324-BGA_ED packaged (AEK) RF Transceiver**

QUALIFICATION PLAN / STATUS			
TEST	SPECIFICATION	SAMPLE SIZE	RESULTS
High Temperature Operating Life (HTOL)*	JEDEC <i>JESD22-A108</i>	3x32	Pass
Temperature Cycle (TC)*	JEDEC <i>JESD22-A104</i>	3x32	Pass
Temperature/Humidity/Bias (THB)*	JEDEC <i>JESD22-A101</i>	3x32	Pass
Data Retention Bake (DR)	JEDEC <i>JESD22-A117</i>	3x32	Pass
Solder Heat Resistance (SHR)*	JEDEC/IPC <i>J-STD-020</i>	3x11	Pass
Electrostatic Discharge <i>Human Body Model</i>	ESDA/JEDEC <i>JS-001</i>	3/voltage	Pass ±1000 V
Electrostatic Discharge <i>Field-Induced Charged Device Model</i>	JEDEC <i>JS-002</i>	3/voltage	Pass ±200 V

*Preconditioned per JEDEC/IPC J-STD-020 Level 3