

LT3960 QUALIFICATION SUMMARY				
TEST	SPECIFICATION	CONDITIONS	SAMPLE SIZE (LOTS X SAMPLE)	STATUS
Temperature Cycle (TC)*	JEDEC <i>JESD22-A104</i>	-65°C/+150°C, 1000 Cycles	1 x 77	Pass
High Temperature Storage (HTSL)	JEDEC <i>JESD22-A117</i>	150°C, 2000 Hours	1 x 45	Pass
Highly Accelerated Stress Test (HAST)*	<i>JEDEC JESD22-A110</i>	130°C 85%RH, 33.3 psia, 96 Hours	1 x 77	Pass
Solder Heat Resistance (SHR)*	JEDEC/IPC J-STD-020	-	1 x 231	Pass
Latch-Up	JEDEC <i>JESD78</i>	-	1 x 5	Pass
Electrostatic Discharge <i>Field-Induced Charged Device Model</i>	ESDA/JEDEC <i>JS-002</i>	-	6/voltage	Pass
Electrostatic Discharge <i>Human Body Model</i>	ESDA/JEDEC <i>JS-001</i>	-	6/voltage	Pass

*Preconditioned per JEDEC/IPC J-STD-020