

Qualification Results Summary of AD9161/AD9162/AD9163/AD9164 Die Revision

QUALIFICATION PLAN/STATUS			
TEST	SPECIFICATION	SAMPLE SIZE	EXPECTED COMPLETION DATE
Solder Heat Resistance (SHR)*	JEDEC/IPC <i>J-STD-020</i>	1*30	Pass
Latch-Up	JEDEC <i>JESD78</i>	1*3	Pass⁺
Electrostatic Discharge <i>Human Body Model</i>	ESDA/JEDEC <i>JS-001</i>	3/voltage	Pass⁺
Electrostatic Discharge <i>Field-Induced Charged Device Model</i>	JEDEC <i>JS-002</i>	3/voltage	Pass⁺

*Preconditioned per JEDEC/IPC J-STD-020

⁺See reliability report for passing values