



Product/Process Change Notice - PCN 19_0118 Rev. -

Analog Devices, Inc. Three Technology Way Norwood, Massachusetts 02062-9106

This notice is to inform you of a change that will be made to certain ADI products (see Appendix A) that you may have purchased in the last 2 years. **Any inquiries or requests with this PCN (additional data or samples) must be sent to ADI within 30 days of publication date.** ADI contact information is listed below.

PCN Title: Notification of Change to LTC2974

Publication Date: 22-May-2019

Effectivity Date: 24-Aug-2019 *(the earliest date that a customer could expect to receive changed material)*

Revision Description:

Initial Release.

Description Of Change:

Please be advised that Analog Devices, Inc., has made improvements to the LTC2974 product die to add error correction code (ECC) to the on-chip EEPROM and fix some minor errata.

ECC was added to the non-volatile memory to enhance its reliability. This change is transparent to the user and requires no modifications to programming files or system firmware.

Minor errata and enhancements were also addressed in the new silicon:

- The servo DAC voltage buffer layout was modified to improve offset and improve yield.
- The package mold compound was converted to a low alpha version of the same material. This continual improvement effort reduces the chance of soft errors occurring in the RAM.
- Added read-only command MFR_INFO and the Command Plus protocol
- Removed the Mfr_config_all_fast_fault_log bit from the MFR_CONFIG_ALL_LTC2974 command
- Modified the via layers in the metal pad stack to support other potential package options
- Fixed some minor errata most customers will not encounter. Contact factory for more detail.

Reason For Change:

ECC was added to the non-volatile memory to enhance its reliability. The package mold compound was converted to a low alpha version of the same material. This continual improvement effort reduces the chance of soft errors occurring in the RAM.

Impact of the change (positive or negative) on fit, form, function & reliability:

This change is transparent to the user and requires no modifications to programming files or system firmware. Product specifications are unaffected.

Product Identification *(this section will describe how to identify the changed material)*

The new silicon can be identified with the MFR_SPECIAL_ID PMBus register value of 0x0215.

Summary of Supporting Information:

Qualification has been performed per industry standard test methods. See attached qualification test results.

Supporting Documents

Attachment 1: Type: Qualification Results Summary

ADI_PCN_19_0118_Rev_-_Qualification Results Summary - G770SHC Low Alpha MC at UTAC.pdf

Attachment 2: Type: Datasheet Specification Comparison

ADI_PCN_19_0118_Rev_-_2974fd_redline.pdf

For questions on this PCN, please send an email to the regional contacts below or contact your local ADI sales representatives.

Americas:
PCN_Americas@analog.com

Europe:
PCN_Europe@analog.com

Japan:
PCN_Japan@analog.com

Rest of Asia:
PCN_ROA@analog.com

Appendix A - Affected ADI Models

Added Parts On This Revision - Product Family / Model Number (4)

LTC2974 / LTC2974CUP#PBF

LTC2974 / LTC2974CUP#TRPBF

LTC2974 / LTC2974IUP#PBF

LTC2974 / LTC2974IUP#TRPBF

Appendix B - Revision History

Rev	Publish Date	Effectivity Date	Rev Description
Rev. -	22-May-2019	24-Aug-2019	Initial Release.

Analog Devices, Inc.

DocId:6733 Parent DocId:None Layout Rev:7