

## Die Revision for AD9171/AD9172/AD9173

### Qualification Results Summary of AD9171/AD9172/AD9173 Die Revision

QUALIFICATION PLAN / STATUS			
TEST	SPECIFICATION	SAMPLE SIZE	RESULTS
Solder Heat Resistance (SHR)*	JEDEC/IPC <i>J-STD-020</i>	<b>1*30</b>	<b>Pass</b>
Latch-Up	JEDEC <i>JESD78</i>	<b>1*9</b>	<b>Pass</b>
Electrostatic Discharge <i>Human Body Model</i>	ESDA/JEDEC <i>JS-001</i>	<b>3/voltage</b>	<b>Pass ±500V</b>
Electrostatic Discharge <i>Field-Induced Charged Device Model</i>	JEDEC <i>JS-002</i>	<b>3/voltage</b>	<b>Pass ±2000V</b>

\*Preconditioned per JEDEC/IPC J-STD-020, MSL 3, Max Peak Reflow Temp of 260C