

14引脚SOIC封装的四通道高速运算放大器通用评估板

特性

- 可快速实现试验板试验/原型操作
- 用户自定义电路配置
- 支持边缘安装型SMA连接器
- 可轻松连接至测试设备和其它电路
- 符合RoHS标准

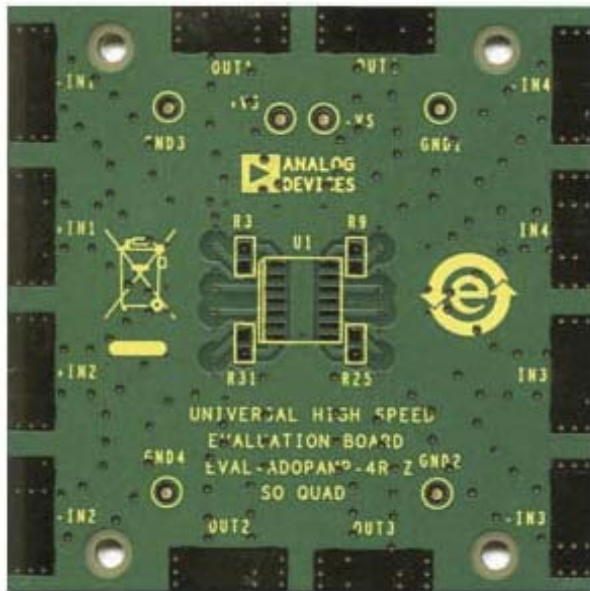
概述

ADI公司的EB-O14R-4Z通用评估板旨在帮助用户评估14引脚SOIC封装的四通道高速运算放大器。EB-O14R-4Z是裸板（即评估板上未焊接任何器件），用户可以快速完成各种四通道运算放大器电路的原型设计，从而最大程度地降低风险，加快产品上市。图1显示的是裸评估板的器件侧，而图2显示的是裸评估板的电路侧。

该2层评估板的输入端和输出端支持SMA边缘安装型连接器，可高效连接至测试设备或其它电路。接地层、器件放置和电源旁路经过专门设计，可以将寄生电感和电容降至最低。除旁路电解电容（C1和C2）为3528尺寸外，评估板器件主要采用SMT 0805外形尺寸。

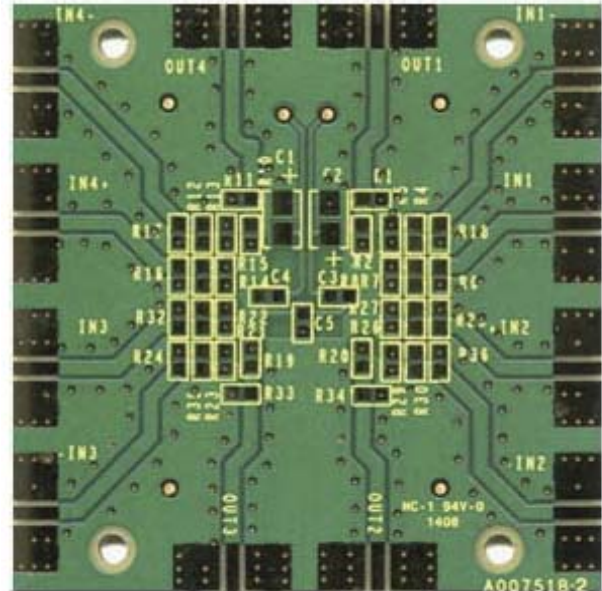
图3显示的是评估板原理图。图4和图5显示的是PCB装配图。图6和图7显示的是PCB布局图。

评估板的器件侧和电路侧图片



NOTES
1. THE EVALUATION BOARD SILKSCREEN PART NUMBER LABELLING ON YOUR BOARD MAY BE DIFFERENT FROM WHAT IS SHOWN HERE.

图1. EB-O14R-4Z评估板器件侧



NOTES
1. THE EVALUATION BOARD SILKSCREEN PART NUMBER LABELLING ON YOUR BOARD MAY BE DIFFERENT FROM WHAT IS SHOWN HERE.

图2. EB-O14R-4Z评估板电路侧

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修订历史

2010年3月—修订版0：初始版

评估板原理图、装配图和布局图

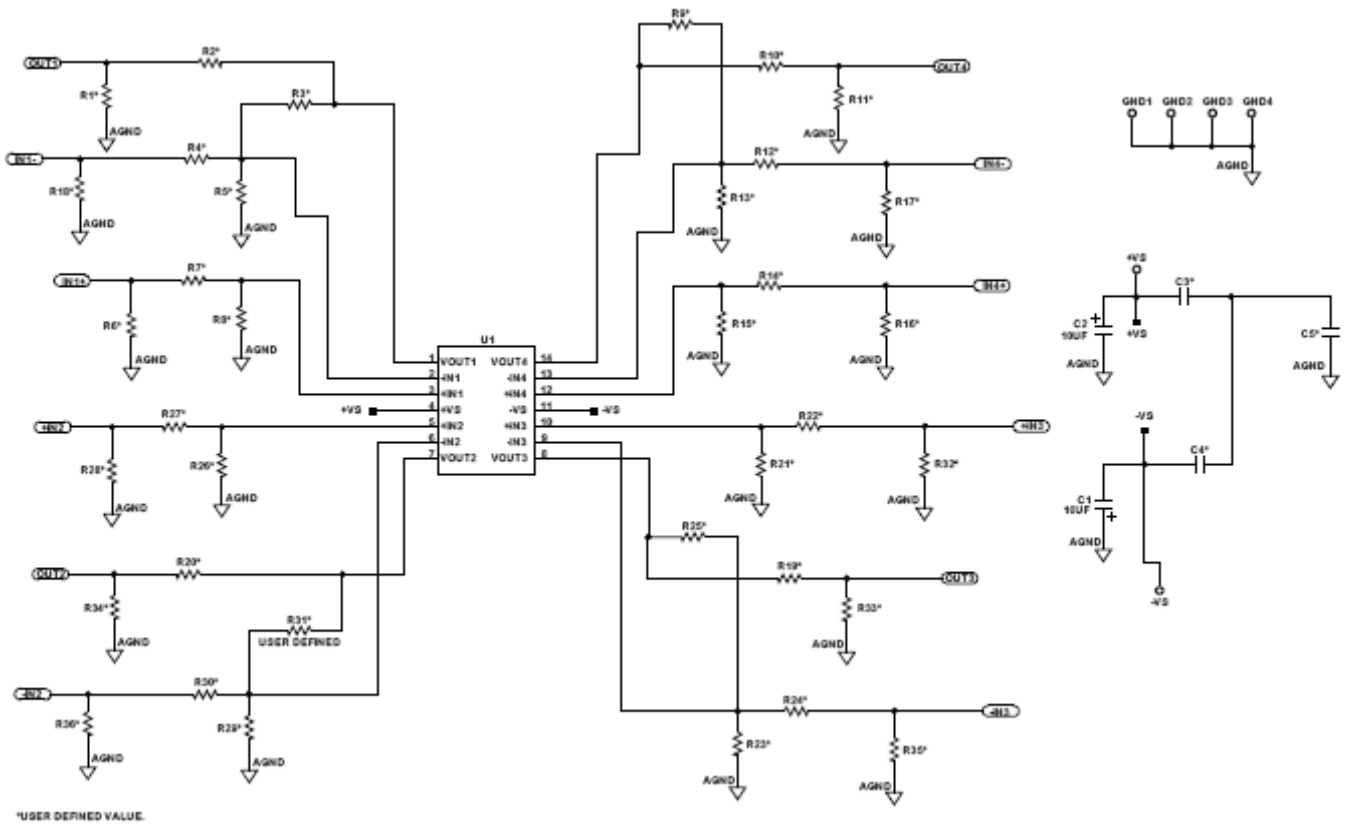


图3. EB-O14R-4Z通用评估板原理图

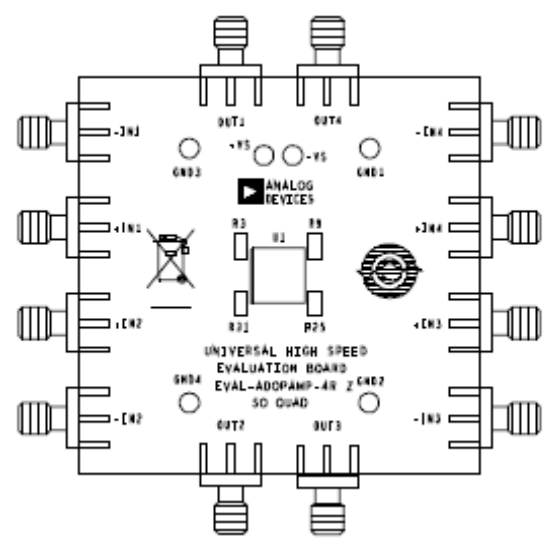


图4. 器件侧装配图

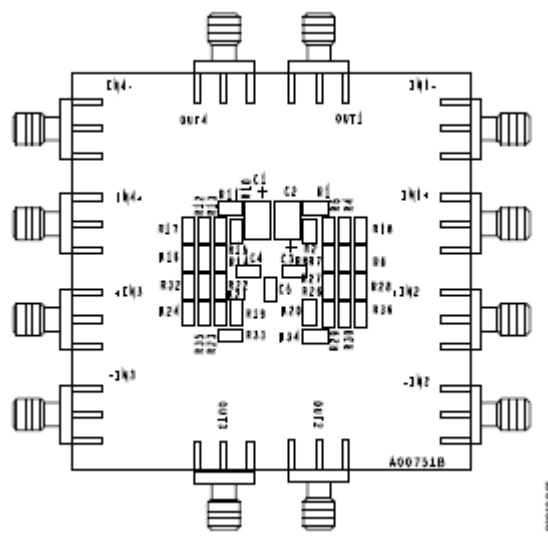


图5. 电路侧装配图

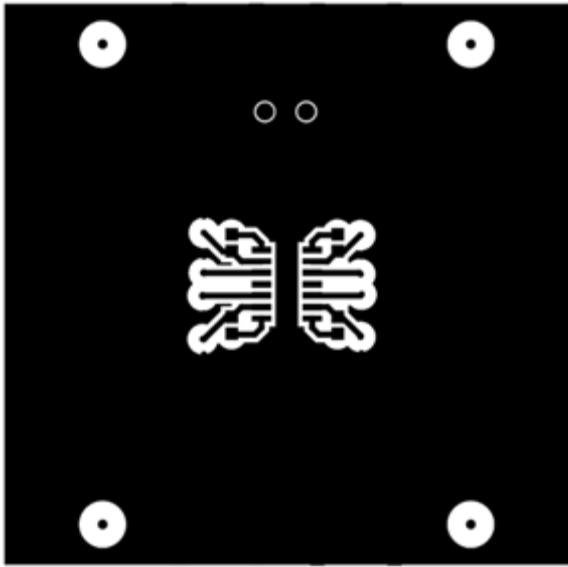


图6. 器件侧布局图

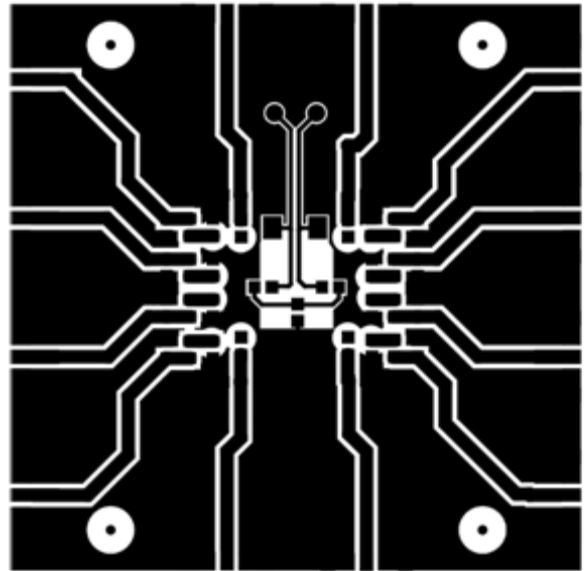


图7. 电路侧布局图

订购信息

物料清单

表1.

| 数量 | 索引标识符 | 描述 | 封装 |
|----|---|---------------|----------|
| 12 | +IN2, +IN3, -IN2, -IN3, IN1+, IN1-, IN4+, IN4-, OUT1, OUT2, OUT3, OUT4 | SMA/SMT | SMA/SMT |
| 6 | +VS, -VS, GND1, GND2, GND3, GND4 | 测试点 | TP |
| 2 | C1, C2 | 10 μ F 电容 | 3528 |
| 3 | C3, C4, C5 | 用户自定义电容 | C0805 |
| 36 | R1, R2, R3, R4, R5, R6, R7, R8, R9, R10, R11, R12, R13, R14, R15, R16, R17, R18, R19, R20, R21, R22, R23, R24, R25, R26, R27, R28, R29, R30, R31, R32, R33, R34, R35, R36 | 用户自定义电阻 | R0805 |
| 1 | U1 | 放大器 | 14引脚SOIC |

注释

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**ESD 警告**

ESD (静电放电) 敏感器件。带电器件和电路板可能会在没有察觉的情况下放电。尽管本产品具有专利或专有保护电路，但在遇到高能量 ESD 时，器件可能会损坏。因此，应当采取适当的 ESD 防范措施，以避免器件性能下降或功能丧失。

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