双通道高速运算放大器评估板
（16引脚、4 mm × 4 mm LFCSP封装，带专用反馈引脚）

特性
可快速实现试验板试验/原型操作
用户自定义电路配置
支持边缘安装型SMA连接器
可轻松连接至测试设备和其它电路
两个独立电路可增强灵活性

概述
EB-O16CP44-2Z旨在帮助用户评估双通道高速运算放大器。
EB-O16CP44-2Z是裸板（即评估板上未焊接任何器件），用户可以快速完成各种运算放大器电路的原型设计，从而最大程度地降低风险，加快产品上市。EB-O16CP44-2Z评估板支持ADI公司所有采用16引脚、4 mm × 4 mm引脚架构芯片级封装(LFCSP)并带专用反馈引脚的双通道高速运算放大器。

图1和图2分别显示的是评估板的器件侧和电路侧。图3显示的是评估板原理图。

该4层评估板的输入端和输出端支持边缘安装型SMA连接器，可高效快速连接至测试设备或其它电路。

评估板接地层、器件放置和电源旁路经过优化设计，可以最大程度地提高电路的灵活性和性能。评估板支持多种SMT元件尺寸：0402、0508、0603和0734。

图4和图6显示的是评估板装配图。图5和图7显示的是将评估板连接到运算放大器和支持电路的金属布局图。

评估板的器件侧和电路侧图片

图1. EB-O16CP44-2Z PCB的器件侧
图2. EB-O16CP44-2Z PCB的电路侧
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装配图和布局图

图4. 评估板器件侧装配图
图6. 评估板电路侧装配图

图5. 评估板器件侧布局图
图7. 评估板电路侧布局图
## 订购信息

### 物料清单

**表 1**

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<thead>
<tr>
<th>数量</th>
<th>索引标识符</th>
<th>描述</th>
<th>封装</th>
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<td>SMA/SMT</td>
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<td>C1, C2, C3, C4, C15, C16</td>
<td>0.1 μF电容</td>
<td>C0508</td>
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<tr>
<td>2</td>
<td>C17, C18</td>
<td>用户自定义电容</td>
<td>C0603</td>
</tr>
<tr>
<td>2</td>
<td>C19, C20</td>
<td>用户自定义电容</td>
<td>C0402</td>
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<tr>
<td>4</td>
<td>C11, C12, C13, C14</td>
<td>10 μF电容</td>
<td>C6032</td>
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<td>R1, R2, R3, R4, R5, R6, R7, R8, R9, R10, R11, R12, R13, R14</td>
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<td>R0402</td>
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<tr>
<td>4</td>
<td>R17, R18, R19, R20</td>
<td>0 Ω，两个电源短接在一起</td>
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<td>R15, R16, R21, R22</td>
<td>1 kΩ</td>
<td>R0603</td>
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<td>9</td>
<td>PD1/DIS1, PD2/DIS2, GND1 to GND3, +VS1, −VS1, +VS2, −VS2</td>
<td>测试点</td>
<td>TP</td>
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<td>P1</td>
<td>3位接头</td>
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<td>2</td>
<td>W1, W2</td>
<td>3引脚直插接头</td>
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<td>焊接跳线</td>
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<td>放大器</td>
<td>SOIC</td>
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注释
注释

ESD警告

ESD（静电放电）敏感器件。带电器件和电路板可能会在没有察觉的情况下放电。尽管本产品具有专利或专有保护电路，但在遇到高能量ESD时，器件可能会损坏。因此，应当采取适当的ESD防范措施，以避免器件性能下降或功能丧失。

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