

COMPANY: Analog Devices Inc.

TITLE: EVAL-ADuC7060 Schematic

DRAWN: Mike Looney	DATED: ---	CODE: ---	SIZE: ---	REV: ---
CHECKED: ?????	DATED: ---	RELEASED: ---	DRAWING NO: EVAL-ADuC7060QS	REV: 0
QUALITY CONTROL: ---	DATED: ---	SHEET: 1 OF 1		
RELEASED: ---	DATED: ---	SCALE: ---		

6

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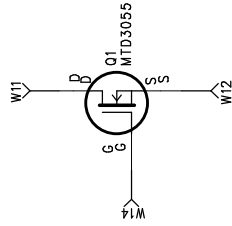
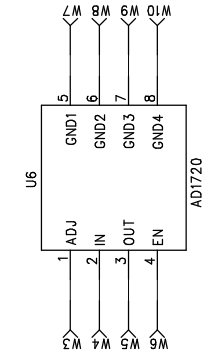
4

3

2

1

Include footprint for MOSFET, 6 x Res, 2 x CAP



REVISION RECORD		
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COMPANY:			
TITLE:			
DRAWN:	DATED:	CODE:	DRAWING NO:
CHECKED:	DATED:	SIZE:	REV:
QUALITY CONTROL:	DATED:	SCALE:	SHEET: OF
RELEASED:	DATED:		

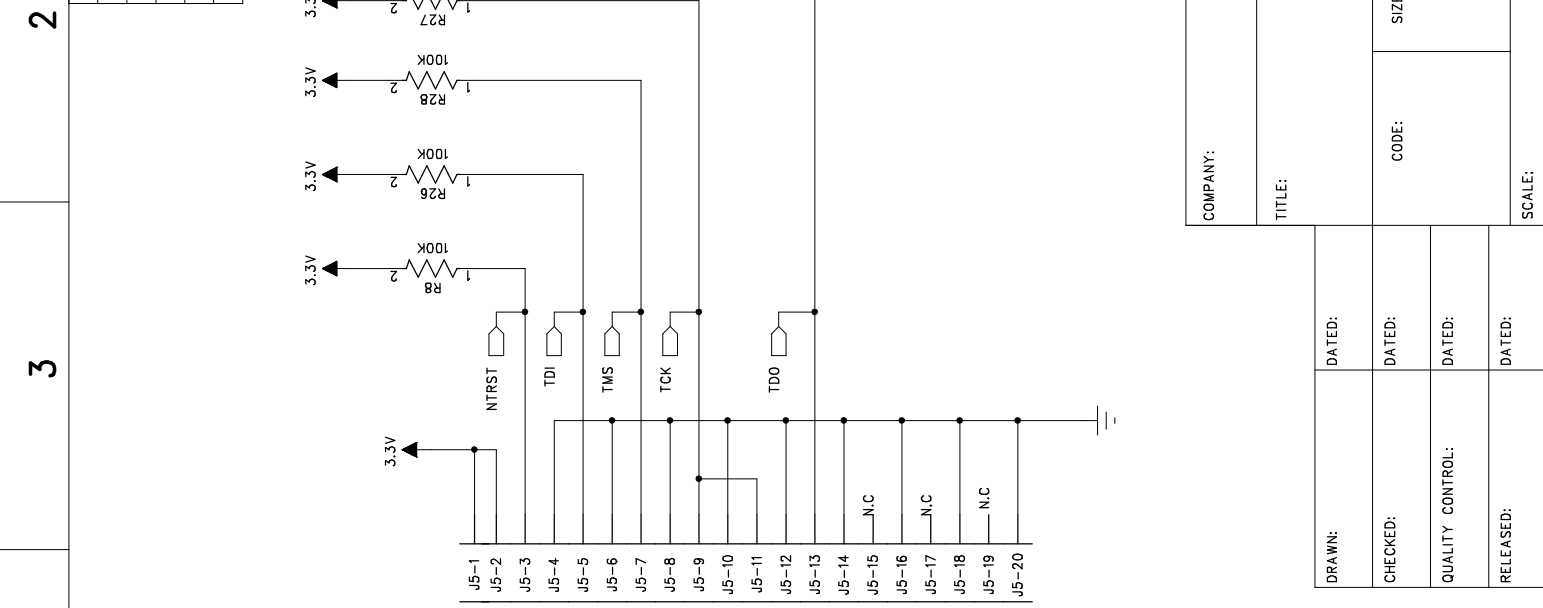
D

C

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LTR		REVISION RECORD	
ECO NO:	APPROVED:	DATE:	



COMPANY:		DRAWN:		DATED:	
TITLE:		CHECKED:		DATED:	
DRAWING NO:		QUALITY CONTROL:		DATED:	
REV:		RELEASED:		DATED:	
SIZE:	CODE:	SCALE:	SHEET:	OF	