

6 LAYER STACKUP

	PRIMARY SILKSCREEN
	PRIMARY SOLDER MASK
	PRIMARY SIDE
NOMINAL	GND1 PLANE (L2)
FINISHED	PWR1 PLANE (L3)
BOARD	PWR2 PLANE (L4)
THICKNESS	GND2 PLANE (L5)
0.062"	SECONDARY SIDE (L6)
+/- .007	SECONDARY SOLDER MASK
	SECONDARY SILKSCREEN

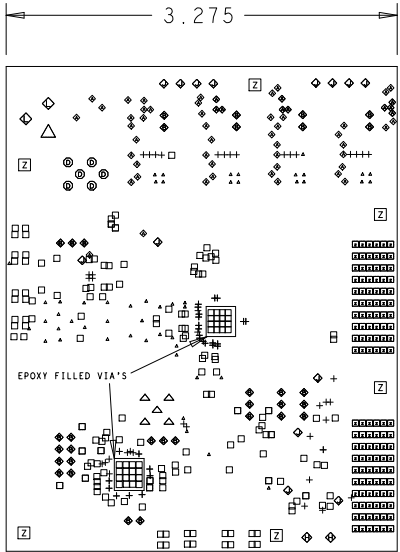
CHARACTERISTIC IMPEDANCE = 50 OHMS +/- 10%
ARTWORK LINE WIDTH FOR
IMPEDANCE CONTROLLED LINES = 8.5 MILS
CHARACTERISTIC IMPEDANCE = 100 OHMS DIFF +/- 10%
ARTWORK LINE WIDTH FOR
IMPEDANCE CONTROLLED LINES = 5 MIL TRACE/5 MIL SPACE

NOTES: MATERIALS; MANUFACTURE TO MEET EU RoHS DIRECTIVE, ALL MATERIAL,
(INCLUDING SOLDERMASK) REFER TO EU RoHS DIRECTIVE 20002/95/EC.
LAMINATE & RESIN MATERIALS; Tg>170C. Td>310c
ACCEPTABLE MATERIALS; NELCO N4000-11, NELCO N40000-29, ISOLA IS410,
POLYCLAD 370 HR, FR4-08.
CLADDING; EXTERNAL LAYERS 1/2 OZ. COPPER. OVERPLATE TO 1 1/2 OZ.
INTERNAL SIGNAL LAYERS 1 OZ. COPPER.
INTERNAL PLANE LAYERS 1 OZ. COPPER.
SOLDER MASK; SHALL BE LIQUID PHOTOIMAGABLE (LPI) APPLIED ON BOTH SIDES OVER
BARE COPPER AND SHALL MEET IPC-SM-840 (LATEST REV.) CLASS 3.
SILK SCREEN; SHALL BE PERMANENT NON-CONDUCTIVE EPOXY INK, COLOR WHITE.
U.L. RATING; 94VO MINIMUM.
FINISH; SILVER IMMERSION.

FABRICATION:

- REFER TO IPC-4101B/99 SERIES (LATEST REV.), CLASS 2 FOR FABRICATION UNLESS OTHERWISE SPECIFIED.
- UNDIMENSIONED HOLES TO BE LOCATED WITHIN +/- .005 OF THEIR TRUE POSITION WITH RESPECT TO ARTWORK.
- PLATED HOLE WALL THICKNESS SHALL NOT BE LESS THAN .001 INCH MINIMUM AVERAGE, WITH NO READING LESS THAN .0008 BY CROSS SECTION.
- HOLE DIAMETERS APPLY AFTER PLATING.
- FINISHED CONDUCTOR WIDTHS SHALL NOT BE REDUCED FROM THE NOMINAL, INDICATED ON THE MASTER PATTERN, BY MORE THAN THE CONDUCTOR THICKNESS.
- MINIMUM DESIGN LINE WIDTH IS .008 INCH.
- MINIMUM DESIGN SPACING IS .008 INCH.
- BOARD/PANEL MUST MEET IPC-A-600 (LATEST REV.) CLASS 2 FOR FLATNESS.
- MFGR. TO LEGIBLY ETCH OR STAMP/SCREEN WITH PERMANENT NON-CONDUCTIVE INK ON SECONDARY SIDE IN A CLEAR AREA UNLESS OTHERWISE INDICATED;
A. U.L. CODE D. MFGR. LOGO - DESIGNATED AREA,
B. DATE CODE (STAMP) E. SUCCESSFUL ELECTRICAL BOARD TEST.
C. FLAMMABILITY RATING F. LOT NUMBER - DESIGNATED AREA.
- NON-FUNCTIONAL PADS MAY BE REMOVED FROM INNER SIGNAL LAYERS AT MFGR. DISCRETION.
- IF PAD SIZES PROVIDED ARE NOT LARGE ENOUGH TO MAINTAIN ANNULAR RING REQUIREMENT, MFGR. MAY TEAR DROP PADS TO MAINTAIN ANNULAR RING AT PAD TO CIRCUIT INTERFACE ONLY AND MUST INSURE ELECTRICAL INTEGRITY.
- REPAIRS PER IPC-R-700 ARE ALLOWED.
- MODIFICATIONS TO THE ARTWORK, OTHER THAN THOSE DESCRIBED ON THE FABRICATION DRAWING, ARE NOT ALLOWED WITHOUT WRITTEN AUTHORIZATION.
- CORE BETWEEN LAYERS 2 AND 3, AND LAYERS 4 AND 5 SHOULD BE NO MORE THAN 4 MILS IN THICKNESS TO HAVE OPTIMUM INNER PLANE CAPACITANCE.
- SILKSCREEN CROSS HATCHING UNDER LFCSP PACKAGE (U4) IS REQUIRED AND NOT TO BE REMOVED DURING PROCESSING.
- VIA'S UNDER U2 & U4 ARE TO BE FILLED WITH NON-CONDUCTIVE EPOXY. THAN PLANERIZED FOR FLATNESS BEFORE FINISH.

FIDUCIALS; TOP & BOTTOM ARE MIRRORED COPIES.
(0,200), (0,3800) & (3000,3800)



PRIMARY SIDE

DIMENSIONS ARE FINISHED HOLES SIZE				
ALL UNITS ARE IN MILS				
FIGURE	SIZE	PLATED	QTY	TOLERANCE/NOTES
+	10.0	PLATED	68	
□	12.0	PLATED	180	
•	16.0	PLATED	51	
◆	20.0	PLATED	58	
■	28.0	PLATED	120	
◆	40.0	PLATED	33	
⊙	45.0	PLATED	6	
◇	50.0	PLATED	14	
◇	65.0	PLATED	2	
△	140.0	PLATED	1	
⊞	156.0	NON-PLATED	6	

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES DECIMALS FRACTIONS ANGLES .XX +/- .010 +/- 1/32 +/- 2 .XXX +/- .005	APPROVAL		DATE	TITLE			
	DRAWN BY		D.M.L	10/07/07	AD9434 ENG. EVAL.PCB		
	DESIGNED						
	CHECKED						
MATERIAL	APPROVED			SIZE	FSCM NO	DRAWING NUMBER	REV
	MFG ENGINEER			C		9434EE01C	C
DO NOT SCALE DWG				SCALE	1/1		SHEET 1 OF 1



ANALOG
DEVICES

HSC
DIVISION
7910 TRIAD CENTER DRIVE
GREENSBORO, NC 27409