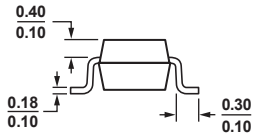
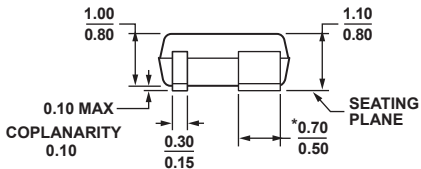
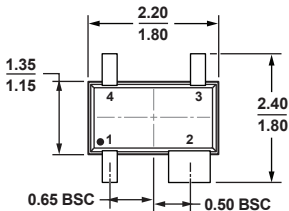


**4-Lead Thin Shrink Small Outline Transistor Package [SC70]  
(KS-4)**

Dimensions shown in millimeters



\*PACKAGE OUTLINE CORRESPONDS IN FULL TO EIAJ SC82  
EXCEPT FOR WIDTH OF PIN 2 AS SHOWN.