

µModule Regulator Charges Supercapacitor Backup Supply, Supporting LDO Outputs When the Input Supply Fails

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The LTM8001 is a µModule regulator that combines a 5A switching regulator with an array of five 1.1A low noise LDOs. The switching regulator can be set for constant current, suitable to charge supercapacitors for power backup. The LTM8001 operates from 6V to 36V inputs. The switching regulator is capable of constant output voltage or constant output current regulation at switching frequencies from 200kHz to 1MHz. The output of the switching regulator can be adjusted from 1.2V to 24V and the outputs of the LDOs are adjustable from 0V to 24V.

The switching regulator is set to regulate output current at 5.6A (typical) to provide a current limit that is above the maximum output current of 5A. The regulated current level can be easily lowered. The inputs for three of the LDOs are hardwired to the output of the switching regulator, but the input to the remaining bank of two LDOs is undedicated, so it can be connected to the switching regulator or elsewhere. The bias inputs to the LDOs are undedicated but are separated into two inputs: one for the bank of three connected to the switching regulator and the other for the remaining bank of two LDOs. The outputs of the LDOs can be operated separately or paralleled for higher output currents.

2-OUTPUT REGULATOR WITH SUPPLY RIDE-THROUGH SUPERCAPACITOR

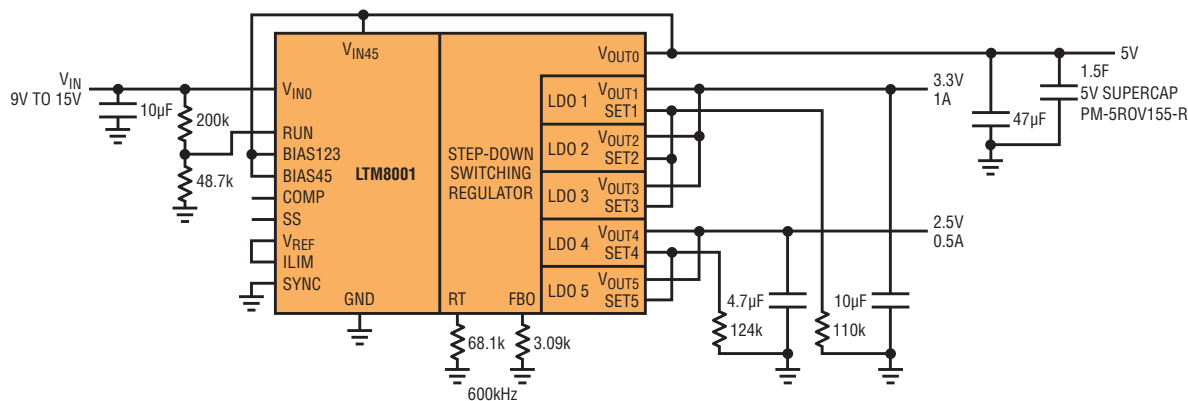
Figure 1 shows the LTM8001 in a dual output application: 3.3V at 1A and 2.5V at 0.5A. This setup also charges a supercapacitor and draws on the supercap to support the outputs in the face of input supply failures.

The switching frequency is 600kHz and the output voltage of the switching regulator is 5V when the supercapacitor is fully charged. The input voltage is from 9V to 15V and the LTM8001 charges the supercapacitor at 5.6A, typical. The resistor divider on the RUN pin programs the circuit to turn on for a 9V or higher input, but also ensures that the switching

regulator remains off when back-fed by the supercapacitor when there is an interruption to the input power.

Figure 2 shows LDO V_{BIAS} -to-output dropout voltage vs output current. According to Figure 2, the bias of the higher voltage, 3.3V/1A LDO output must be 1.5V higher than 3.3V, or 4.8V for proper regulation. This means that the LDO outputs remain in regulation during the time the supercapacitor voltage decays 100mV from 4.9V to 4.8V. The 0.07Ω ESR of the PM-5R0V155-R supercapacitor reduces the available voltage from the supercapacitor from 5V to 4.9V while the supercapacitor provides 1.5A to the LDOs. If the supercapacitor is 1.5F and the total

Figure 1. The LTM8001 producing 3.3V at 1A and 2.5V, 0.5A regulated outputs while charging a supercapacitor for backup power.



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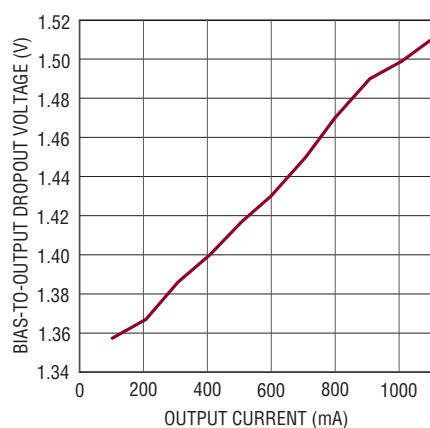


Figure 2. LDO V_{BIAS} -to-output dropout voltage vs output current

output current of the LDOs is 1.5A, the holdup time for the 3.3V LDO output is:

$$\begin{aligned} 3.3V \text{ HOLDUP TIME} &= \frac{C}{I} \Delta V \\ &= \frac{1.5}{1.5} 0.1 \\ &= 100ms \end{aligned}$$

Both the LDO bias and LDO input power are connected to 5V from the supercapacitor. Although 5V is non-optimal with

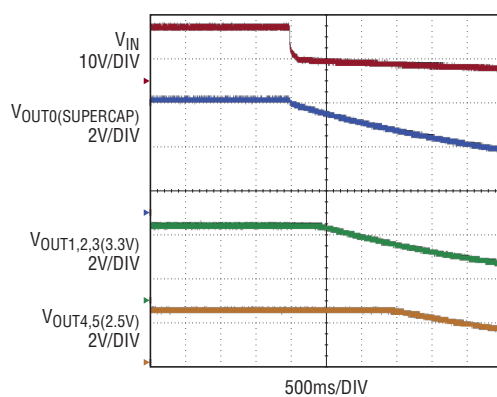


Figure 3. Supercapacitor power backup system holds up the 3.3V output for well over 100ms

regard to power dissipation, it maximizes holdup time if the input supply fails.

Power loss is minimized by operating the LDO with inputs that just meet, and do not exceed, the bias dropout requirements of the 3.3V LDO. But the supercapacitor voltage must exceed the input power dropout requirement to meet bias dropout and holdup requirements. To mitigate this increased power dissipation,

the LTM8001 parallels LDOs to distribute heat and lower operating temperatures.

Holdup time is longer when the supercapacitor provides bias to the LDOs compared to using a conventional capacitor for that purpose. This avoids detrimental effects of charging a large capacitor directly with the input voltage. Figure 3 shows that the 3.3V output holdup time exceeds 100ms when the supercapacitor is charged to 5V and the LDO outputs are 3.3V at 1A and 2.5V at 0.5A.

CONCLUSION

The LTM8001 makes it easy to design a multiple output voltage regulator circuit featuring supercapacitor backup power. It is possible to achieve significant holdup time without adding large and undesirable capacitance directly to input power.

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is low. External gate network C1 and D2 limits these inrush currents by controlling Q1's gate voltage slew rate. Since V_{IN} follows Q1's gate voltage, the external gate network forces V_{IN} to ramp modestly compared to the abrupt input voltage transient present on V_{SUPPLY} , as shown in Figure 6.

CONCLUSION

The high voltage standoff capability of the series connected MOSFET blocks dangerous spikes from reaching the

LT3504. During normal operation, the LT3504's built-in boost regulator permits 100% switch duty cycle operation and serves as an excellent MOSFET gate driver. The LT3504, along with a MOSFET and gate clamp, provides a transient-robust, compact multioutput solution.

Visit www.linear.com/LT3504 for data sheets, demo boards and other applications information. ■

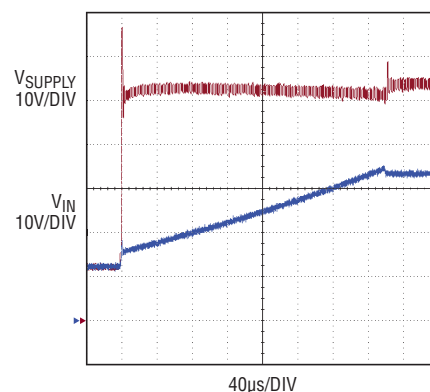


Figure 6. Fast V_{SUPPLY} dV/dt is blocked from V_{IN} by series MOSFET and gate network