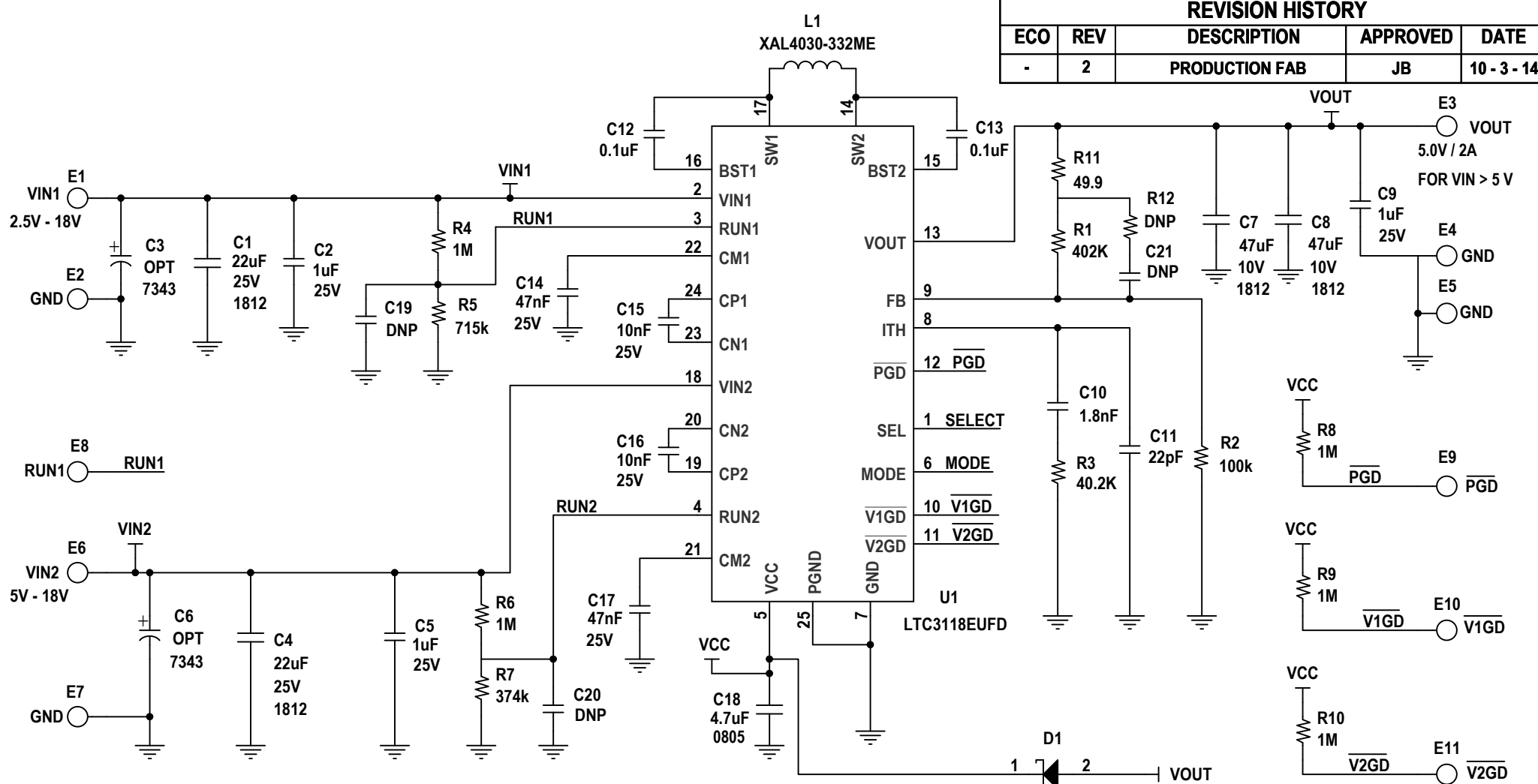


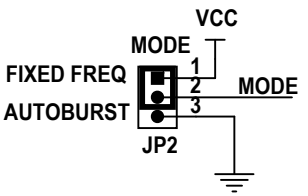
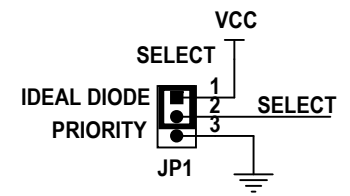
REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
-	2	PRODUCTION FAB	JB	10 - 3 - 14



NOTE: UNLESS OTHERWISE SPECIFIED

1. ALL RESISTORS ARE IN OHMS, 0402
ALL CAPACITORS ARE IN MICROFARADS, 0603.

REMOVE D1 IF VOUT > 5.5 VOLTS



CUSTOMER NOTICE
 LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

APPROVALS	
PCB DES.	NC
APP ENG.	JB
SCALE = NONE	

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 Fax: (408)434-0507
 LTC Confidential-For Customer Use Only

TITLE: SCHEMATIC
18V, 2A Buck-Boost DC/DC Converter with Low-Loss Dual Input PowerPath

SIZE	IC NO.	REV.
N/A	LTC3118EUFD DEMO CIRCUIT 2045A	2

DATE: 10 - 3 - 14 SHEET 1 OF 1