FEATURES
Very low power, high performance, low IF transceiver
Fully integrated io-homecontrol compliant protocol covering
  Layer 1, Layer 2, and time critical elements of Layer 3
Media access
  Master, slave, and beacon modes supported
Automatic io-homecontrol channel scan
Automatic CRC, preamble, start byte insertion/check
UART data encoding as per io-homecontrol
Smart preamble detect/packet sniffing
Automatic address filtering
Low power modes
Autonomous packet handling without intervention of host
  microprocessor thus significantly increasing battery life
1-way and 2-way communication supported
Automatic wake-up timer
32-bit hardware timer, 16-bit firmware timer (48 bits total)
Uses either
  External 32 kHz crystal
  Internal 32 kHz RC oscillator
Patented fast settling automatic frequency control (AFC)
Fully integrated image rejection calibration (patent pending)
Digital RSSI
Operating frequencies
  Channel 1: 868.25 MHz
  Channel 2: 868.95 MHz
  Channel 3: 869.85 MHz

FUNCTIONAL BLOCK DIAGRAM

For more information on the ADF7022, contact a local sales office at Analog Devices, Inc.

Rev. SpA
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