INTRODUCTION

Traditional combustion engine vehicles emit engine sound, even at low travel speeds. Typically, pedestrians and other traffic participants recognize an approaching or departing vehicle through sight and auditory identification of tire sounds and other emitted noise when the vehicle is out of sight.

All electric vehicles (EV) do not emit engine sound. Hybrid electric vehicles (HEV) or plug-in hybrid electric vehicles (PHEV) move almost silently when travelling at low speeds and before the conventional internal combustion engine (ICE) kicks in. These vehicles are difficult to hear when travelling at speeds less than 19 mph. At greater speeds, tire sound becomes dominant.

Global governing bodies are exploring legislation that seeks to establish a minimum level of sound for PEVs and HEVs when operating in electric mode so that visually impaired people, pedestrians, and cyclists can hear these vehicles approach and determine from which direction these vehicles are approaching.

An example of this legislation can be found on the National Highway Traffic Safety Administration (NHTSA) website.

An electric vehicle warning sound system (EVWSS) produces a series of sounds designed to alert pedestrians to the presence of an EV, HEV, and PHEV. The driver can initiate warning sounds (similar to the sound from a car horn, but less urgent); however, the sounds must automatically be enabled at low speeds. These sounds vary from artificial tones to realistic sounds that mimic engine noise and tires moving over gravel.

Analog Devices, Inc., offers two different solutions for advanced applications with an in-cabin engine sound for an EV as well as an external engine sound. Analog Devices developed a solution based on the ADSP-BF706. For entry-level systems, Analog Devices developed a solution based on the ADAU1450 SigmaDSP®. These solutions can synthesize sound and adjust frequency, sound volume, and other parameters depending on the travelling speed, and these solutions can send the audio to an audio power amplifier. Depending on the requirements of certain legislation, the warning sound can be simulated using combustion engine sounds or any other synthesized tones.
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# REVISION HISTORY

5/2018—Revision 0: Initial Version
The ADSP-BF706 Blackfin+ Processor® provides a single-chip solution for audio processing and interfacing to the control area network (CAN) bus. Analog Devices developed a CAN software stack that runs on the ADSP-BF706, which enables users to build automotive grade demonstrations with minimal effort (a vector CAN stack can also be used). Additionally, Analog Devices provides full hardware and software reference design, and SigmaStudio™ compatibility for live tuning of parameters.

Figure 1 shows the different processing blocks inside the ADSP-BF706. External waveform audio files (WAVs) store signature engine sounds or audio tones. Up to 25 WAV files can be accessed simultaneously from the external serial peripheral interface (SPI). These files are frequency shifted and mixed internally in the digital signal processor (DSP) before adding the dynamic volume control.

The ADSP-BF706 utilizes a memory mapped SPI interface that provides faster, simplified access to the external memory, which eliminates the need for an external double data rate (DDR) memory for this application. Up to 25 WAV files can be accessed simultaneously from the SPI flash memory. The large number of accessible WAV files helps to create more realistic engine sounds.

The ADSP-BF706 can also implement up to 16x pitch shifting, a recommendation from the United States (US) NHTSA, which increases the frequency of the output sound as the vehicle speed increases. The ADSP-BF706 can dynamically control the volume as the vehicle speed from the CAN bus increases.

Figure 2 shows a detailed system block diagram. A Power By Linear™ LT8602 quad monolithic synchronous, step-down regulator provides all voltage rails required in the system, derived from the 12 V car battery supply. The 2 MHz switching frequency allows users to avoid critical, noise sensitive frequency bands, for example, the AM band. The 3 V to 42 V input voltage range of the LT8602 makes the device ideal for automotive applications, which must regulate through cold crank and start stop scenarios with minimum input voltages as low as 3 V and load dump transients more than 40 V.

Figure 3 shows an alternative system block diagram with a reduced set of peripherals, connectors, and one automotive qualified connector carrying all relevant signals. This configuration allows the design of a smaller form factor board.

This system solution results in a reduced system bill of materials (BOM) cost because the ADSP-BF706 acts as a microcontroller and an audio processor.

For full details, see the EVWSS v1 Demo Manual and the EVWSS v2 Demo Manual, which are provided in the software download package. This software package (EVWSS-BF_SRC-Rel2.0.0) is available by request from the Software Request Form page on the Analog Devices website. For full details on the ADSP-BF706, see the ADSP-BF70x Blackfin+™ Processor Hardware Reference and the ADSP-BF7xx Blackfin+™ Processor Programming Reference.
Figure 2. Detailed System Block Diagram with Blackfin+ Processor, Full Featured Board
Figure 3. Detailed System Block Diagram with Blackfin+ Processor, Form Factor Board with Reduced Components
**EVWSS SOFTWARE ARCHITECTURE FOR THE ADSP-BF706 BLACKFIN+ PROCESSORS**

The EVWSS software architecture is based on the ADSP-BF706 hardware architecture. The processor dependence on the hardware architecture is due to the memory mapped SPI. The ADSP-BF706 can read directly from the flash memory using memory mapped SPI. This feature reduces the complexity of the EVWSS library and makes the memory access efficient for warning sound generation.

**SOFTWARE COMPONENTS**

The EVWSS software architecture consists of the components shown in Figure 4.

![Figure 4. EVWSS Software Architecture Block Diagram](image)

This section details the software components. The SPORT callback feature maps to the audio data sample rate and runs in the SPORT transceiver interrupt service routine (ISR) context, reading flash files (SPI memory mapped), performing audio manipulation using the EVWSS library, and sending out modified audio on the SPORT transceiver interface. The EVWSS library holds the different functions to synthesize the warning sound. The EVWSS library also receives vehicle speed input from the CAN stack (or the universal asynchronous receiver and transmitter (UART) interface for debugging). The TDA7803 driver controls the external power amplifier to generate the warning sound. The EVWSS application framework configures the system peripherals, CAN stack, and the TDA7803 driver.

**EVWSS LIBRARY FUNCTIONS**

The following sections describe the functions of the EVWSS library. For full details, see the Electronic Vehicle Warning Sound System Release Notes, which can be found in the software download package.

**Pitch Control**

Pitch shifting is the concept of shifting the spectrum of the audio signal based on a control input. In the EVWSS application, the base pitch of the WAV file is shifted depending on the vehicle speed input.

**Frequency Modulation and Amplitude Modulation**

The engine sound is dependent on the engine strokes, which include intake, compression, power (expansion), and exhaust. These strokes create frequency modulated tones instead of just pure tones. Vary the pitch shift parameter across the samples to achieve frequency modulation.

Two kinds of modulation (saw tooth and triangular) are included in this application. In saw tooth modulation, the frequency ramps from lowest to highest and then back to lowest with a jump. In triangular modulation, the frequency ramps from lowest to highest and then ramps back down to lowest.

**Audio Mixing in Slew**

For audio mixing, configure the various gains with respect to vehicle speed.

**Playback of WAV Files**

Although the required WAV files are present in flash, the user can play or stop some of the WAV files, depending on the dynamic conditions.
**SigmaDSP-BASED SOLUTION**

For entry-level applications, an ADAU1450 SigmaDSP processor can be used as an alternative to the ADSP-BF706 processor. For evaluation purposes, the EVAL-ADAU1452 evaluation board can be used.

Figure 5 shows the different processing blocks inside the SigmaDSP processor.

The following software requirements are supported in the ADAU1450 using the SigmaStudio programming environment:

- Multiple tone generation
- Dynamic volume control with up to 64 ranges
- Sound mixing
- Limiter
- Pitch shifting which increases audio pitch as speed increases
- Simultaneous playback of up to 5 WAV files from the SPI flash memory

Analog Devices provides an engine sound simulator module in SigmaStudio to simplify engine sound tuning and reduce the number of external, simultaneous WAV files required. The engine sound simulator can internally generate up to 32 harmonics. The order and amplitude of these harmonics can be programmed through the graphical user interface (GUI).

The engine sound simulator module is released in SigmaStudio and available by request from the Software Request Form page on the Analog Devices website.

Note that SigmaStudio cannot support a CAN software stack; however, an external microprocessor is required.
**SigmaStudio**

SigmaStudio is a graphical programming environment originally designed for the SigmaDSP portfolio of processors. This software has a built-in library of algorithms developed specifically for automotive applications. The GUI simplifies the tuning process and provides controls and filter coefficients that can be varied on the fly without the requirement for writing code. SigmaStudio can be downloaded from the SigmaStudio page on the Analog Devices website.
CONCLUSION

Analog Devices offers comprehensive solutions for an entry level system and for an advanced engine sound system that supports an in cabin engine sound and an external engine sound. This application note aims to ease the decision making process for the user, as well as reduce user time to market. Analog Devices offers the complete system solution, including the necessary software components for rapid prototyping and product development.

I2C refers to a communications protocol originally developed by Philips Semiconductors (now NXP Semiconductors).