

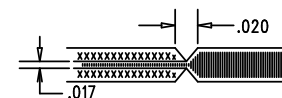
SHOWN FROM COMPONENT SIDE


REVISIONS			
REV	DESCRIPTION	APPR	DATE
A	1ST PROTOTYPE	LEO C.	03-02-09

SIZE	QTY	SYM	PLATED	TOL
0.015	44	+	YES	+/-0.003"
0.07	3	X	NO	+/-0.003"
0.064	13	□	YES	+/-0.003"
0.035	26	◇	YES	+/-0.003"

NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600
- MATERIAL: EPOXY FIBERGLASS, NEMA GRADE FR-4
FINISHED THICKNESS TO BE 0.062" +/- .005"
TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE
OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
FLAMMABILITY RATING: 94 V-0 MINIMUM .
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00" ARE PRIMARY DATUMS.
- DRILLING: DRILL HOLES PER SCHEDULE. PLATE THROUGH
HOLES WITH COPPER, 0.001" THICK MIN.
ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
HOLE LOCATION TOLERANCES ARE +/-0.003"
IN RELATION TO CENTER
- FINISH: SMOBC USING LPI BOTH SIDES, COLOR GREEN.
GOLD IMMERSION. USE LEAD FREE SOLDER FOR PROTOTYPE.
FOR SILKSCREEN: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB:



APPROVALS			 LINEAR TECHNOLOGY 1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 LTC CONFIDENTIAL - FOR CUSTOMER USE ONLY	
	INIT	DATE		
DRAWN			TITLE: FABRICATION DRAWING OCTAL 12-BIT I2C VOUT DAC WITH 10PPM/DEGREE C REFERENCE	
CHECK				
DESIGN	KIM T.	03-02-09		
ENGR	LEO C.	03-02-09		
			SIZE A	REV. A
			DEMO DC1534A - LTC2637	
SCALE = NONE			SHT 1 of 1	