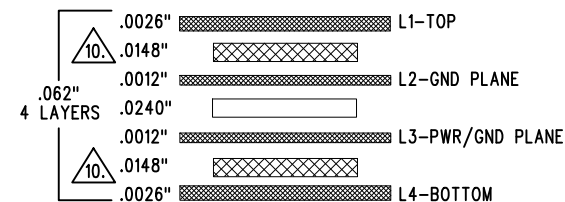


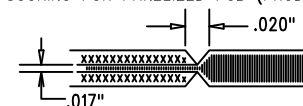
REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	2	PRODUCTION	MICHEL A.	09-12-12

LAYER STRUCTURE



NOTES: UNLESS OTHERWISE SPECIFIED

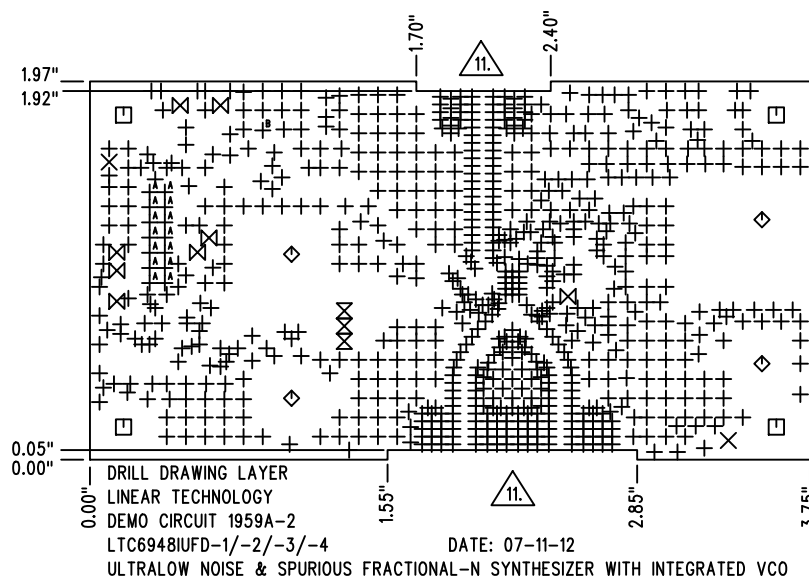
- FAB PER IPC-A-600.
- MATERIAL: -EPOXY FIBERGLASS, NELCO 4000-13
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00" ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



- CONTROLLED 50 OHM +/-10% IMPEDANCE FOR LAYERS 1-2 USING 0.03" TRACE.

10. SUBJECT TO CHANGE BY MANUFACTURER, DEPENDING ON DIELECTRIC CONSTANT DEVIATIONS. PLEASE CONSULT LTC.

11. INNER AND OUTER LAYER COPPER SHALL BE EXPOSED ALONG BOARD EDGES. DO NOT MODIFY INNER LAYER COPPER BACKOFF OUTLINE (SMA CONNECTOR).



SIZE	QTY	SYM	PLATED	TOL
0.008	764	+	YES	+/-0.003"
0.07	2	X	NO	+/-0.003"
0.19	4	□	NO	+/-0.003"
0.215	4	◇	YES	+/-0.003"
0.035	3	⊗	YES	+/-0.003"
0.025	8	⊗	YES	+/-0.003"
0.04	14	+ ^A	YES	+/-0.003"
0.094	1	+ ^B	YES	+/-0.003"

UNLESS OTHERWISE SPECIFIED		APPROVALS			
DIMENSIONS ARE IN INCHES		PCB DES.	KIM T.		
TOLERANCES:		APP ENG.	MICHEL A.	TITLE: FABRICATION DRAWING	
0.XX" = ±0.01"				ULTRALOW NOISE & SPURIOUS FRACTIONAL-N SYNTHESIZER WITH INTEGRATED VCO	
0.XXX" = ±0.005"					
INTERPRET DIM AND TOL PER ASME Y14.5M-1994				SIZE	IC NO. LTC6948IUFD-1/-2/-3/-4
THIRD ANGLE PROJECTION				N/A	DEMO CIRCUIT 1959A
		SCALE = NONE		FILENAME: DC1959A-2.PCB	SHT 1 OF 1

1630 MCCARTHY BLVD
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