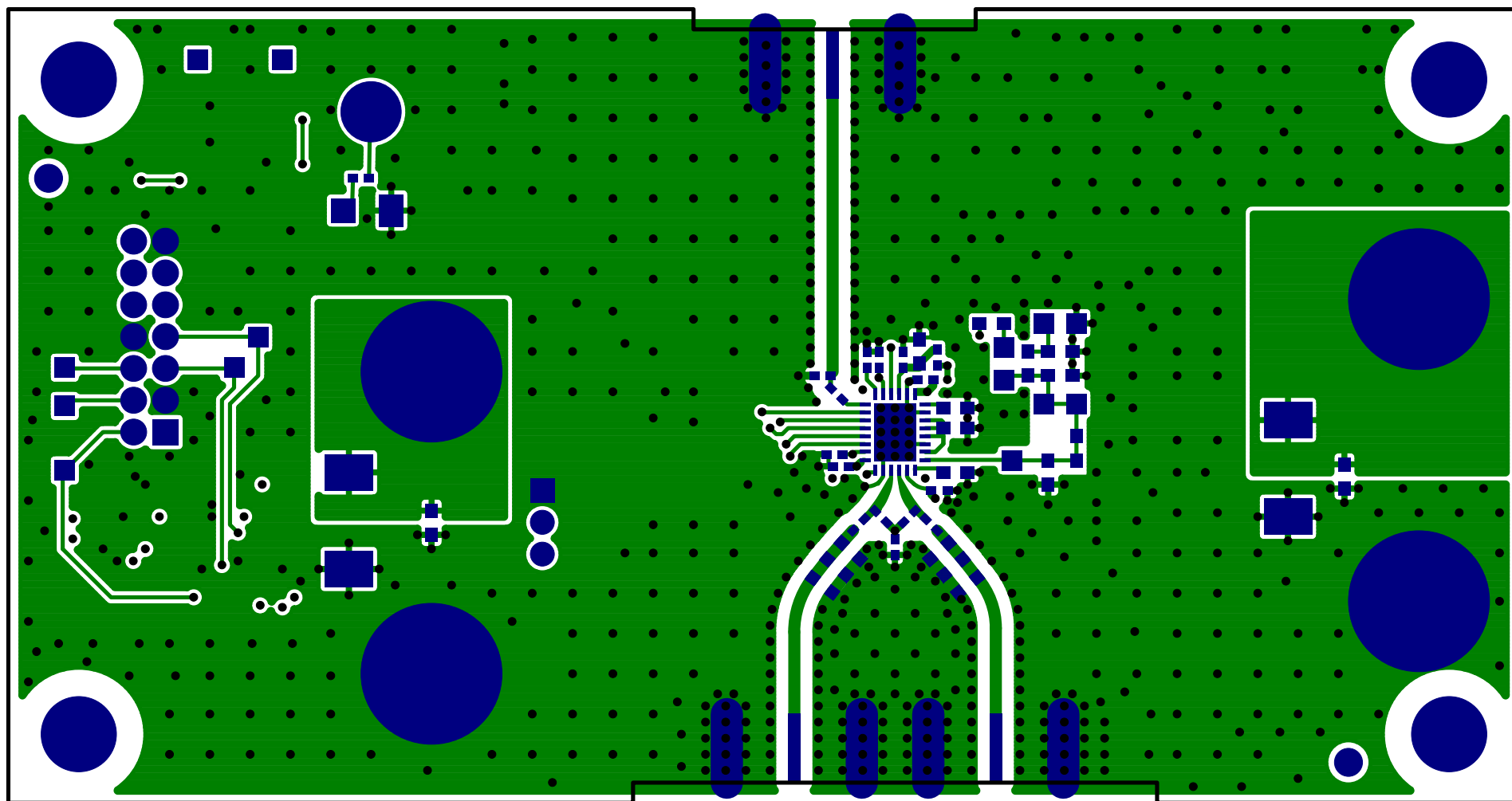




LAYER 1 – TOP LAYER

LINEAR TECHNOLOGY

DEMO CIRCUIT 1959A-2

LTC6948IUFD-1/-2/-3/-4

DATE: 07-10-12

ULTRALOW NOISE & SPURIOUS FRACTIONAL-N SYNTHESIZER WITH INTEGRATED VCO