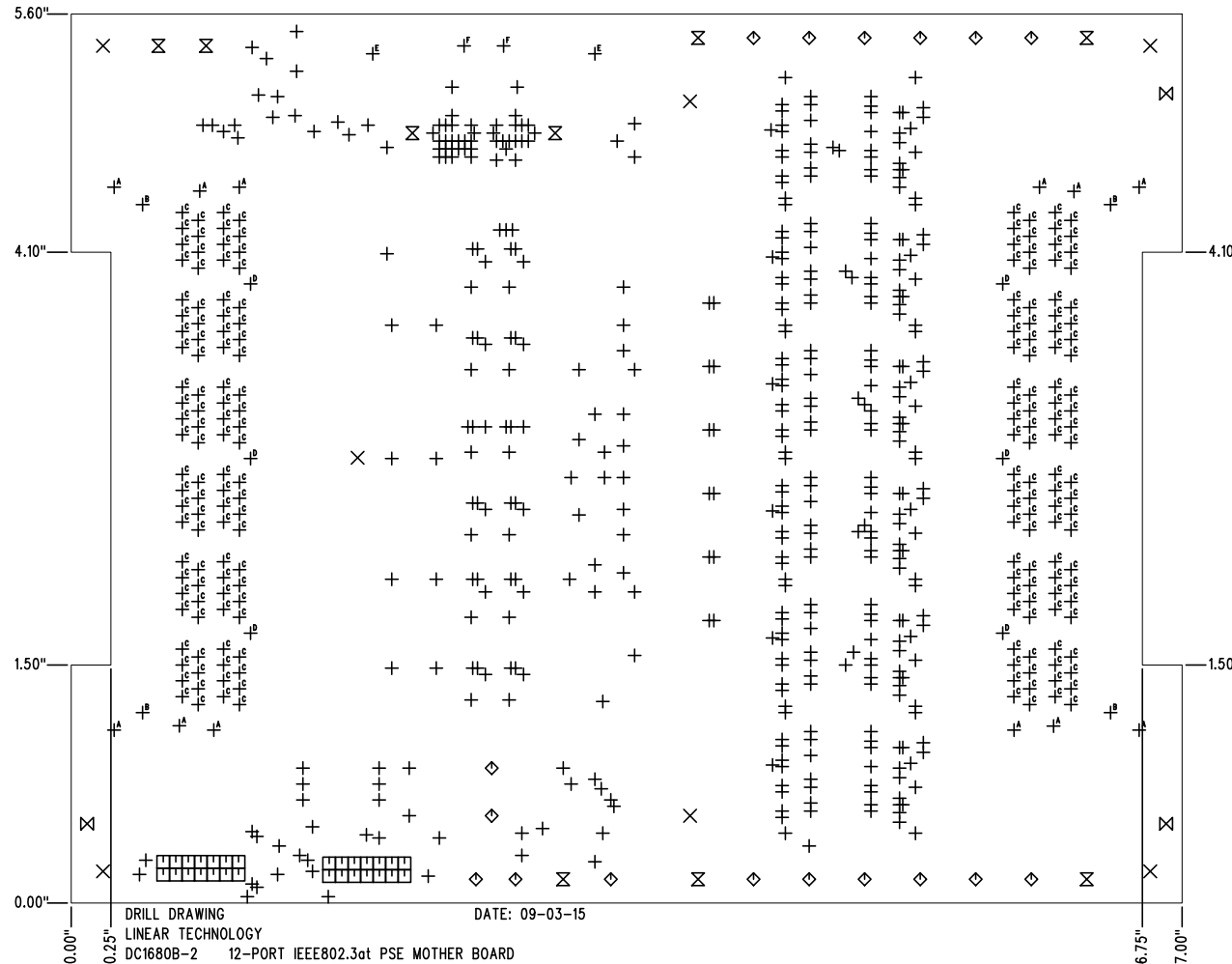


SHOWN FROM TOP SIDE

REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	2	REBUILD WITH CHANGE	DILIAN R.	09-03-15



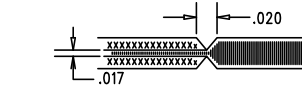
LAYER STRUCTURE

0.062"	6 LAYERS	.0028"	L1-TOP
		.0045"	
		.0014"	L2-AGND, CGND PLANE 1
		.004"	
		.0014"	L3-SIG, AGND, CGND PLANE 2
		.0014"	
		.0014"	L4-AGND, CGND PLANE 3
		.004"	
		.0014"	L5-SIG, AGND, CGND PLANE 4
		.0045"	
		.0028"	L6-BOTTOM


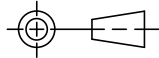
SIZE	QTY	SYM	PLATED	TOL
0.01	450	+	YES	+/-0.003"
0.19	7	×	NO	+/-0.003"
0.04	28	□	YES	+/-0.003"
0.063	17	◇	YES	+/-0.003"
0.094	9	⊠	YES	+/-0.003"
0.07	3	⊠	NO	+/-0.003"
0.065	12	† ^A	YES	+/-0.003"
0.13	4	† ^B	NO	+/-0.003"
0.035	192	† ^C	YES	+/-0.003"
0.065	6	† ^D	NO	+/-0.003"
0.207	2	† ^E	YES	+/-0.003"
0.07	2	† ^F	YES	+/-0.003"

NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 6 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00" ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



9. SUBJECT TO CHANGE BY MANUFACTURER, DEPENDING ON DIELECTRIC CONSTANT DEVIATIONS. PLEASE CONSULT LTC.

UNLESS OTHERWISE SPECIFIED		APPROVALS		 LINEAR TECHNOLOGY 1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL - FOR CUSTOMER USE ONLY	
DIMENSIONS ARE IN INCHES		PCB DES.	KIM T.		
TOLERANCES:		APP ENG.	DILIAN R.	TITLE: FABRICATION DRAWING 12-PORT IEEE802.3at PSE MOTHER BOARD	
0.XX" = ±0.01"					
0.XXX" = ±0.005"				SIZE N/A IC NO. DEMO CIRCUIT 1680B	
INTERPRET DIM AND TOL PER ASME Y14.5M-1994				REV 2	
THIRD ANGLE PROJECTION				FILENAME: DC1680B-2.PCB	
		SCALE = NONE		SHT 1 OF 1	