


NOTES : Unless Otherwise Specified

1. MAXIMUM SOLDER TEMPERATURE IS 240 DEG C.
2. BREAK BOARDS FROM THE PANEL WHEN ASSEMBLY IS COMPLETE.
3. OPTIONAL COMPONENTS ARE UNSTUFFED AND SHOULD BE FREE OF SOLDER AFTER ASSEMBLY.
4. JUMPER SHUNTS INSTALLATION: JP2;
JP7, JP8 : LO
JP9 : ON

ASSY DRAWING

APPROVALS			 1630 McCarthy Blvd. Milpitas, CA 95035 PH: (408)432-1900	
	INIT	DATE		
DRAWN				
CHECK				
DESIGN	J. Wu	3/28/08	TITLE: LTM4601AEV/LTM4601AHVEV	
ENGR	H. Moo	3/28/08	High Efficiency DC/DC μ Module	
			Demo Circuit 1414B	REV. B
SCALE = NONE			SHT 1 of 1	