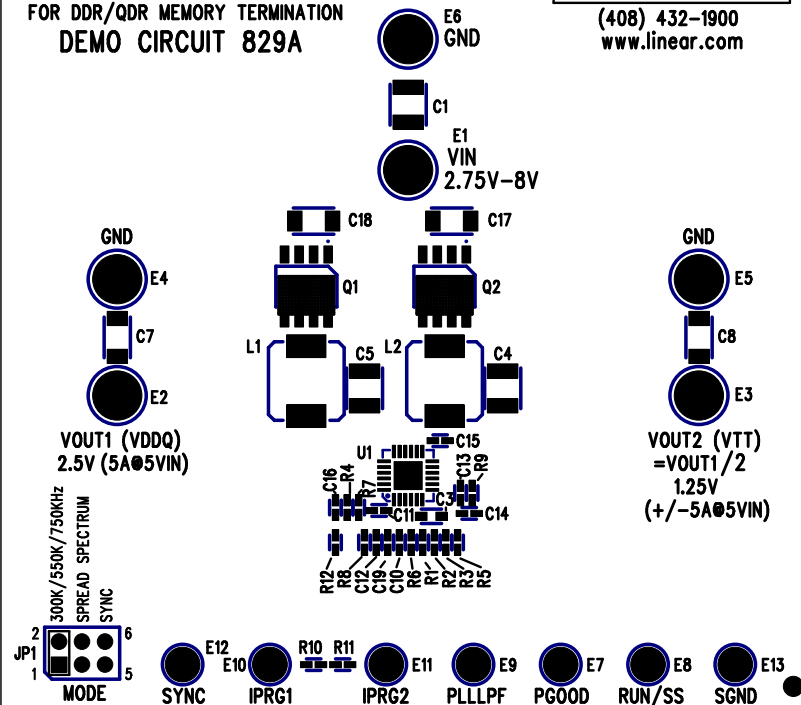


# REVISIONS

REV	DESCRIPTION	APPR	DATE
A	PROTOTYPE RELEASE		

## LTC3776EUF 2-PHASE DUAL STEP-DOWN CONVERTER FOR DDR/QDR MEMORY TERMINATION DEMO CIRCUIT 829A



## NOTES: UNLESS OTHERWISE SPECIFIED

1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. INSTALL SHUNTS ON JP1 PIN1 AND 2.
3. PARTS TO OMIT WILL BE SPECIFIED ON THE BILLS OF MATERIAL. LOCATIONS OF OMITTED PARTS SHALL BE FREE OF SOLDER. MASK THE SOLDER STENCIL WHERE SMT PARTS ARE OMITTED.
4. DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON FOUR SIDES OF THE BOARD EDGE.
5. ASSY PROCESSES SHALL INCLUDE: REFLOW SOLDER TOP SIDE SMD.

## APPROVALS

	INIT	DATE
DRAWN		
CHECK		
DESIGN	KIM T.	01-20-05
ENGR	CHARLIE Z.	01-20-05



1630 MCCATHY BLVD  
MILPITAS, CA 95035  
PH: (408)432-1900  
LTC CONFIDENTIAL-  
FOR CUSTOMER USE ONLY

TITLE: TOP ASSEMBLY DRAWING  
2-PHASE DUAL STEP-DOWN CONVERTER  
FOR DDR/QDR MEMORY TERMINATION

SIZE A DEMO DC829A - LTC3776EUF REV. A

SCALE = NONE

DES- 0000

SHT 1 OF 2