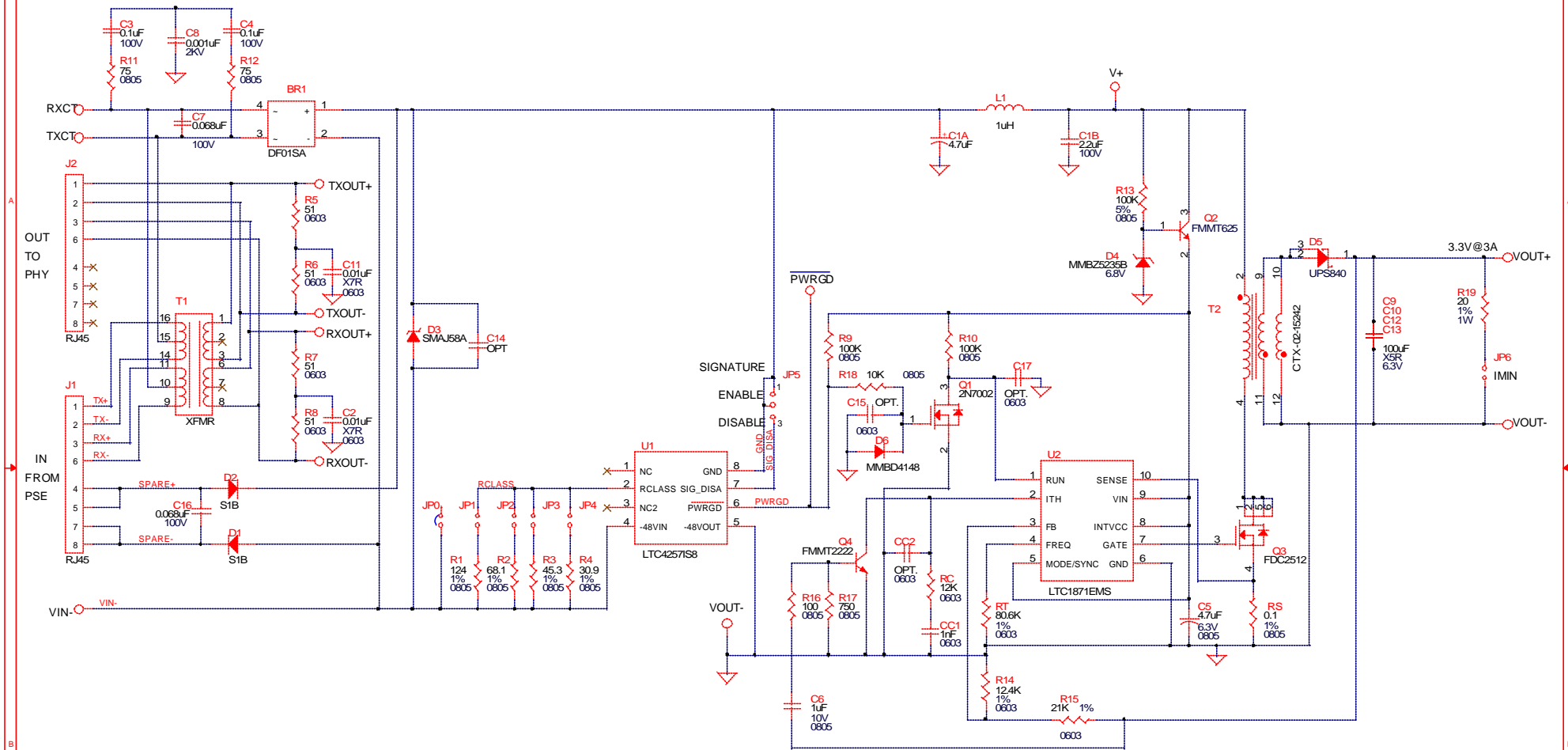
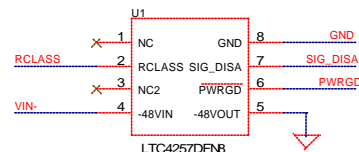


| REVISION HISTORY |     |             |          |          |
|------------------|-----|-------------|----------|----------|
| ECO              | REV | DESCRIPTION | DATE     | APPROVED |
|                  | 4   | PROTO       | 12/02/02 |          |



NOTES: UNLESS OTHERWISE SPECIFIED,

1. ALL RESISTOR VALUES ARE 5%.
2. ASSEMBLY: INSTALL ONLY 2 JUMPERS ON PCB. PUT ONE JUMPER ON JP1 AND PUT ONE JUMPER ON JP5-ENABLE. CUSTOMER: INSTALL JUMPER ON JP1 FOR CLASSIFICATION CLASS 1, JP2 FOR CLASS 2, ETC. INSTALL JUMPER ON JP5-ENABLE FOR NORMAL OPERATION. INSTALL JUMPER JP5-DISABLE TO DISABLE 25K OHM SIGNATURE.



|   |  |                             |  |  |  |
|---|--|-----------------------------|--|--|--|
| UNLESS OTHERWISE SPECIFIED<br>DIMENSIONS ARE IN INCHES<br>TOLERANCE ON ANGLE ...<br>2 PLACES ... 3 PLACES ...<br>INTERPRET DIM AND TOL<br>PER ASME Y14.5M -1994 |  | CONTRACT NO.                |  | DATE   |  |
| THIRD ANGLE PROJECTION  |  | APPROVALS                   |  | 12-02-02   |  |
|   |  | DRAWN MEI                   |  |  |  |
|   |  | CHECKED                     |  |  |  |
|   |  | APPROVED                    |  |  |  |
|   |  | DESIGNER                    |  |  |  |
| DO NOT SCALE DRAWING  |  | Thursday, December 05, 2002 |  | <div> </div>   |  |
|   |  | TITLE                       |  | SCH, LTC4257IS8 IEEE 802.3af PD INTERFACE CONTROLLER |  |
|   |  | SIZE                        |  | CAGE CODE  |  |
|   |  | Custom                      |  | DWG NO   |  |
|   |  |                             |  | DC485A   |  |
|   |  |                             |  | REV  |  |
|   |  |                             |  | 4  |  |
|   |  | SCALE: NONE                 |  | FILENAME: 485A-4.DSN                                 |  |
|   |  | SHEET                       |  | 1 OF 1   |  |