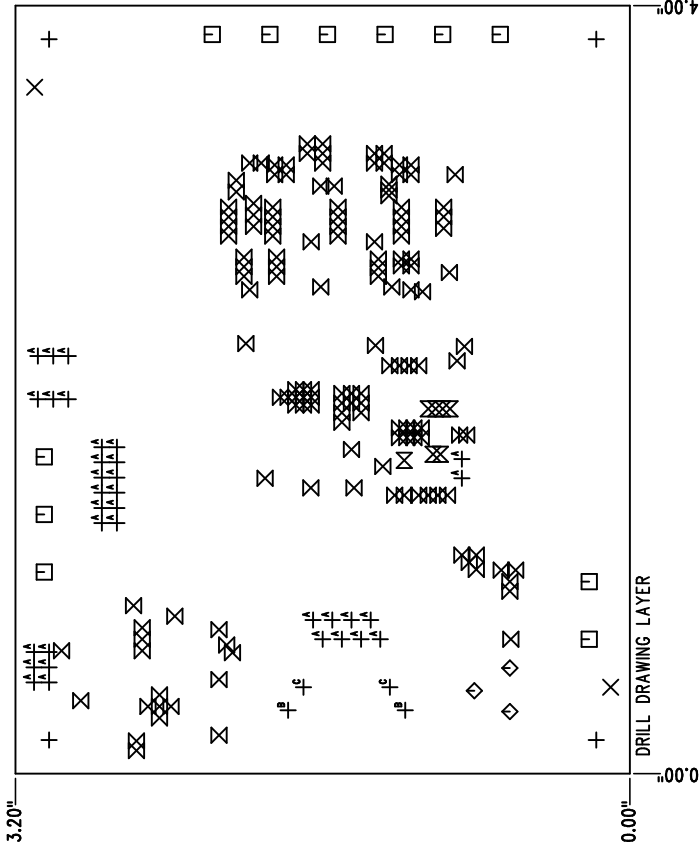
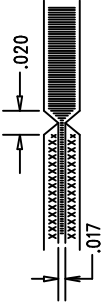


| REVISIONS |             |      |      |
|-----------|-------------|------|------|
| REV       | DESCRIPTION | APPR | DATE |
|           |             |      |      |

| SIZE | QTY | SYM | PLTD | TOL    |
|------|-----|-----|------|--------|
| 187  | 4   | +   | N0   | +/-0.0 |
| 70   | 2   | ×   | N0   | +/-0.0 |
| 94   | 11  | □   | YES  | +/-0.0 |
| 120  | 3   | ◇   | YES  | +/-0.0 |
| 15   | 147 | ⊗   | YES  | +/-0.0 |
| 10   | 7   | ⊗   | YES  | +/-0.0 |
| 35   | 34  | ⊕   | YES  | +/-0.0 |
| 60   | 2   | ⊕   | YES  | +/-0.0 |
| 125  | 2   | ⊕   | N0   | +/-0.0 |

NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-600. LEAD FREE ASSEMBLY TEMPERATURE REQUIRED.
2. MATERIAL: EPOXY FIBERGLASS, NEMA GRADE FR-4  
FINISHED THICKNESS TO BE 0.062" +/- 0.006"  
TOTAL OF 4 LAYERS WITH 2 OZ. COPPER ON THE OUTER LAYERS AND 1 OZ. COPPER ON THE INTERNAL LAYERS.  
FLAMABILITY RATING: 94 V-0 MINIMUM .
3. FINISH: SMOBC USING LPI BOTH SIDES, COLOR GREEN.  
GOLD IMMERSION (OMIKRON). ( not required for prototype )  
FOR SILKSCREEN: USE WHITE NON-CONDUCTIVE INK.
4. DRILLING: DRILL HOLES PER SCHEDULE. ALL PLATED THROUGH HOLES WITH COPPER, 0.001" MIN. / 0.0015" MAX. COPPER PLATE  
ALL HOLE SIZES ARE SPECIFIED AFTER PLATING. HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER  
5. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.  
6. SCORING FOR PANELIZED PCB: ( not required for prototype )



SHOWN FROM COMPONENT SIDE

| APPROVALS                                                                                                                        |      |      |            |
|----------------------------------------------------------------------------------------------------------------------------------|------|------|------------|
| DRAWN                                                                                                                            | INIT | DATE |            |
| CHECK                                                                                                                            | KL   |      |            |
| DESIGN                                                                                                                           |      |      |            |
| ENGR                                                                                                                             |      |      |            |
| TITLE: FABRICATION DRAWING<br>Triple Output, High Power, High Efficiency POE<br>PD Interface with Integrated Switching Regulator |      |      |            |
| SIZE                                                                                                                             | A    | REV. | A          |
| SCALE = NONE                                                                                                                     |      |      |            |
| 1630 McCarthy Blvd.<br>Milpitas, CA 95035<br>PH: (408)432-1900                                                                   |      |      | SHT 1 of 1 |