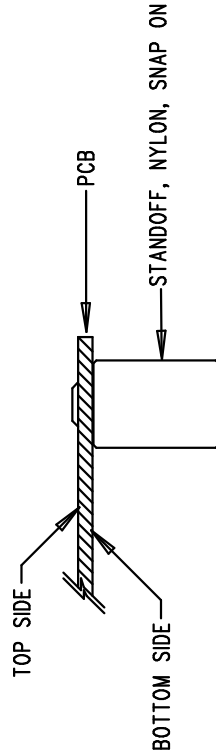


REVISIONS			
REV	DESCRIPTION	APPR	DATE

NOTES: UNLESS OTHERWISE SPECIFIED

1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. INSTALL SHUNT ON JP1-JP3 PIN 1 AND 2.
3. PARTS TO OMIT WILL BE SPECIFIED ON THE BILLS OF MATERIAL. LOCATIONS OF OMITTED PARTS SHALL BE FREE OF SOLDER. MASK THE SOLDER STENCIL WHERE SMT PARTS ARE OMITTED.
4. DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON FOUR SIDES OF THE BOARD EDGE.
5. ASSY PROCESSES SHALL INCLUDE: REFLOW SOLDER TOP SIDE SMD.
6. INSTALL 4 STANDOFFS AT 4 CORNERS AS SHOWN BELOW:



ASSY DRAWING

APPROVALS

INIT	DATE
DRAWN	KL
CHECK	
DESIGN	
ENGR	

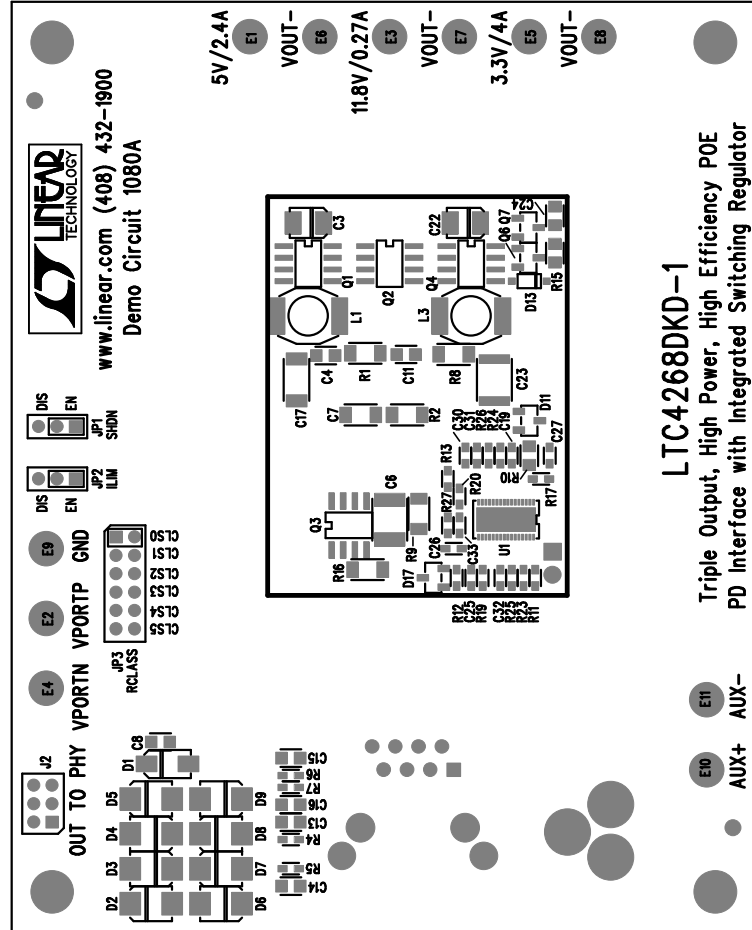


TITLE: TOP ASSEMBLY DRAWING

Triple Output, High Power, High Efficiency POE
PD Interface with Integrated Switching Regulator

SIZE	REV.
A	A
DC1080A, LTC4268DKD-1	

SHT 1 of 2



ASSY TOP

