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For your convenience, the below describes the related updates:

For revised/new documentation, please reference 2021-01-22 in the page headings.

E516634-20200930-Description
Figure-1-Total
Illustration-2-Total

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File E516634 Project 4789491519

September 30, 2020

REPORT

on

COMPONENT - Equipment and Systems for Use in Hazardous Locations - Component

(OERX2, OERX8)

Complementary Product Category

COMPONENT - Intrinsically Safe Equipment and Systems for Use in Zone

Classified Hazardous Locations - Component

(OEVX2, OEVX8)

Recognized Company:

ANALOG DEVICES INC WILMINGTON, MA

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File E516634 Vol. 1 Sec. 1 Page 1 Issued: 2020-09-30 and Report

DESCRIPTION

PRODUCT COVERED:

USR, CNR - Digital Isolator, Intrinsically Safe Component for use in Class I, Division 1, Groups A, B, C, and D

USR - Digital Isolator, Intrinsically Safe Component for use in Class I, Zone 0, AEx ia IIC, Hazardous Locations

CSR - Digital Isolator, Intrinsically Safe Component for use in Class I, Zone O, Ex ia IIC, Hazardous Locations

Models: ADuM1440ARQZ
ADuM1441ARQZ
ADuM1445ARQZ
ADuM1445ARQZ
ADuM1446ARQZ
ADuM1447ARQZ
ADuM1440ARSZ
ADuM1441ARSZ
ADuM1441ARSZ
ADuM1445ARSZ
ADuM1445ARSZ
ADuM1445ARSZ
ADuM1447ARSZ
ADuM1447ARSZ
ADuM1447ARSZ
ADUM1447ARSZ

GENERAL:

These devices are non-optical isolators consisting of an encoder and decoder. The encoder and decoder are separated by a transformer. Internal "chips" are connected to lead frames that are molded into the enclosure.

These products shall comply with all elements of ordinary locations File E214100, issued date 2014-08-09, Non-optical Isolating Device, covering for this Applicant and with the following description. Should the above mentioned Procedure File be withdrawn, labeling under this Procedure must be discontinued until authorization to resume is received.

File E516634 Vol. 1 Sec. 1 Page 2 Issued: 2020-09-30 and Report

TECHNICAL CONSIDERATIONS (NOT FOR FIELD REPRESENTATIVE'S USE):

USR indicates investigation to United States Standards:

- UL 913 Standard For Intrinsically Safe Apparatus and Associated Apparatus For Use in Class I, II, III, Division 1, Hazardous (Classified) Locations- Edition 8 Revision Date 2018/12/10
- UL60079-0, Explosive Atmospheres Part 0: Equipment General Requirements, Edition 7, Revision Date 04/15/2020
- UL 60079-11 Explosive atmospheres Part 11: Equipment protection by intrinsic safety "i"- Edition 6 Revision Date 2018/09/14

CNR indicates investigation to Canadian National Standards:

- CSA C22.2 No. 60079-0, Electrical Apparatus for Explosive Gas Atmospheres Part 0: General Requirements, Edition 4, Issue Date 02/2019
- CSA C22.2 NO. 60079-11:14 Explosive atmospheres Part 11: Equipment protection by intrinsic safety "i"- Edition 2 Issue Date 2014/02/01

The models ADuM144XARYZ are intended for use in an ambient temperature of -40°C to 85°C .

The model AD71217ARSZ is intended for use in an ambient temperature of -55°C to 85°C .

CONDITIONS OF ACCEPTABILITY -

- 1. The components being certified comply with UL 60079-11:2014. When one of these components will be used in an equipment, the component is to be fitted on a PCB inside a suitable enclosure and re-certified as equipment. The operating temperature range of these components is -55°C to 85°C.
- 2. 2 This assembly is an isolating component between separate intrinsically safe circuits. The assembly should be connected to suitably certified intrinsically safe circuits considering following entity parameters:

Terminal Encoder

Parameter	Ui	Ii	Pi	Li	Ci
All Pins	42V	275mA	1.3W	0uH	4pF

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Terminal Decoder

Parameter	Ui	Ii	Pi	Li	Ci
All Pins	42V	275mA	1.3W	0uH	4pF

3. The components (i.e. Digital Isolators) being certified will have the following safety ratings. The temperature class will be determined based on the following table:

Package	Temperature	Maximum Permissible
Туре	Coefficient	Temperature
QSOP-16	40.4°C/W	189.8°C
SSOP-20	51.2°C	218.0°C

*

- * 4. The capability of the device to control a load has not been investigated.
- * 5. These devices should be installed in a suitable end product enclosure.
- * 6. If the maximum operating (ambient) temperature exceeds the rating noted in the ratings table, additional means should be used to determine if the maximum junction temperature of the device is exceeded.
- * 7. For single protection devices, the insulation to the case has not been evaluated. For double protection devices, the insulation to the case has been evaluated to the isolation voltage specified in the ratings table.

CONSTRUCTION DRAWING:

* The construction drawing is shown in ILL. 1.

MARKING:

* The marking drawing is shown in ILL. 2.

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MODEL NOMENCLATURE:

The Model number designates the following:

ADuM144XARYZ:

- X Number 0, 1, 2, 5, 6, 7 Determines numbers of inputs on encoder or decoder side.
 - 0- Encoder 4 inputs, Decoder 0 inputs
 - 1- Encoder 3 inputs, Decoder 1 inputs
 - 2- Encoder 2 inputs, Decoder 2 inputs
 - 5- Encoder 4 inputs, Decoder 0 inputs
 - 6- Encoder 3 inputs, Decoder 1 inputs
 - 7- Encoder 2 inputs, Decoder 2 inputs
- Y Q or S Determines package size.
 - Q- RQ-16, QSOP-16 package
 - S- RS-20, SSOP-20 package

AD71217ARSZ:

3 encoder inputs, 1 decoder input, RS-20, SSOP-20 package size, and a low ambient of -55°C.

CONSTRUCTION DETAILS:

General - The devices are constructed in accordance with the following figures, illustrations and descriptive pages. Dimensions are approximate unless specifically indicated otherwise.

[X] Spacings - Where affecting intrinsic safety, spacings have been evaluated and found to be in accordance with Table 5 of UL 60079-11, Sixth Edition and CSA C22.2 No. 60079-11:14.

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DOCUMENTATION:

The devices are constructed in accordance with the following drawings:

FIG.	ILL.			
No.	No.	Description	Drawing No.	Revision
1		ADuM144XARQZ		
	1	Construction Drawing	CF0002	0
	2	Marking Drawing	MF0002	0

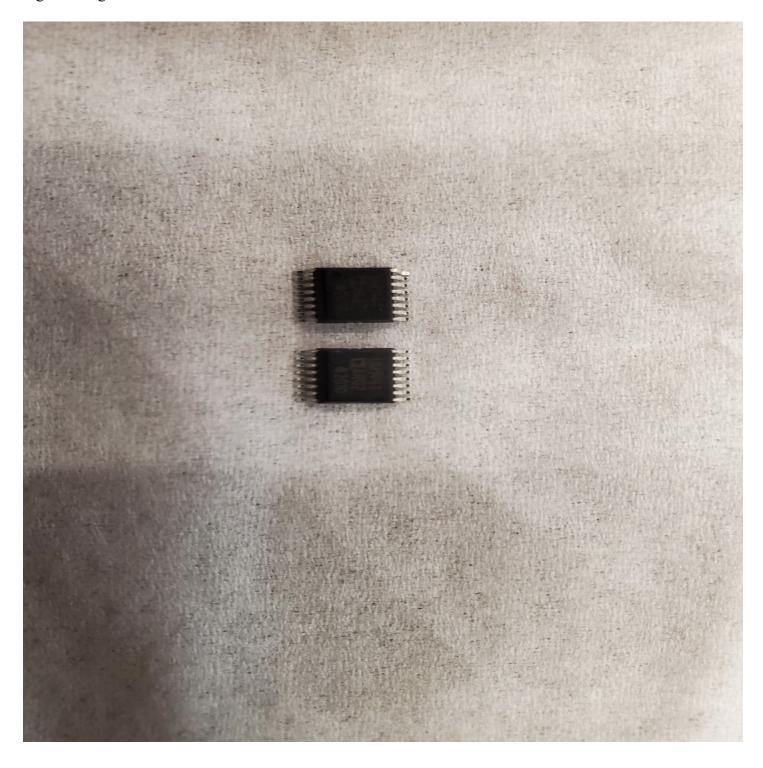
NOTE FOR FIELD REPRESENTATIVE - Confirm all drawing numbers and revision levels/dates.

Fig.1 is a representative sample picture, ADuM144XARQZ models have 16 pins and ADuM144XARSZ and AD71217ARSZ models have 20 pins.

Enclosure - ADuM144XARQZ, where X can be any of these numbers 0, 1, 2, 5, 6, 7, RQ-16 package with overall dimensions of 0.193 in. by 0.154 in. by 0.057 in. Molded from G600C epoxy manufactured by Suitomo. Minimum separation distance from encoder to decoder pins of 3.1mm.

Enclosure - AD71217ARSZ and ADuM144XARSZ, where X can be any of these numbers 0, 1, 2, 5, 6, 7, RS-20 package with overall dimensions of 7.20 in. by 5.30 in. by 1.75 in. Molded from G600C epoxy manufactured by Suitomo. Minimum separation distance from encoder to decoder pins of $4.6 \, \mathrm{mm}$.

Figure-1 Page-1



The following Page(s) are related to Illustration-1. The next supplement, if applicable, will be identified with a new Supplement Page Heading.

	REVISIONS		
REV	DESCRIPTION	DATE	APPROVED
Α	INITIAL RELAESE	9-18-20	

Certificate component number: E516634

The components being certified comply with IEC 60079-11:2011. When one of these components will be used in an equipment, the component is to be fitted on a PCB inside an enclosure and re-certified as equipment. The creepage and clearance distances across the isolating component have been evaluated but the distances to other circuitry remain the responsibility of the user of the certified equipment

When the component will be fitted in an Intrinsically Safe equipment, 500V Routine Dielectric strength test must be carried out in accordance with 6.3.13 of IEC 60079-11:2011.

DIMENS	THERWISE SI ONSARE IN I OLERANCES FRACTIONS +- 1/32	NCHES	ASSEM	1BLY		ANALOG DEVICES		
.XXX +005			APPROVAL	DATE	TITLE			
Material			DRAWN BY					
			M S CANTRELL	7-25-2019	V DITI	/1//Y INIT	RINSIC SAFE	TV
			DESIGNED		ADun		MINDIC DALL	_ ' '
			M S CANTRELL	7-25-2019	CO1	ICTDI ICTIO		$\boldsymbol{\sim}$
			CHECKED			12 1 KUC 1 10	N DRAWIN	G
			K MCLAUGHLIN	7-25-2019				
Finish					SIZE	FSCM NO	DRAWING NUMBER	REV
					В		CF0002	0
DO NOT SCA	LE DWG				SCALE N/A		SHEET 1	OF 5

	REVISIONS		
REV	DESCRIPTION	DATE	APPROVED
0	INITIAL RELAESE		

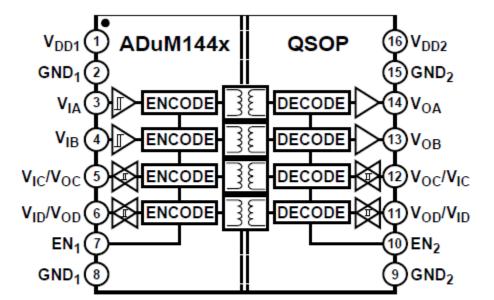


Figure 1.

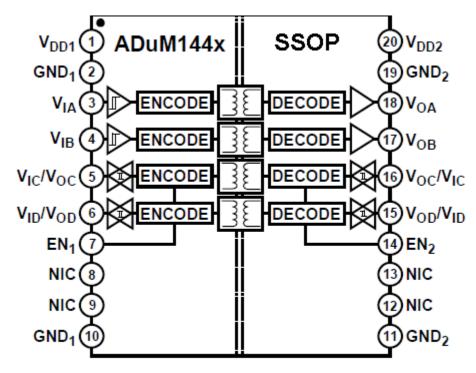


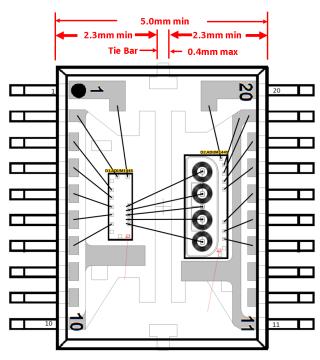
Figure 2

This assembly is an isolating component between separate intrinsically safe circuits. The assembly should be connected to suitably certified intrinsically safe circuits considering below entity parameters:

COMPONENT ENTITY PARAMETERS

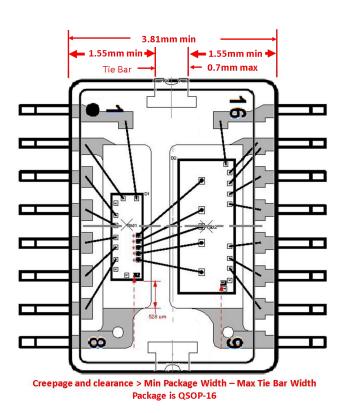
Package type	Entity parameters Side 1	Entity parameters Side 2
	Ui=42V	Ui=42V
	Ii=275mA	Ii=275mA
QSOP-16	Pi=1.3W	Pi=1.3W
	Li=0	Li=0
	Ci=4pF	Ci=4pF
	Ui=42V	Ui=42V
	Ii=275mA	Ii=275mA
SSOP-20	Pi=1.3W	Pi=1.3W
	Li=0	Li=0
	Ci=4pF	Ci=4pF

DIMENS	THERWISE SI IONSARE IN I TOLERANCES FRACTIONS +- 1/32	INCHES	ASSEN	ИВLY		ANALOG DEVICES		
.XXX + .005			APPROVAL	DATE	TITLE			
Material			DRAWN BY		1			
			M S CANTRELL	7-25-2019	\ \D\\\	/1//Y INIT	RINSIC SAFI	TV
			DESIGNED		ADun		MINDIC DALL	_ ' '
			M S CANTRELL	7-25-2019	CO1	ICTDI ICTIO		_
			CHECKED			15 I KUC I IU	N DRAWIN	G
			K MCLAUGHLIN	7-25-2019				
Finish					SIZE	FSCM NO	DRAWING NUMBER	REV
					В		CF0002	0
DO NOT SCA	ALE DWG				SCALE N/A		SHEET 1	OF 5



Creepage and clearance > Min Package Width – Max Tie Bar Width Package is SSOP-20

The minimum specified creepage and clearance distance across the SSOP-20 device is 4.6mm (i.e 2.3mm +2.3mm). The CTI is specified to be at least 100V



The minimum specified creepage and clearance distance across the QSOP-16 device is 3.1mm (i.e 1.55mm +1.55mm). The CTI is specified to be at least 100V

REVISIONS							
REV	DESCRIPTION	DATE	APPROVED				
0	INITIAL RELAESE						

BILL OF MATERIALS

Die 1	ADuM144XIC_A
Die 2	ADuM144xTC_A
Mold Compound	Sumitomo G600C
Coil Insulation Material Asahi I-8124	
Wire Type	M3 4N Gold
Wire Diameter	1 mil
Adhesive Ablestik 84-1 LMISR4 Conductive	
Leadframe	See SSOP Package or QSOP Package sections

ASSEMBLY

Die 1 is adhered to the pin 1 side of the lead frame using Adhesive

Die 2 is adhered to the Pin 16 side of the lead frame using Adhesive

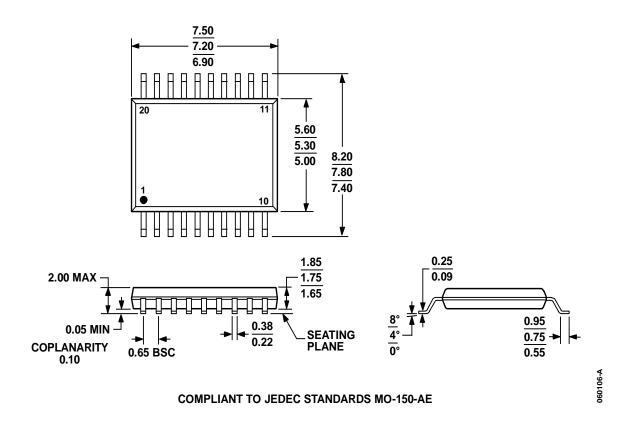
Bond wires are applied depending on the channel configuration

The assembly is placed in a mold and transfer molded to final dimensions.

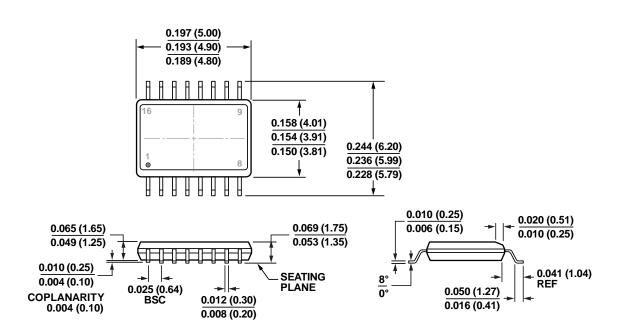
Components are singulated and lead formed to final specifications appropriate to the package.

UNLESS OTHERWISE SPECIFIED DIMENSIONSARE IN INCHES TOLERANCES DECIMALS FRACTIONS ANGLES .XX + .010 + .1/32 + .2	ASSEM	1BLY	_	ANALOG DEVICES		
.XXX +005	APPROVAL	DATE	TITLE			
Material	DRAWN BY					
	M S CANTRELL	7-25-2019	V DITI	/1//Y INIT	RINSIC SAFI	TV
	DESIGNED		ADun		MINDIC DALL	_
	M S CANTRELL	7-25-2019	CO1	ICTULICTIO		
	CHECKED			12 I KUCI IU	N DRAWIN	J
	K MCLAUGHLIN	7-25-2019				
Finish			SIZE	FSCM NO	DRAWING NUMBER	REV
			В		CF0002	0
DO NOT SCALE DWG			SCALE N/A		SHEET 1	OF 5

	REVISIONS		
REV	DESCRIPTION	DATE	APPROVED
0	INITIAL RELAESE		



Package	Leadframe Number
RS20	442436



COMPLIANT TO JEDEC STANDARDS MO-137-AB

CONTROLLING DIMENSIONS ARE IN INCHES; MILLIMETER DIMENSIONS (IN PARENTHESES) ARE ROUNDED-OFF INCH EQUIVALENTS FOR REFERENCE ONLY AND ARE NOT APPROPRIATE FOR USE IN DESIGN.

Package	Leadframe Number
RQ16	442102

UNLESS OTHERWISE SPECIFIED DIMENSIONSARE IN INCHES TOLERANCES DECIMALS FRACTIONS ANGLES .XX + .010 + 1/32 + -2	ASSEM	1BLY		ANALOG DEVICES	804 WOBURN ST WILMINGTON, MA 01	
.XXX +005	APPROVAL	DATE	TITLE			
Material	DRAWN BY					
	M S CANTRELL	7-25-2019	V DIT	/1//Y INIT	RINSIC SAFE	TV
	DESIGNED		ADun		MINDIC DALL	_ ! !
	M S CANTRELL	7-25-2019	\sim	ICTULICTIO		
	CHECKED			12 I KUCI IU	N DRAWIN	J
	K MCLAUGHLIN	7-25-2019				
Finish			SIZE	FSCM NO	DRAWING NUMBER	REV
			В		CF0002	0
DO NOT SCALE DWG			SCALE N/A		SHEET 1	OF 5

	REVISIONS		
REV	DESCRIPTION	DATE	APPROVED
0	INITIAL RELAESE		

	Number of Inputs,	Number of Inputs,	Default Output	Package	Package
Model [,]	V _{DD1} Side	V _{DD2} Side	State	Description	Option
ADuM1440ARQZ	4	0	High	16-Lead QSOP	RQ-16
ADuM1441ARQZ	3	1	High	16-Lead QSOP	RQ-16
ADuM1442ARQZ	2	2	High	16-Lead QSOP	RQ-16
ADuM1445ARQZ	4	0	Low	16-Lead QSOP	RQ-16
ADuM1446ARQZ	3	1	Low	16-Lead QSOP	RQ-16
ADuM1447ARQZ	2	2	Low	16-Lead QSOP	RQ-16
ADuM1440ARSZ	4	0	High	20-Lead SSOP	RS-20
ADuM1441ARSZ	3	1	High	20-Lead SSOP	RS-20
ADuM1442ARSZ	2	2	High	20-Lead SSOP	RS-20
ADuM1445ARSZ	4	0	Low	20-Lead SSOP	RS-20
ADuM1446ARSZ	3	1	Low	20-Lead SSOP	RS-20
ADuM1447ARSZ	2	2	Low	20-Lead SSOP	RS-20
AD71217ARSZ	3	1	High	20-Lead SSOP	RS-20

UNLESS OTHERWISE SPECIFIED DIMENSIONSARE IN INCHES TOLERANCES DECIMALS FRACTIONS ANGLES .XX + .010 + 1/32 + 2	ASSEN	1BLY		ANALOG DEVICES	804 WOBURN ST WILMINGTON, MA 01	
.XXX +005	APPROVAL	DATE	TITLE			
Material	DRAWN BY					
	M S CANTRELL	7-25-2019	V Dill	/1//Y INIT	RINSIC SAFE	TV
	DESIGNED		ADun	NITHHY IIVI	MINDIC DALL	-
	M S CANTRELL	7-25-2019	\sim	ICTULICTIO		
	CHECKED		CON	12 I KUCIIU	N DRAWING	J
	K MCLAUGHLIN	7-25-2019				
Finish			SIZE	FSCM NO	DRAWING NUMBER	REV
			В		CF0002	0
DO NOT SCALE DWG			SCALE N/A		SHEET 1	OF 5

The following Page(s) are related to Illustration-2. The next supplement, if applicable, will be identified with a new Supplement Page Heading.



Marking File

ADuM1440/ADuM1441/ADuM1442/ADuM1445/ ADuM1446/ADuM1447/AD71217

GENERAL DESCRIPTION

This document contains the marking summary of ADuM1440/ADuM1441/ADuM1442/ADuM1445/ADuM1446/ADuM1447 micropower digital isolators in QSOP and SSOP package sizes.

Manufacturer's name:	Analog Devices Inc	
	804 Woburn St,	
	WILMINGTON MA, 01887	
Manufacturer's address:	USA	
	Gateway Business Park	
	Javalera	
	Gen. Trias Cavite	
	Philippines	
Manufacturing Location:	4107	

Certificate number:	E516634
	US: Class I Zone 0 AEx ia IIC Ga CA: Ex ia IIC Ga
Certification code:	US/CA: Class I, Div 1 Groups A, B, C, and D
Ambient range:	-40°C to +85°C
Extended ambient range for	
AD71217	-55°C to +85°C

Package type	Entity parameters Side 1	Entity parameters Side 2
	Ui=42V	Ui=42V
	Ii=275mA	Ii=275mA
QSOP-16	Pi=1.3W	Pi=1.3W
	Li=0	Li=0
	Ci=4pF	Ci=4pF
	Ui=42V	Ui=42V
	Ii=275mA	Ii=275mA
SSOP-20	Pi=1.3W	Pi=1.3W
	Li=0	Li=0
	Ci=4pF	Ci=4pF

Table 1COMPONENT ENTITY PARAMETERS

Marking File

TABLE OF CONTENTS

General Description	QSOP Package Marking3
Revision History	SSOP Package Marking6
Package marking3	

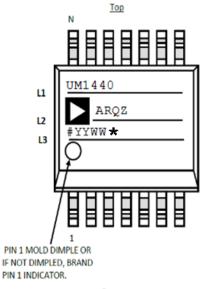
REVISION HISTORY

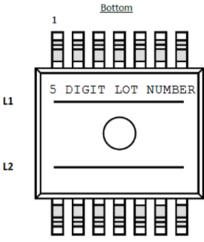
9/20—Revision A: Initial Version

PACKAGE MARKING

QSOP PACKAGE MARKING

Marking Diagram





Note: No bottom capability for EPAD packages

Figure 1 QSOP Package Marking Locations¹

Marking Key

yy last two digits of year of manufacture

ww week of manufacture 1-52

ddddd 5 digit ADI lot code

indicates lead free product

* indicates VDE certification when present

ADuM1440ARQZ

Тор	Content	Characters Used	Characters Available
Line 1	UM1440	6	6
Line 2	ARQZ	4	5
Line 3	#yyww*	6	6
Bottom	Content	Characters Used	Characters Available
Line 1	ddddd	5	5
Line 2		0	5

Table 2 Marking Values for ADuM1440ARQZ in QSOP package

ADuM1441ARQZ

Тор	Content	Characters Used	Characters Available
Line 1	UM1441	6	6
Line 2	ARQZ	4	5
Line 3	#yyww*	6	6
Bottom	Content	Characters Used	Characters Available
Line 1	ddddd	5	5
Line 2		0	5

Table 3 Marking Values for ADuM1441ARQZ in QSOP package

ADuM1442ARQZ

Тор	Content	Characters Used	Characters Available
Line 1	UM1442	6	6
Line 2	ARQZ	4	5
Line 3	#yyww*	6	6
Bottom	Content	Characters Used	Characters Available
Line 1	ddddd	5	5
Line 2		0	5

Table 4 Marking Values for ADuM1442ARQZ in QSOP package

ADuM1445ARQZ

Тор	Content	Characters Used	Characters Available
Line 1	UM1445	6	6
Line 2	ARQZ	4	5
Line 3	#yyww*	6	6
Bottom	Content	Characters Used	Characters Available
Line 1	ddddd	5	5
Line 2		0	5

Table 5 Marking Values for ADuM1445ARQZ in QSOP package

ADuM1446ARQZ

Тор	Content	Characters Used	Characters Available
Line 1	UM1446	6	6
Line 2	ARQZ	4	5
Line 3	#yyww*	6	6
Bottom	Content	Characters Used	Characters Available
Line 1	ddddd	5	5
Line 2		0	5

Table 6 Marking Values for ADuM1446ARQZ in QSOP package

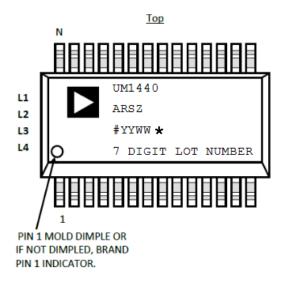
ADuM1447ARQZ

Тор	Content	Characters Used	Characters Available
Line 1	UM1447	6	6
Line 2	ARQZ	4	5
Line 3	#yyww*	6	6
Bottom	Content	Characters Used	Characters Available
Line 1	ddddd	5	5
Line 2		0	5

Table 7 Marking Values for ADuM1447ARQZ in QSOP package

SSOP PACKAGE MARKING

Marking Diagram



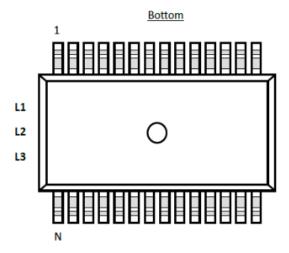


Figure 2 Marking locations for the SSOP package¹

Marking Key

yy last two digits of year of manufacture

ww week of manufacture 1-52

ddddd 7 digit ADI lot code

indicates lead free product

indicates VDE certification when present

ADuM1440ARSZ

Тор	Content	Characters Used	Characters Available
Line 1	UM1440	6	6
Line 2	ARSZ	4	6
Line 3	#yyww*	6	6
Line 4	ddddddd	7	7
Bottom	Content	Characters Used	Characters Available
Line 1		0	6
Line 2		0	3
Line 3		0	3

Table 8 Marking Values for ADuM1440ARSZ in SSOP package

ADuM1441ARSZ

Тор	Content	Characters Used	Characters Available
Line 1	UM1441	6	6
Line 2	ARSZ	4	6
Line 3	#yyww*	6	6
Line 4	ddddddd	7	7
Bottom	Content	Characters Used	Characters Available
Line 1		0	6
Line 2		0	3
Line 3		0	3

Table 9 Marking Values for ADuM1441ARSZ in SSOP package

ADuM1442ARSZ

Тор	Content	Characters Used	Characters Available
Line 1	UM1442	6	6
Line 2	ARSZ	4	6
Line 3	#yyww*	6	6
Line 4	ddddddd	7	7
Bottom	Content	Characters Used	Characters Available
Line 1		0	6
Line 2		0	3
Line 3		0	3

Table 10 Marking Values for ADuM1442ARSZ in SSOP package

ADuM1445ARSZ

Тор	Content	Characters Used	naracters Used Characters Available	
Line 1	UM1445	6	6	
Line 2	ARSZ	4 6		
Line 3	#yyww*	6 6		
Line 4	ddddddd	7	7	
Bottom	Content	Characters Used	Characters Available	
Line 1		0	6	
Line 2		0	3	
Line 3		0	3	

Table 11 Marking Values for ADuM1445ARSZ in SSOP package

ADuM1446ARSZ

Тор	Content	Characters Used	d Characters Available	
Line 1	UM1446	6	6	
Line 2	ARSZ	4	6	
Line 3	#yyww*	6	6	
Line 4	ddddddd	7	7	
Bottom	Content	Characters Used	Characters Available	
Line 1		0	6	
Line 2		0	3	
Line 3		0	3	

Table 12 Marking Values for ADuM1446ARSZ in SSOP package

ADuM1447ARSZ

Тор	Content	Characters Used	Characters Available	
Line 1	UM1447	6	6	
Line 2	ARSZ	4	6	
Line 3	#yyww*	6	6	
Line 4	ddddddd	7	7	
Bottom	Content	Characters Used	Characters Available	
Line 1		0	6	
Line 2		0	3	
Line 3		0	3	

 $Table\,13\,Marking\,Values\,for\,ADuM1447ARSZ\,in\,SSOP\,package$

AD71217ARSZ

Тор	Content	Characters Used	acters Used Characters Available	
Line 1	71217	6	6	
Line 2	ARSZ	4	6	
Line 3	#yyww*	6	6	
Line 4	ddddddd	7	7	
Bottom	Content	Characters Used	Characters Available	
Line 1		0	6	
Line 2		0	3	
Line 3		0	3	

Table 14 Marking Values for ADuM1441ARSZ in SSOP package

 $^{^{\}rm 1}\,{\rm Pins}$ are shown for illustration purposes only and pin count may differ from the illustration