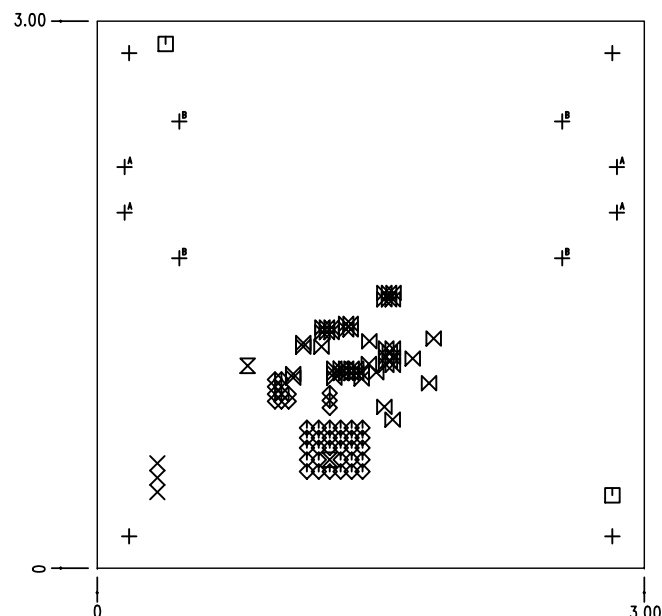


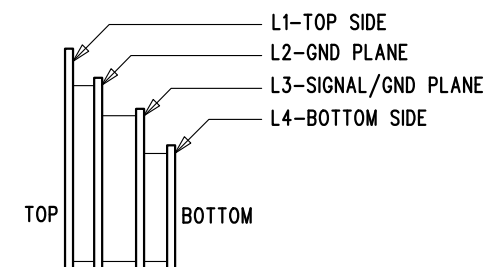
REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	1	PRODUCTION	DAVID B.	02-02-15

TOP SIDE SHOWN



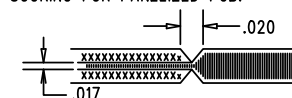
SIZE	QTY	SYM	PLATED	TOL
0.008	50	⊗	YES	+/-0.003
0.012	42	◇	YES	+/-0.003
0.035	3	⊗	YES	+/-0.003
0.04	2	⊗	YES	+/-0.003
0.07	2	□	NO	+/-0.003
0.094	4	+ ^A	YES	+/-0.003
0.187	4	+ ^B	NO	+/-0.005
0.207	4	+ ^B	YES	+/-0.005

LAYER STRUCTURE



NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062+/- .005
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
O/O ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001 THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003 IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB:



UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: 0.XX = ± 0.01 0.XXX = ± 0.005 INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION 	APPROVALS		1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY	
	PCB DES.	LT		
	APP ENG.	DAVID B.	TITLE: FABRICATION DRAWING	
			HI VOLTAGE SURGE SUPPRESSOR WITH TIMER	
			SIZE	IC NO.
			N/A	LTC7860EMSE
				DEMO CIRCUIT 2239A
				REV 1
	SCALE = NONE		FILENAME: DC2239A-1.PCB	SHT 1 OF 1