

ADSP-SC584 EZ Board

Revision 1.1

Reference Design Information

Introduction:

This reference design package contains all electronic data for the design, layout, fabrication and assembly of this EZ-Board.

This information is provided for reference only. Analog Devices is not responsible for any errors that may be contained in the information. There is no support provided for any of this information and no additional information will be provided.

This information is only provided in the formats described below and not available in any other formats.

Package Contents:

Schematics

- The schematic revision is 1.1A. The “A” denotes that an ECO has been applied to the board that could affect component value changes made to the BOM and/or minor rework(s) done to the board. All of the ECO modification are updated in schematic revision 1.1A.
- This project was created with Mentor Graphics DxDesigner, version VX2.1

Layout

- PCB layout file created from PADS Layout, version 9.5
- PDF file containing all the board layers, fabrication and assembly drawing

Fabrication

- Contains all the Gerber and N/C drill information needed to fabricate the board

Assembly

- Contains all the standard files needed to assemble the board